


# PROJECT COVER

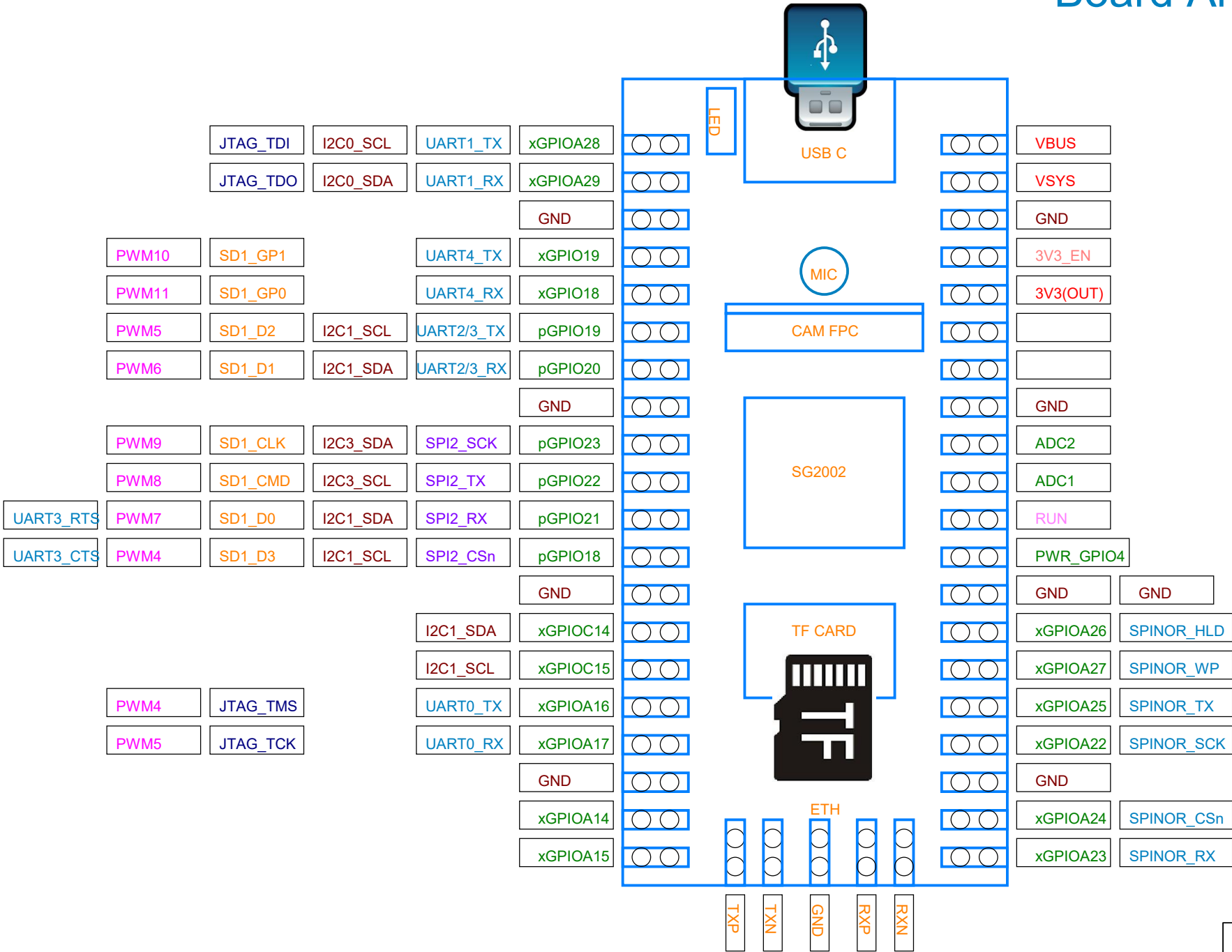
Board Name	DUO_256M
Board Version	V1.0
Designer	
Reviewed by	
CIS Library	YES

			
Project:	SG2002		
File:	01 Project Cover		
Date:	Tuesday, January 02, 2024	Rev:	1.0
Designed by:	<designer>	Sheet:	1 of 9

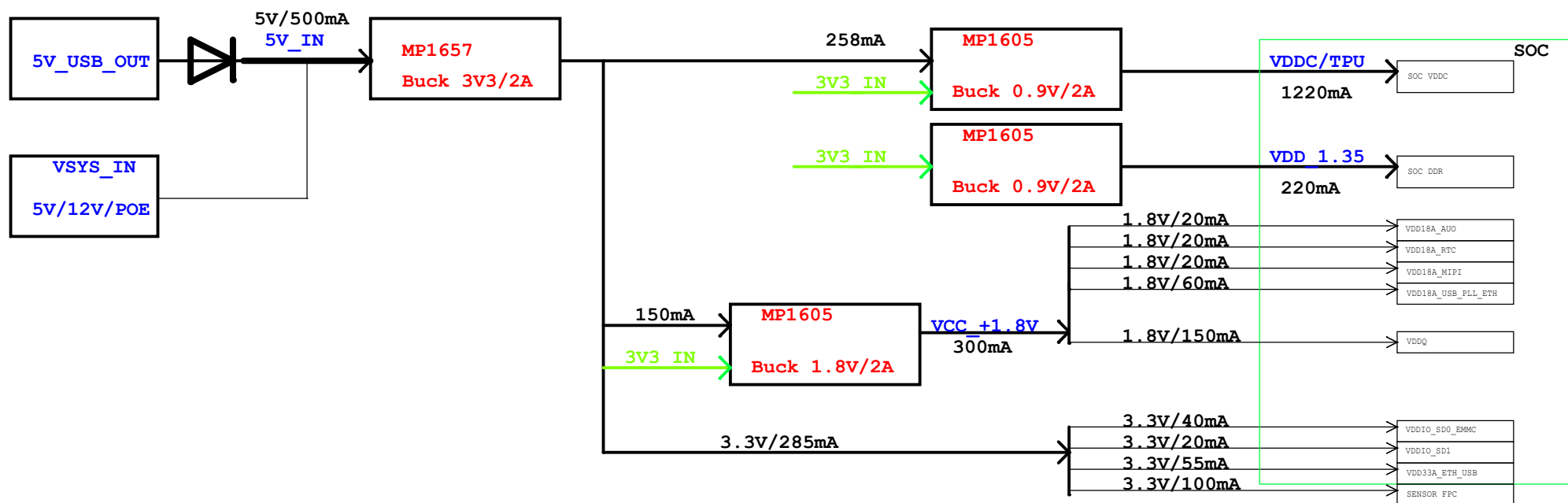
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# Board Architecture

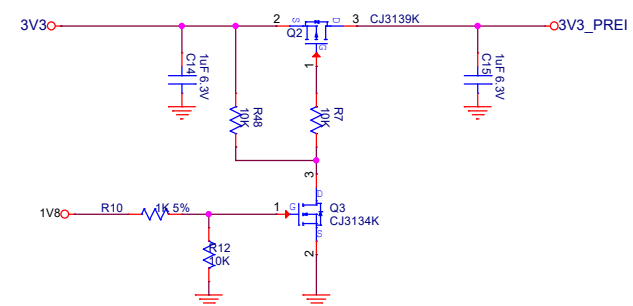
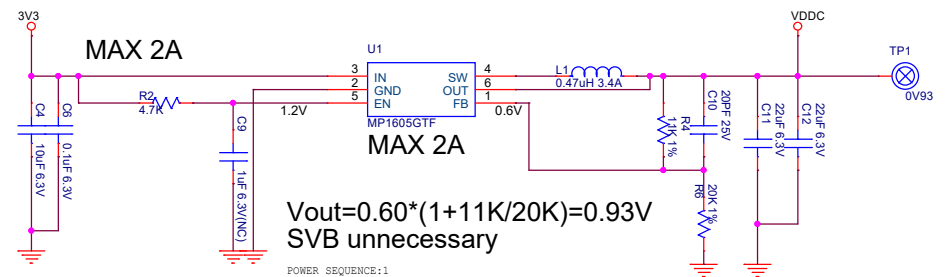



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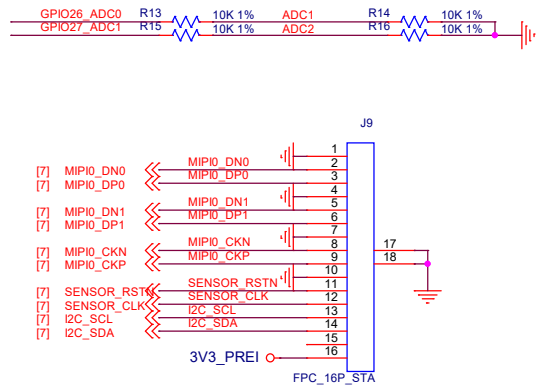
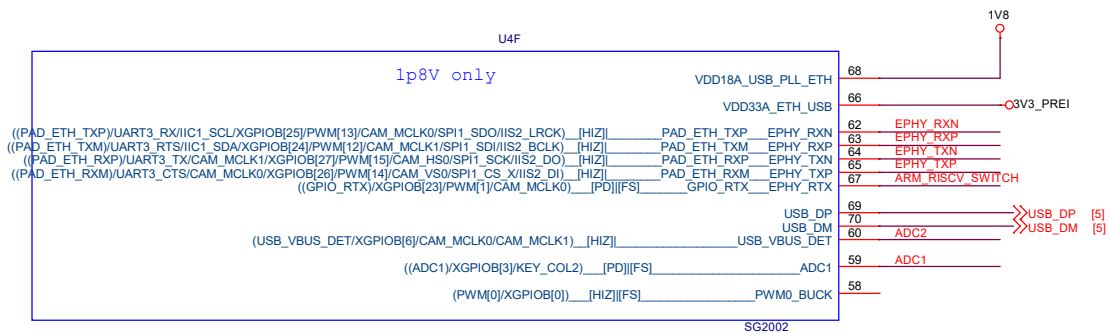


Project:	SG2002		
File:	04 Power Tree		
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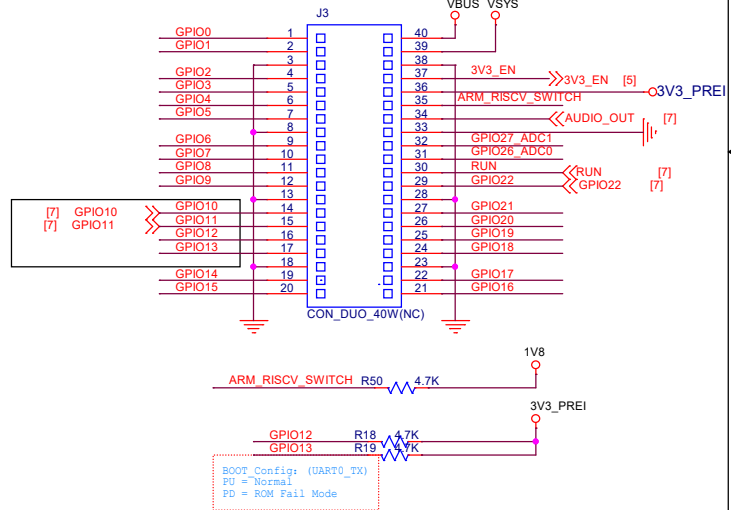
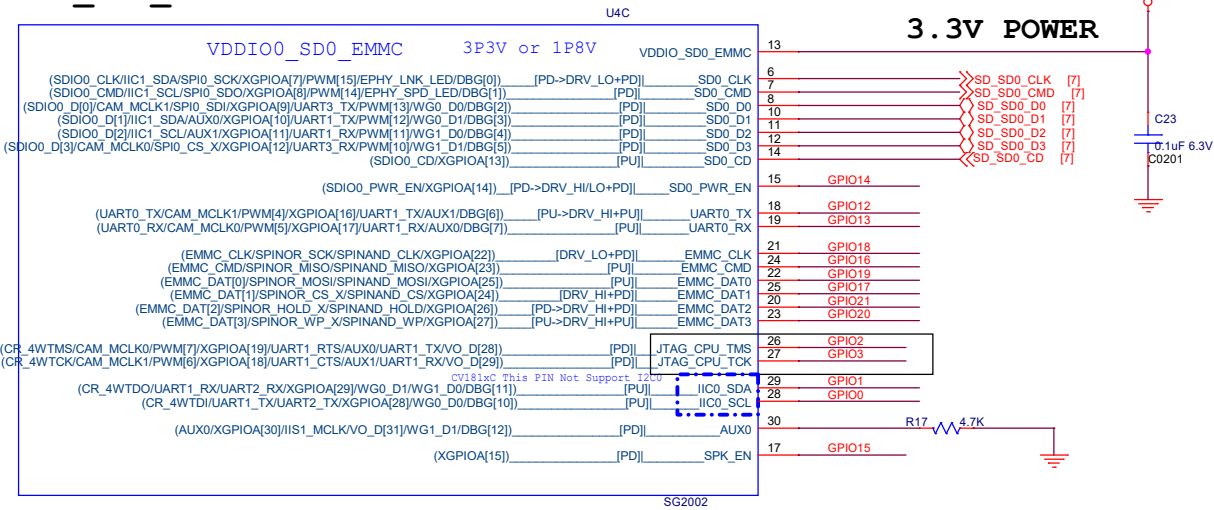
ilkv



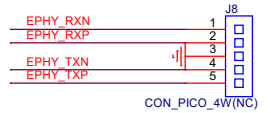
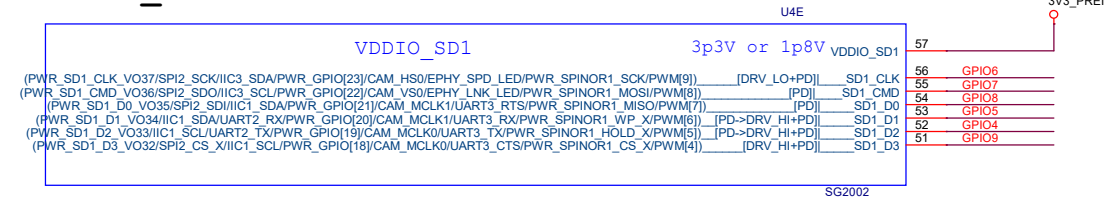
			
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File:	05 Power		
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VDDIO\_SD0\_EMMC



VDDIO\_SD1



**milkV**

Project: SG2002

File: 06 SG2002 PART1

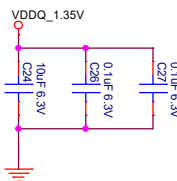
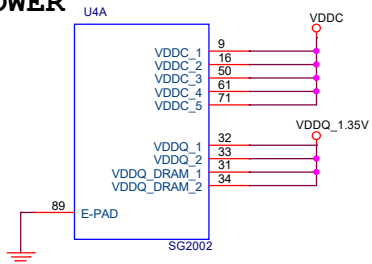
Date: Tuesday, January 02, 2024

Designed by: <designer>

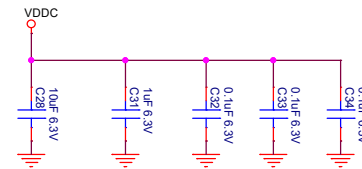
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## DDR2 POWER

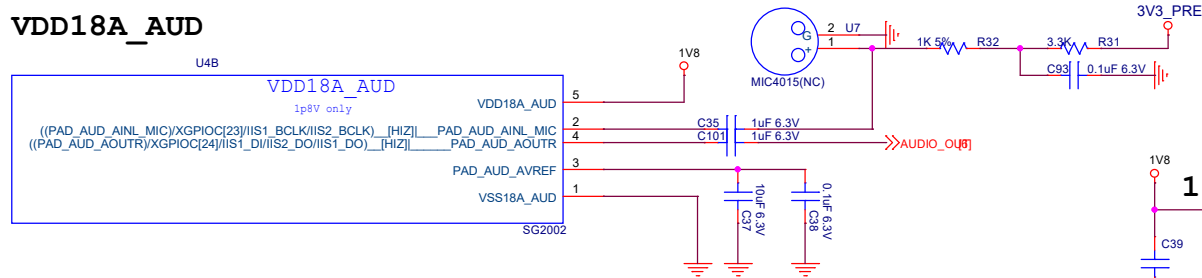


## VDDC&TPU POWER

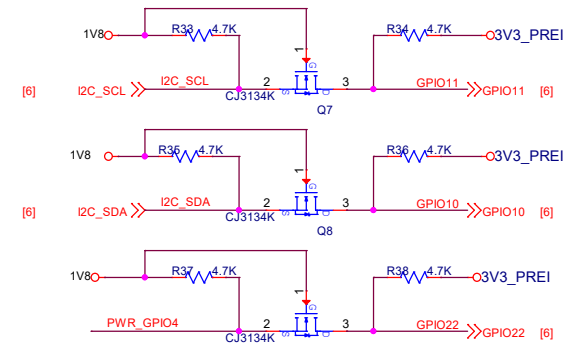
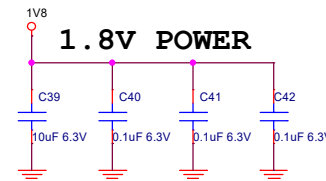


## SG2002 II

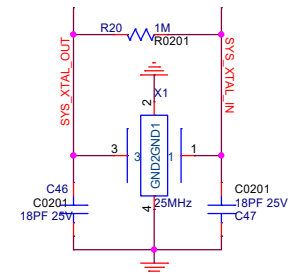
## VDD18A AUD



## 1.8V POWER

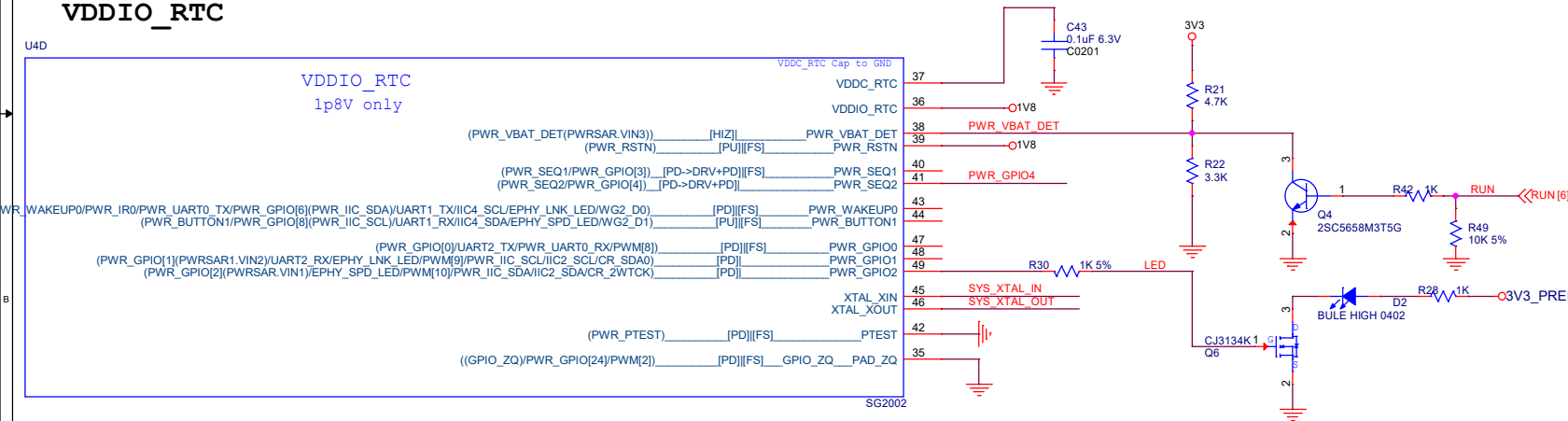


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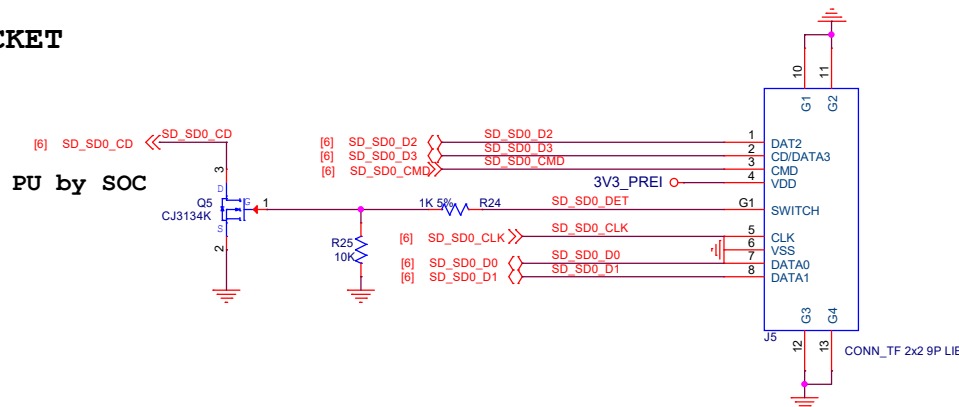


## SYS XTAL

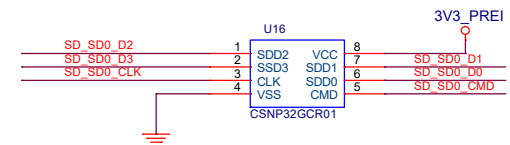
VDDIO\_RTC



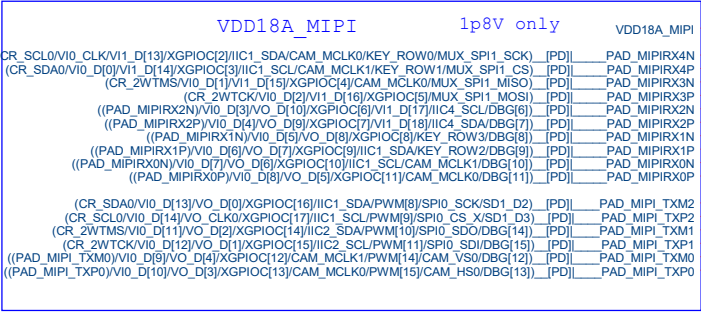
## TF\_SOCKET



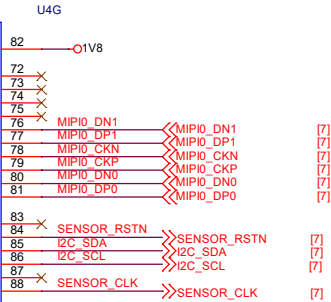
## SD NAND



<b>Project:</b>	SG2002		
<b>File:</b>	07 SG2002 PART2		
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SG2002



REV.	CHANGE LIST	EDITOR	DATE
V 1.0			2023/12/18
V 1.1			
V 1.2			