

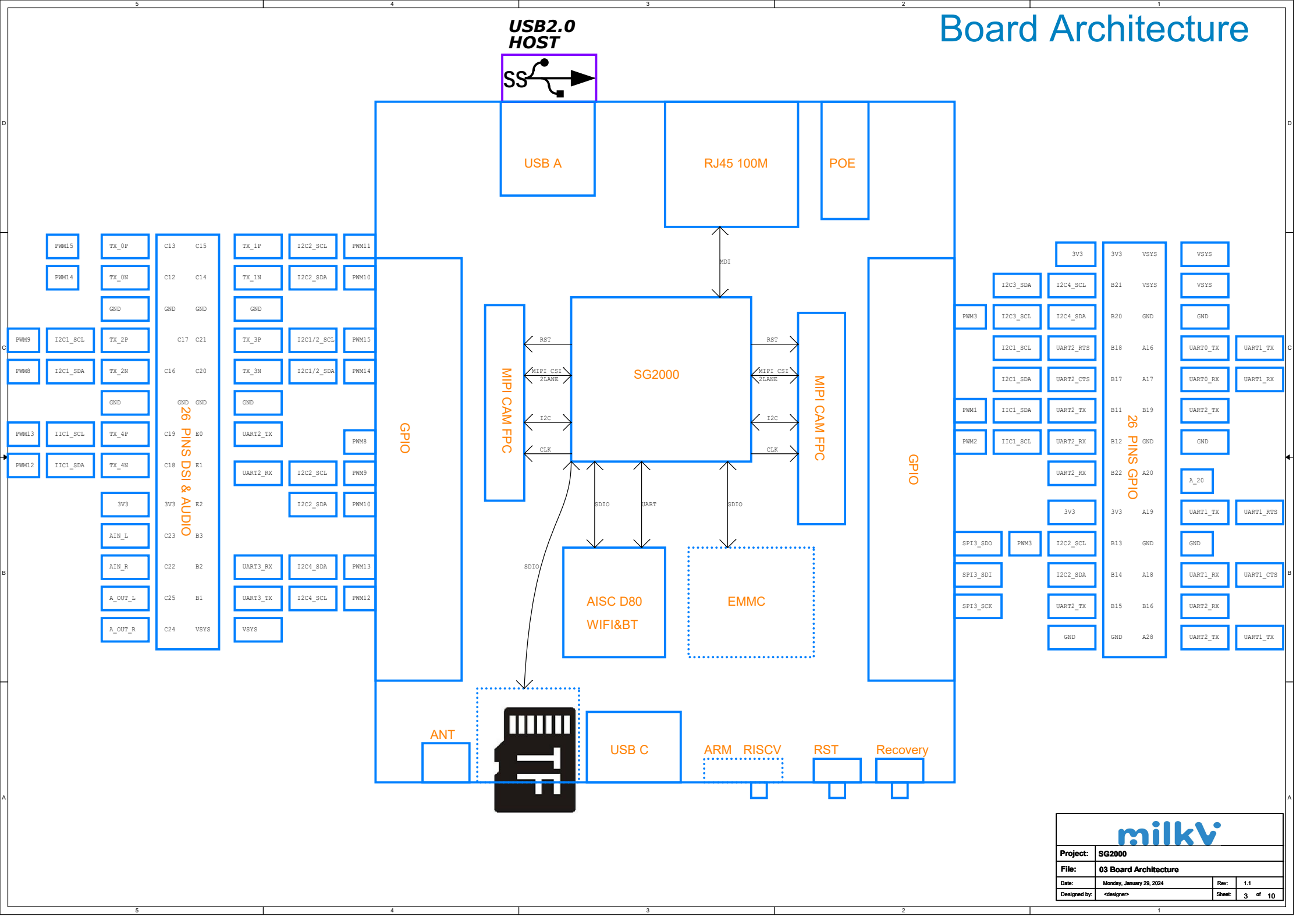
PROJECT COVER

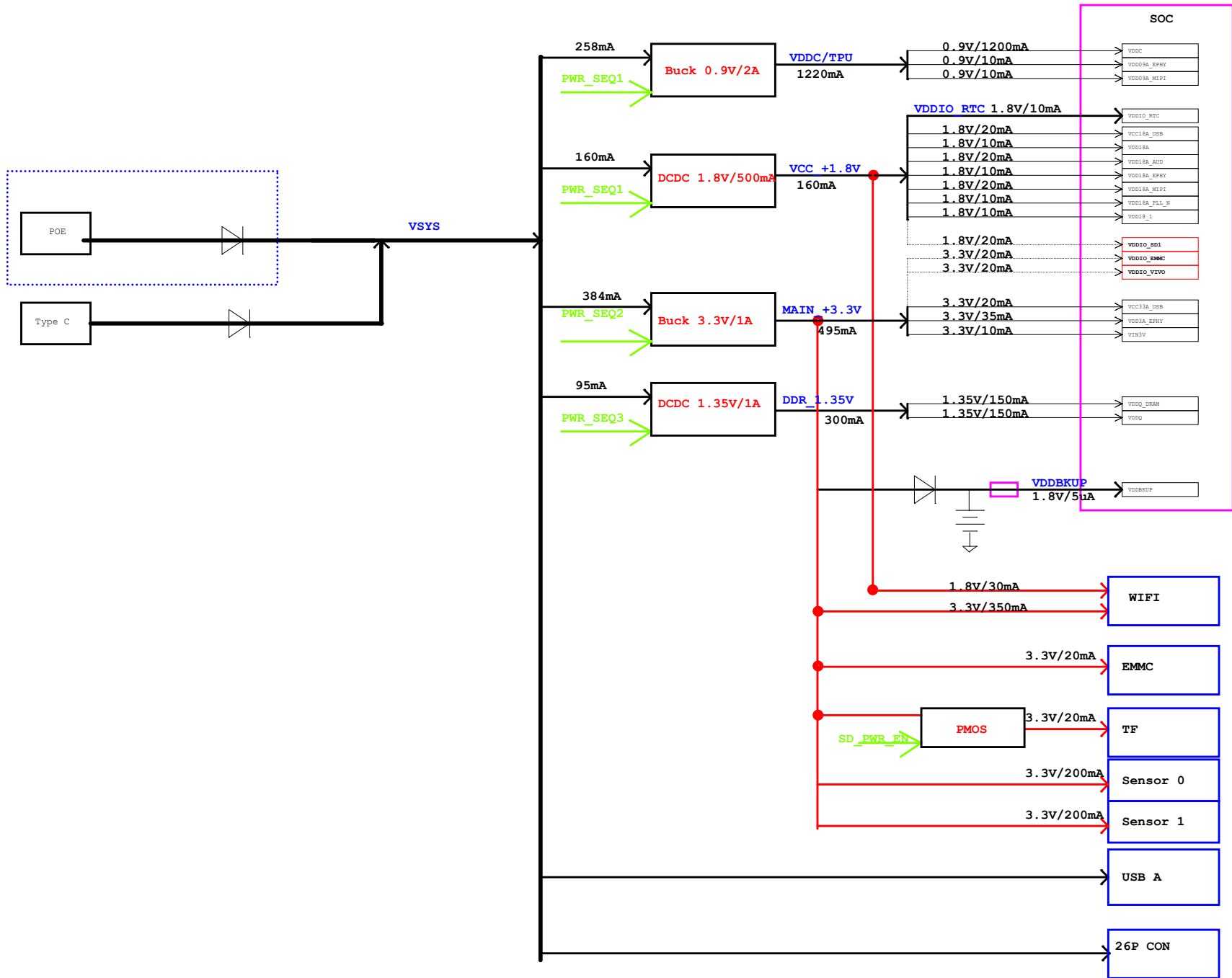
Board Name	DUO S
Board Version	V1.1
Designer	
Reviewed by	

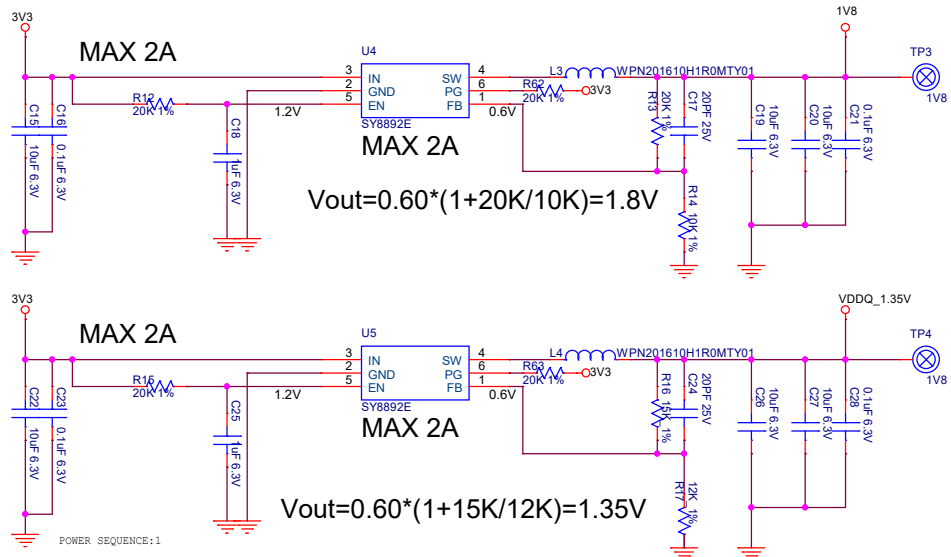
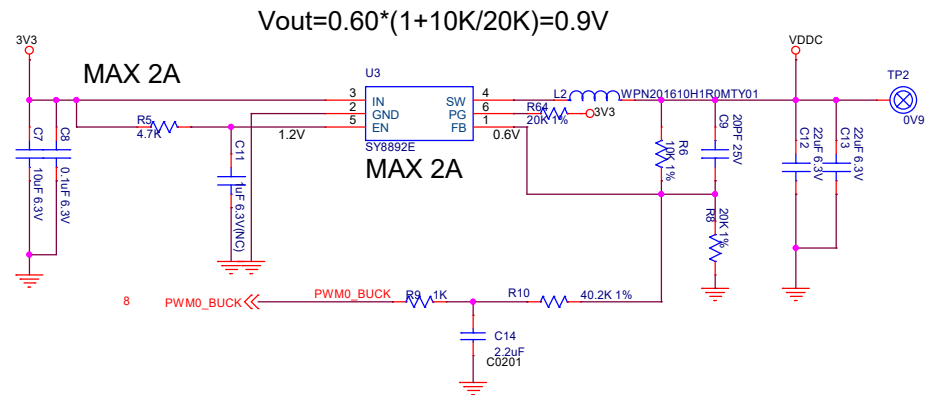
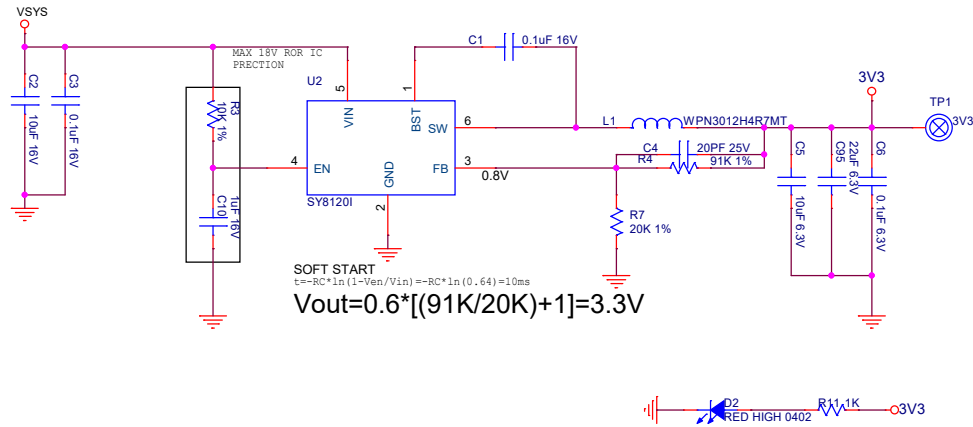
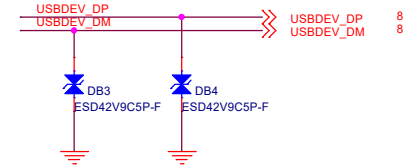
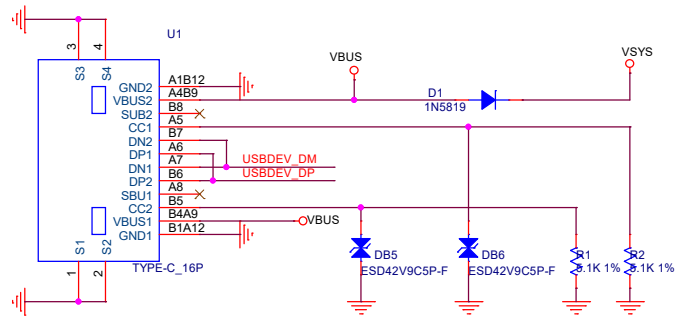
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Board Architecture

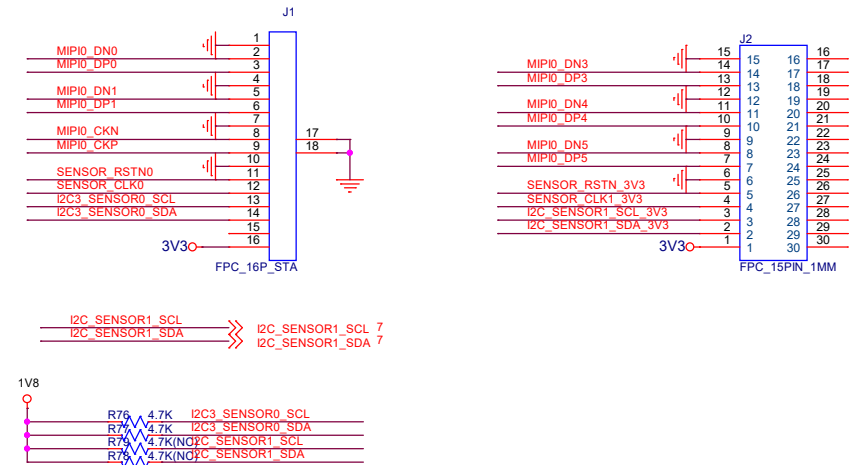
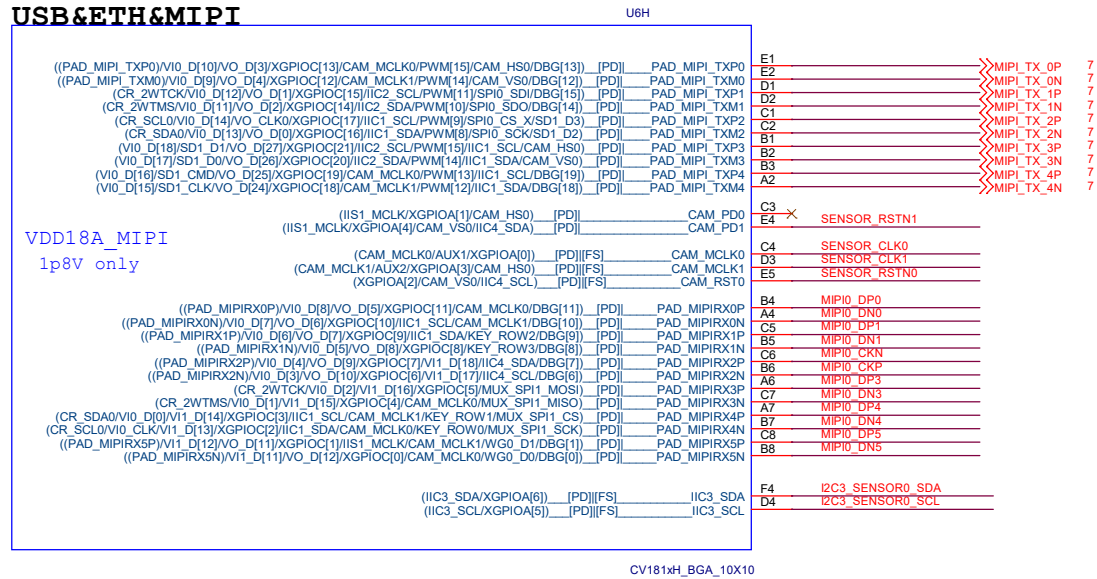




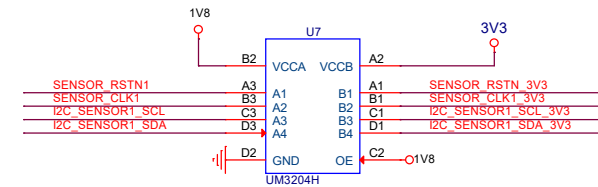
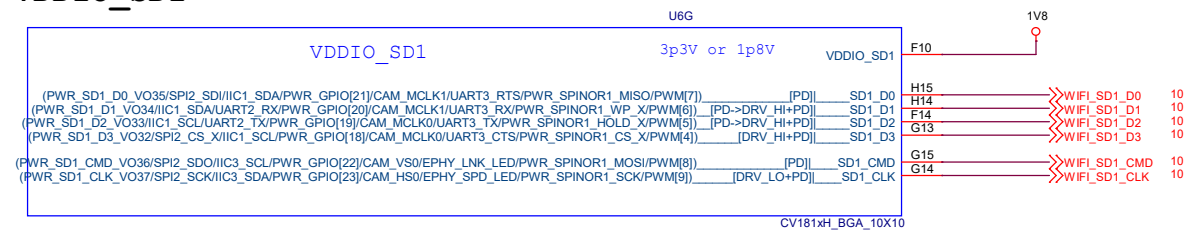


SG2000 PART I

USBÐ&MIPI

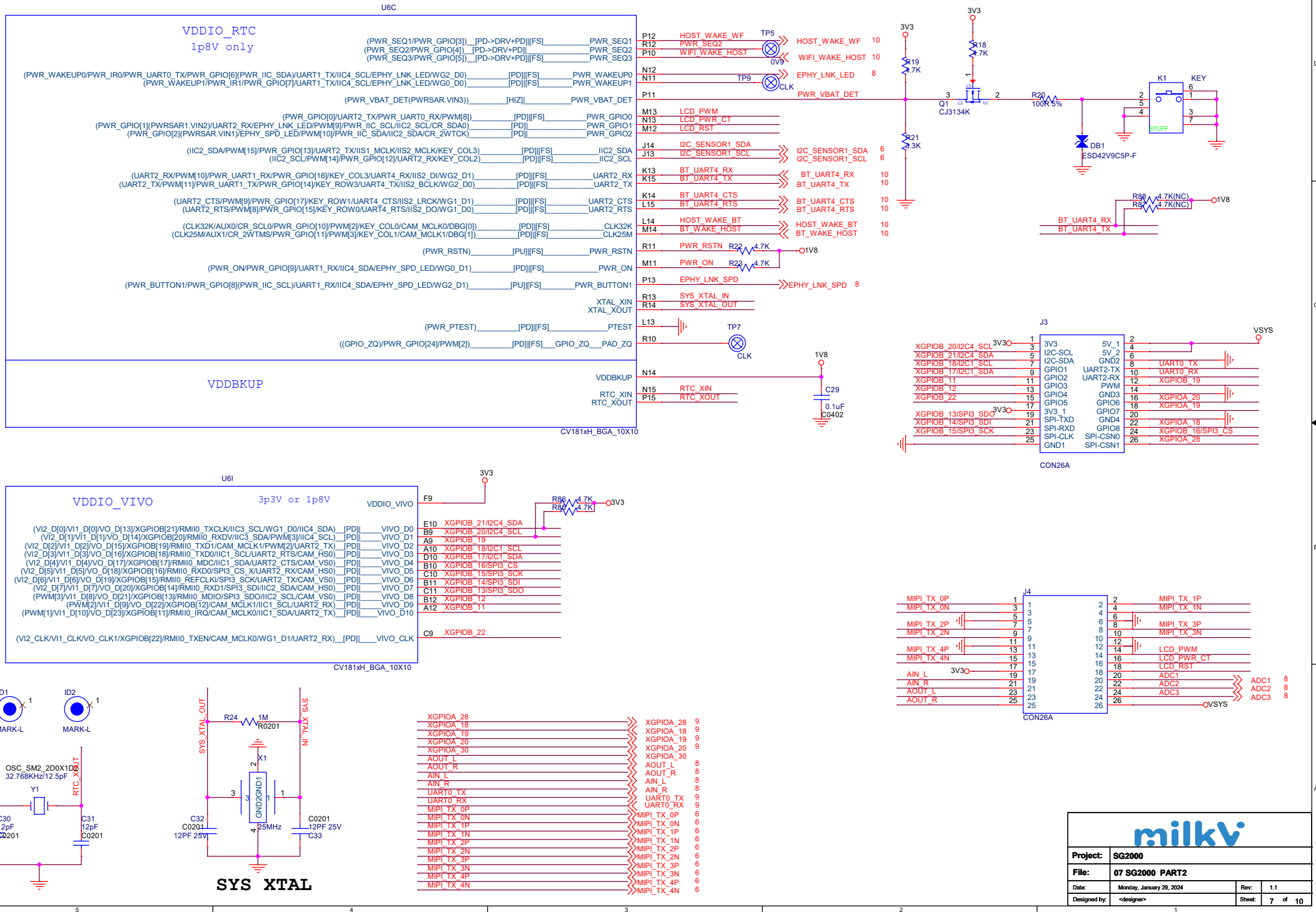


VDDIO_SD1

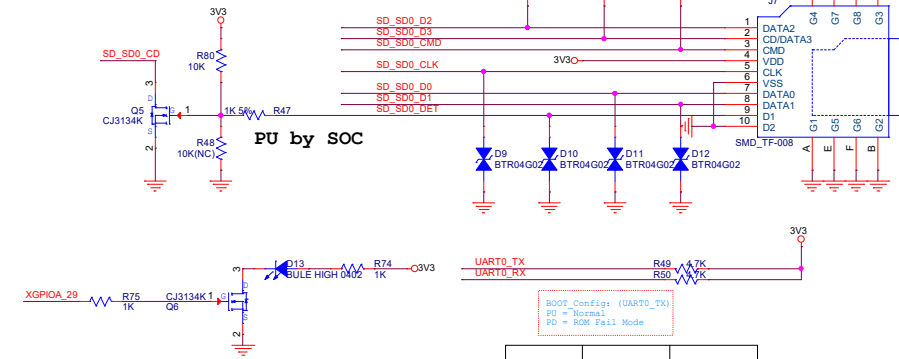


Project:	SG2000		
File:	06 SG2000 PART1		
Date:	Monday, January 29, 2024	Rev:	1.1
Designed by:	<designer>	Sheet:	6 of 10

SG2000 PART II



TF_SOCKET



eMMC_D0	eMMC_D3	boot device selection
1	1	eMMC
1	0	Serial NOR
0	0	Serial NAND



U11-2

