

Milk-V Mars CM(eMMC) schematic

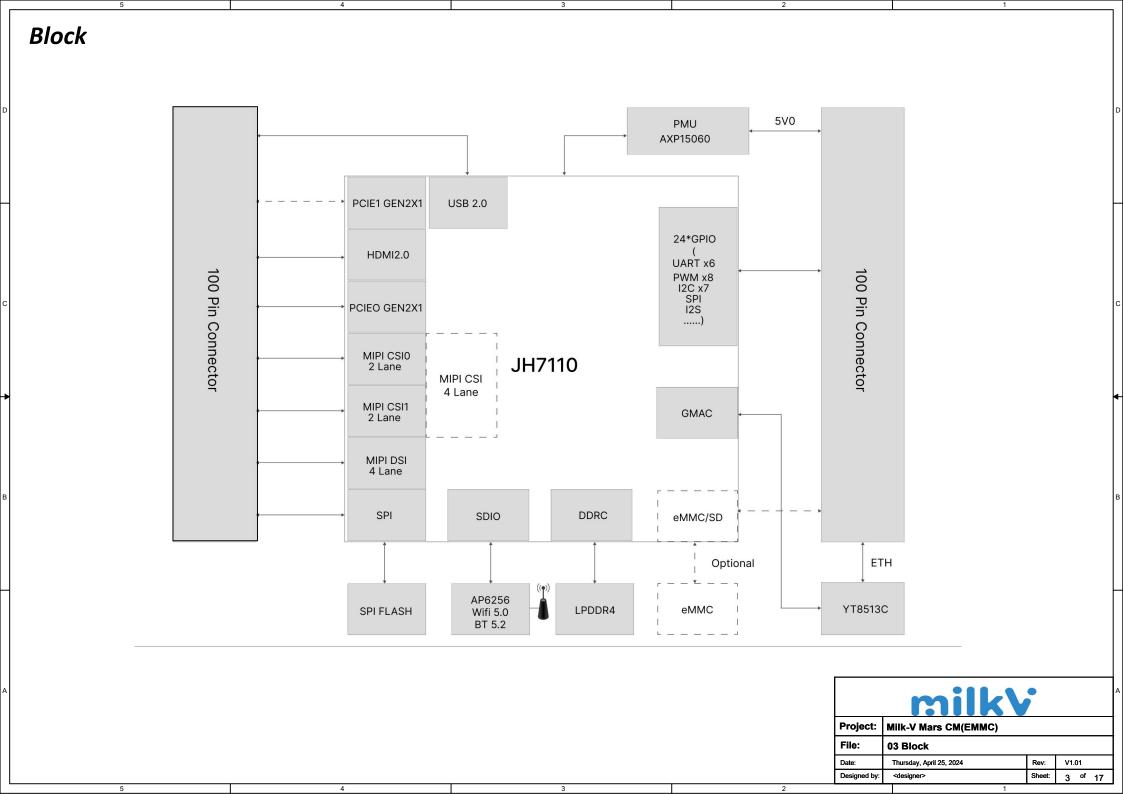
V1.01

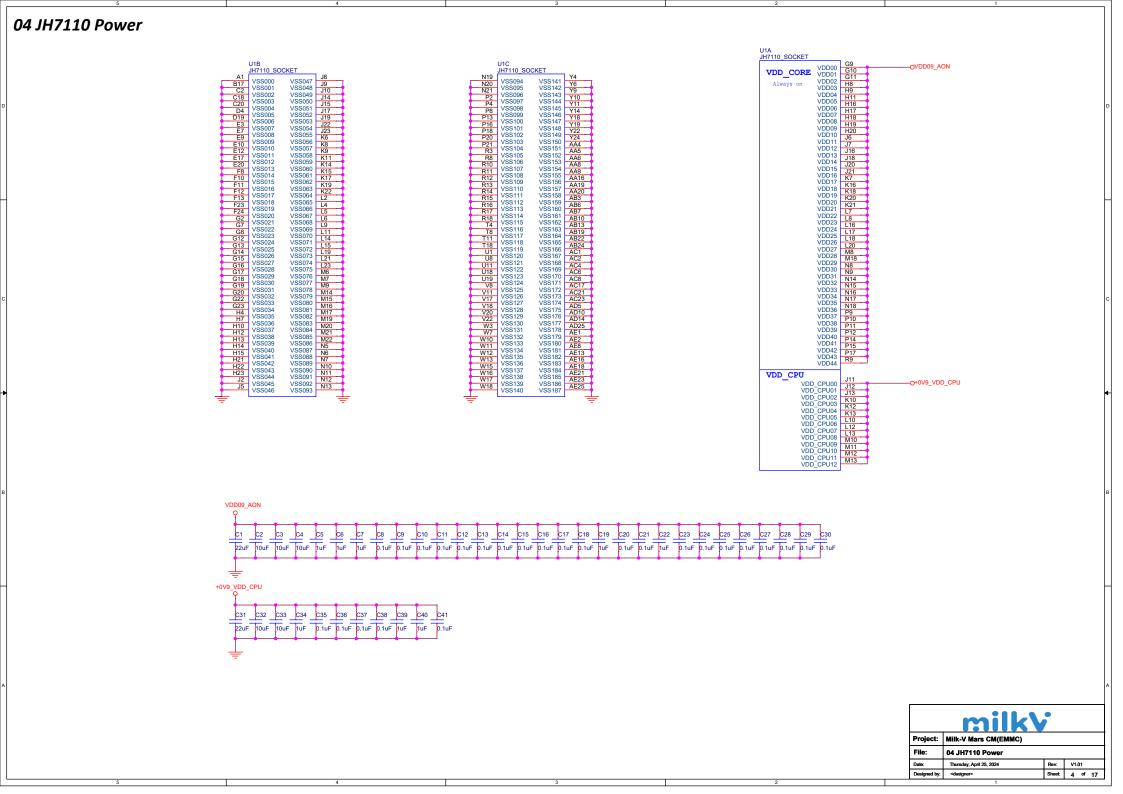
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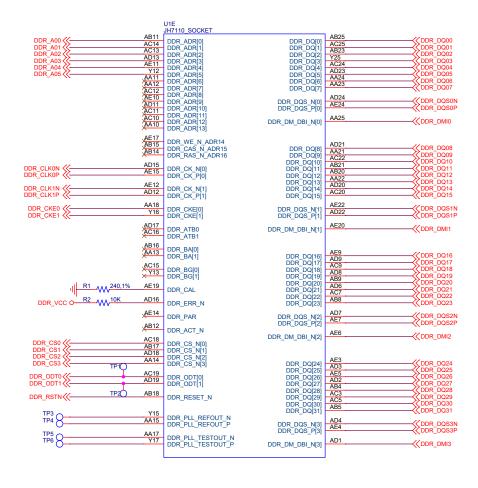
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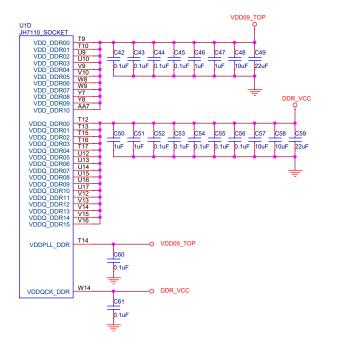
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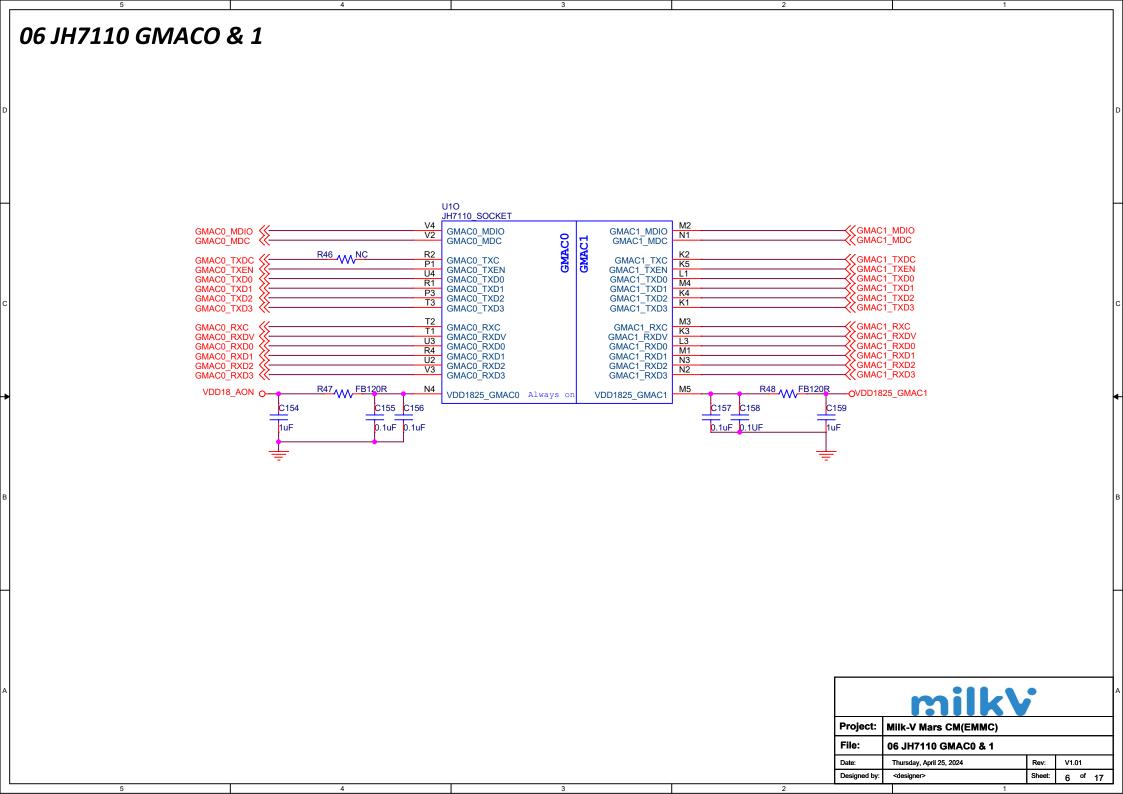


05 JH7110 DDR Ctrl

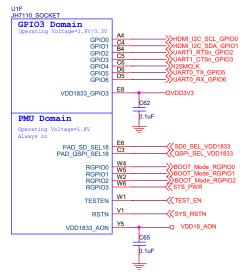


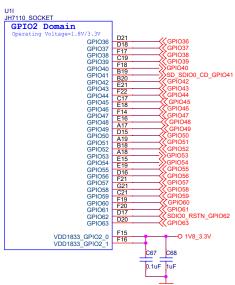


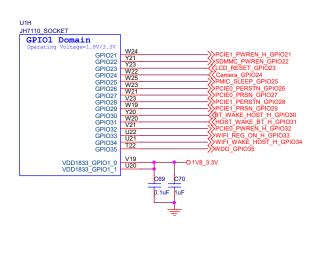
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File: 05 JH7110 DDR Ctrl			
Date:	Thursday, April 25, 2024	Rev:	V1.01
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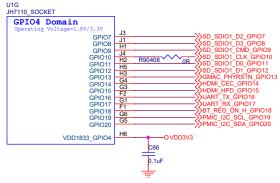


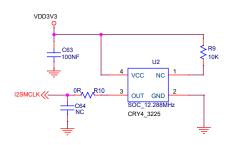
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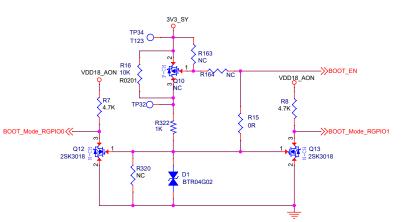


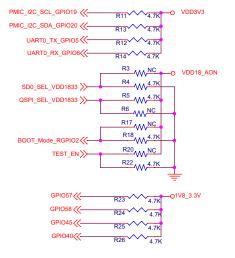




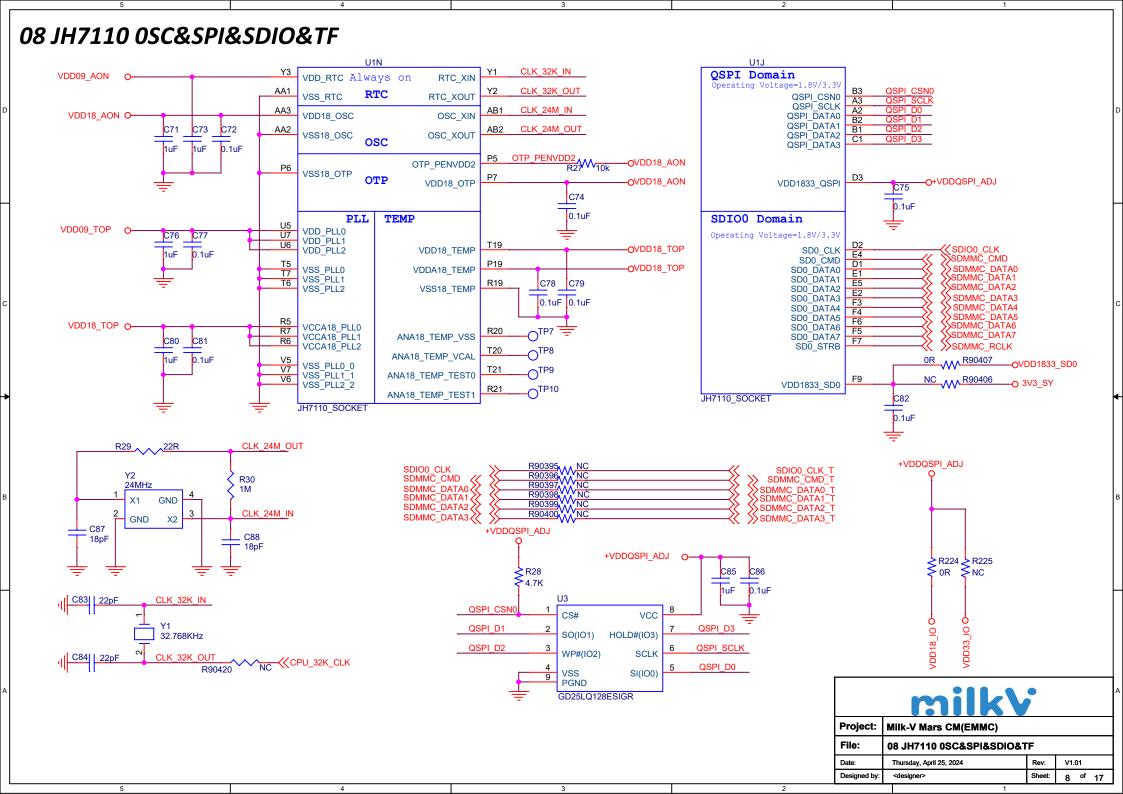


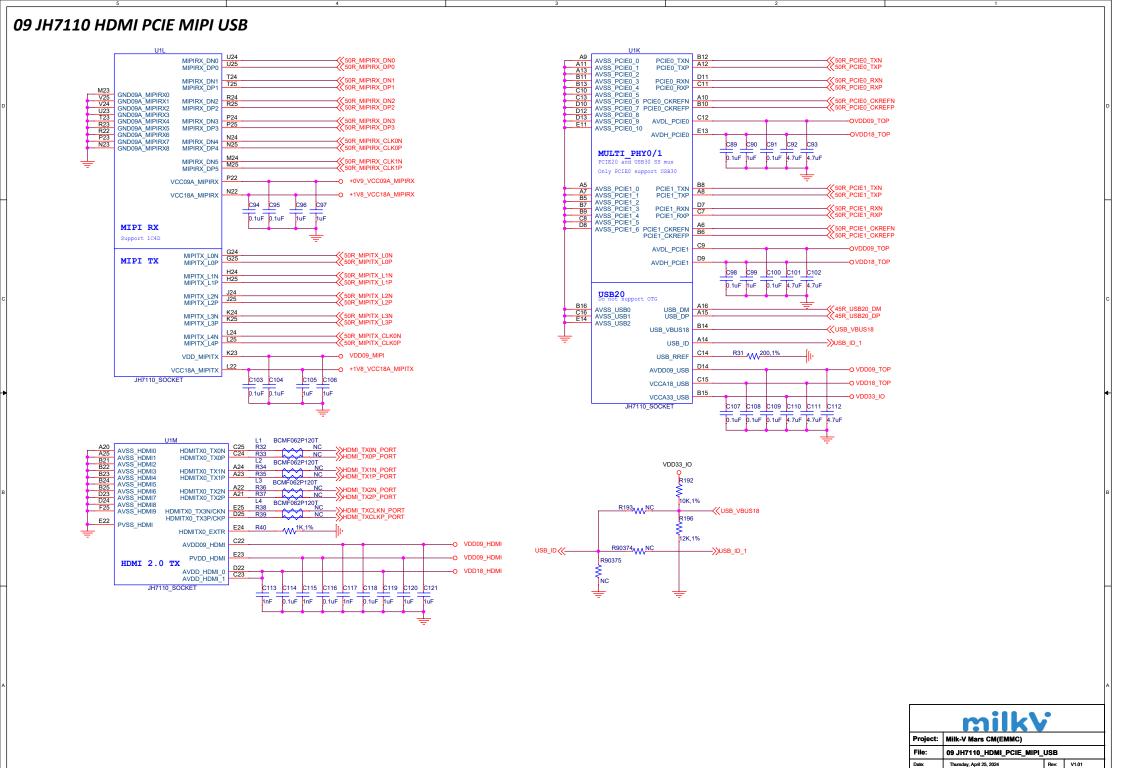






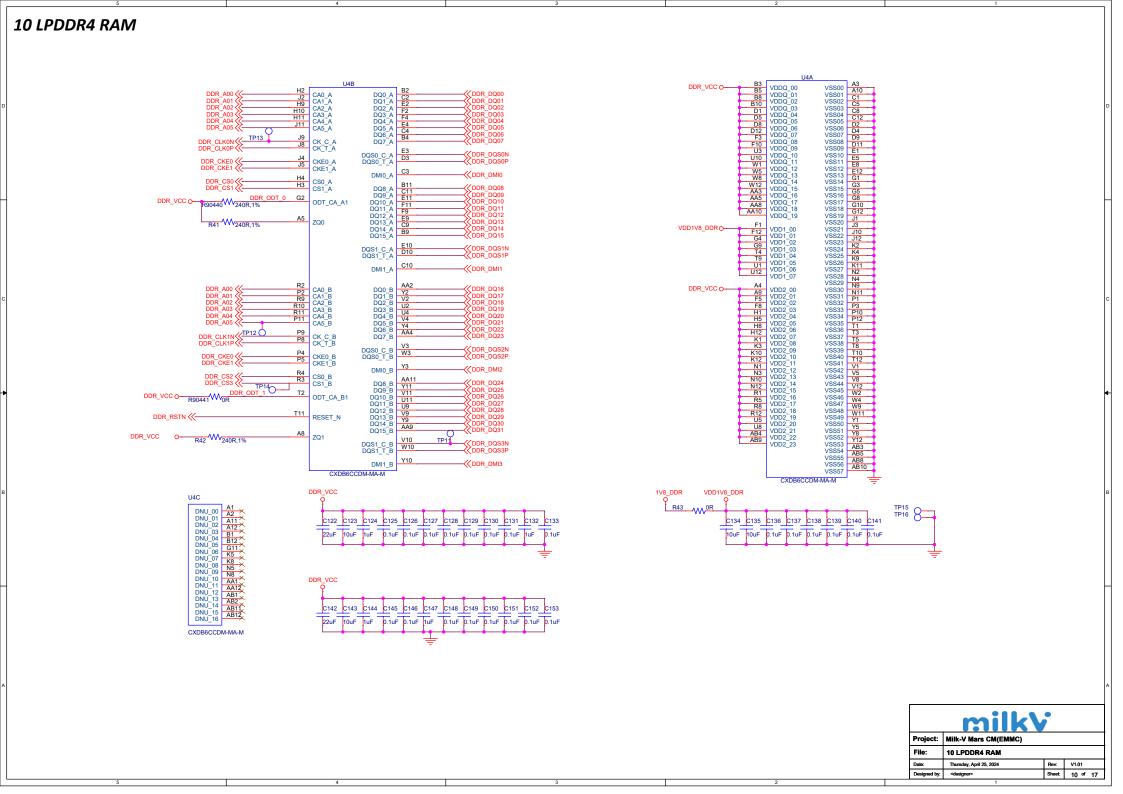


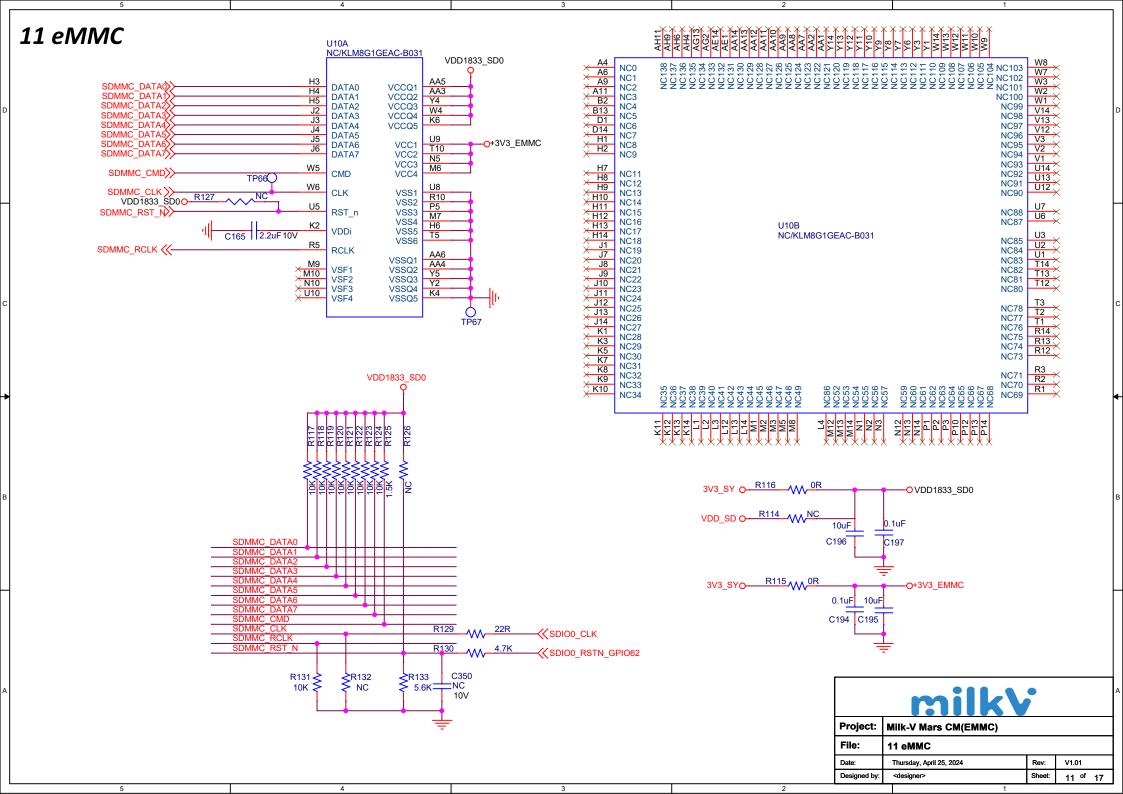


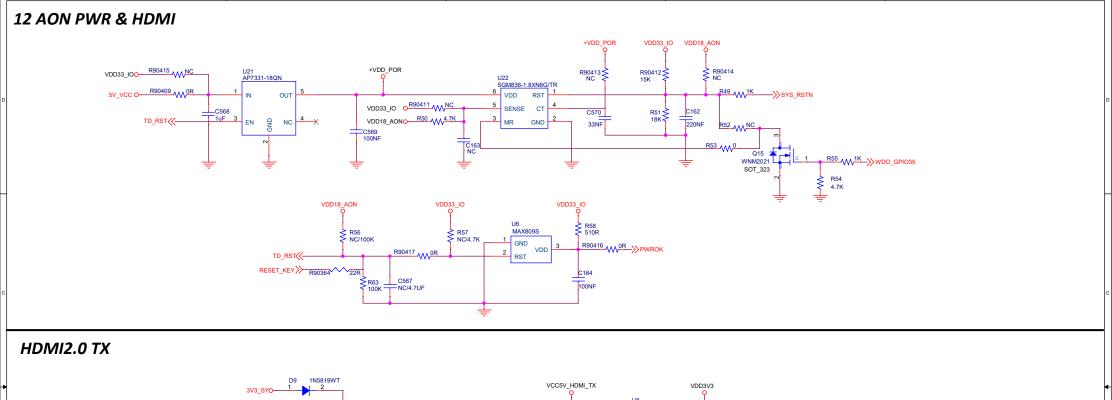


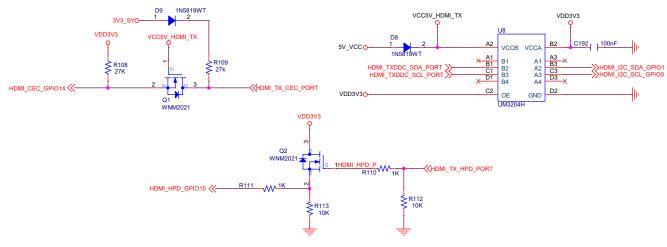
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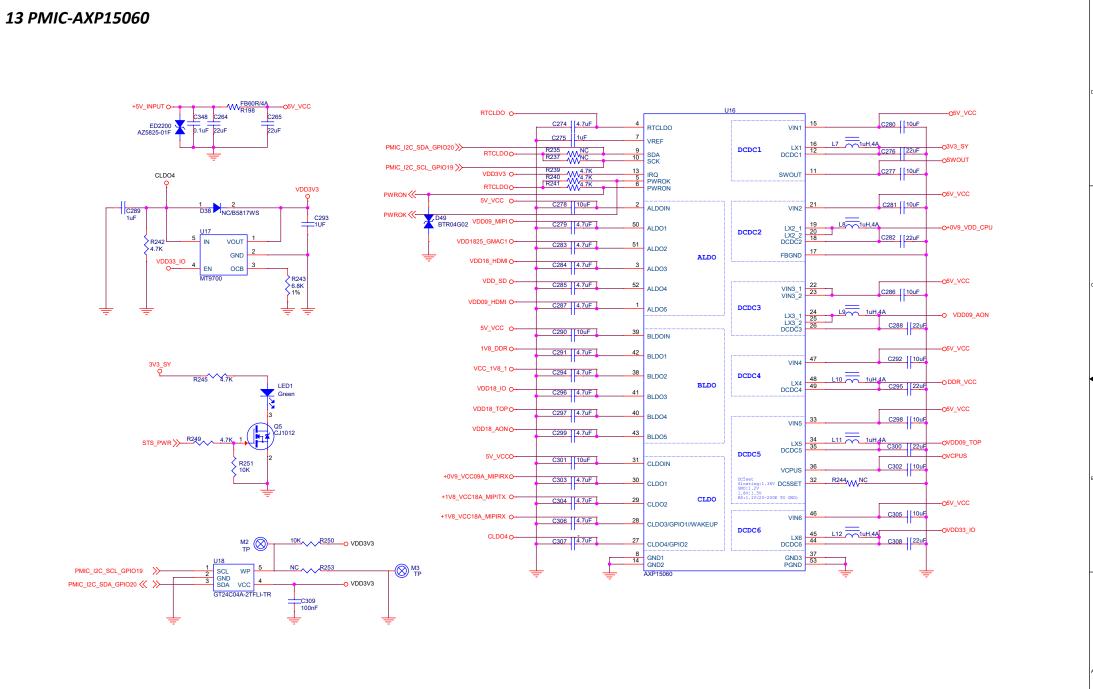




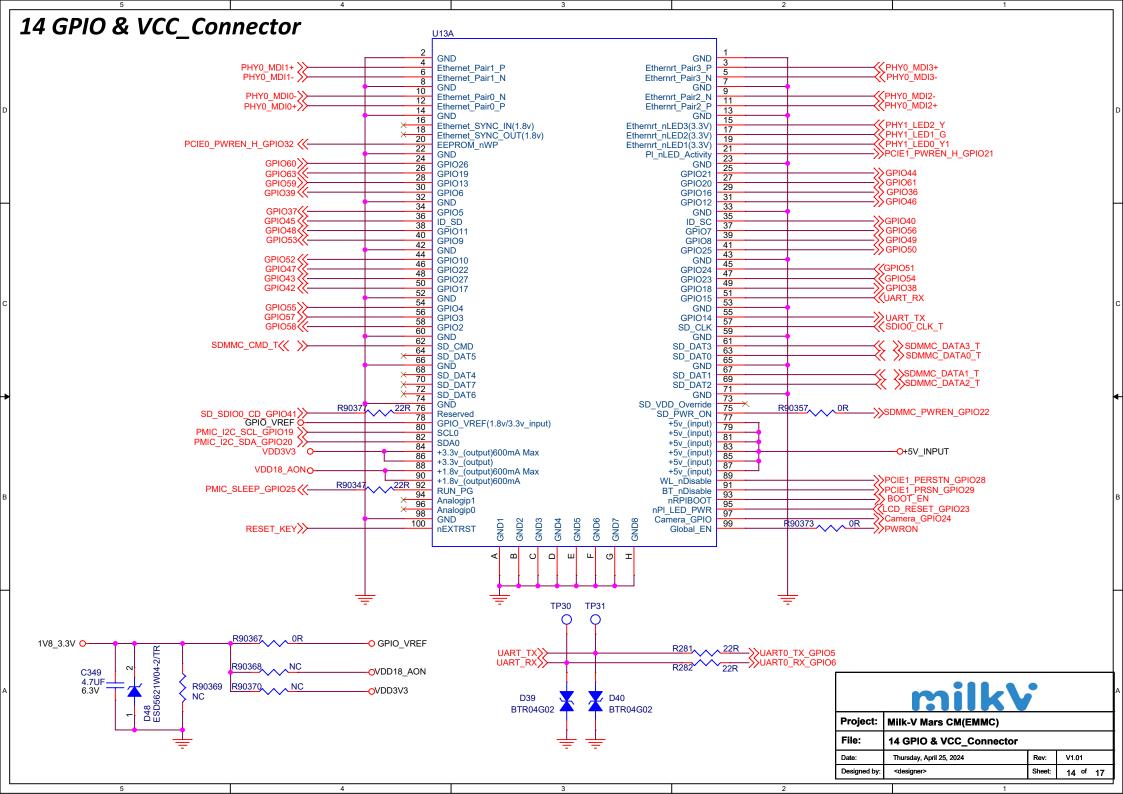




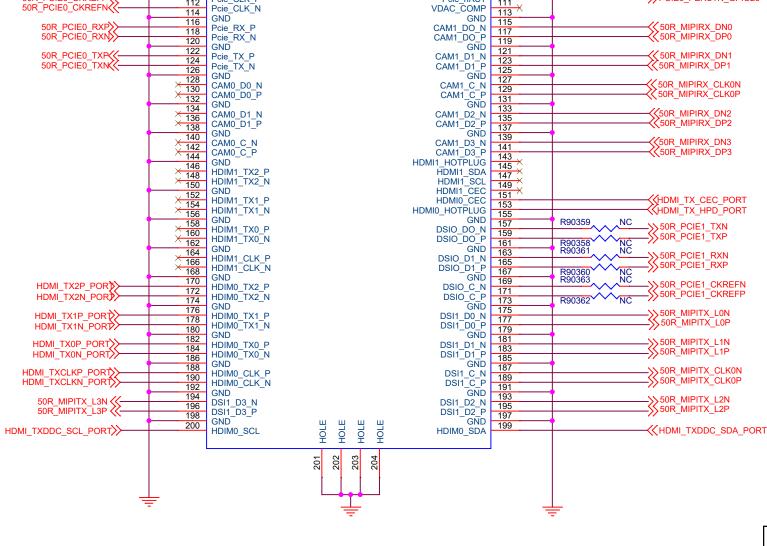
milkV				
Project:	Milk-V Mars CM(EMMC)			
File:	12 AON PWR & HDMI			
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milkV			
Project:	Project: Milk-V Mars CM(EMMC)		
File:	File: 13 PMIC-AXP15060		
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15 High Speed Serial_Connector PCIE0_PRSN_GPIO27 <<-Pcie CLK nREQ USB OTG ID 104 103 345R_USB20_DM 45R_USB20_DP × 106 Reserved USB2 N 105 USB2 P × 108 Reserved GND GND 110 109 50R PCIE0 CKREFP ->>PCIE0 PERSTN GPIO26 Pcie CLK P Pcie nRST 112 111 50R_PCIE0_CKREFN Pcie_CLK_N VDAC_COMP 114 113 GND **GND** 116 115 50R_PCIE0_RXP>> Pcie_RX P √50R MIPIRX DN0 CAM1 DO N 118 50R_PCIE0_RXN 50R_MIPIRX_DP0 Pcie_RX_N CAM1_DO_P 120 119 GND 122 121 50R_PCIE0_TXP Pcie_TX_P CAM1_D1_N √50R MIPIRX DN1 124 123 50R_PCIE0_TXN CAM1_D1_P (50R MIPIRX DP1 Pcie_TX_N 126 125 GND GND 128 127 CAM1 C_N CAM0_D0_N √50R MIPIRX CLK0N × 130 × 132 ₹ 50R MIPIRX CLK0P CAMO DO P CAM1 C P 131 **GND** GND 134 133 × 1<u>36</u> **≪**50R MIPIRX DN2 CAM1_D2_N CAM0_D1_N 135 √50R MIPIRX DP2 CAMO D1 P CAM1 D2 P



milkV			
Project:	Project: Milk-V Mars CM(EMMC)		
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