

Milk-V Mars CM(TF) schematic

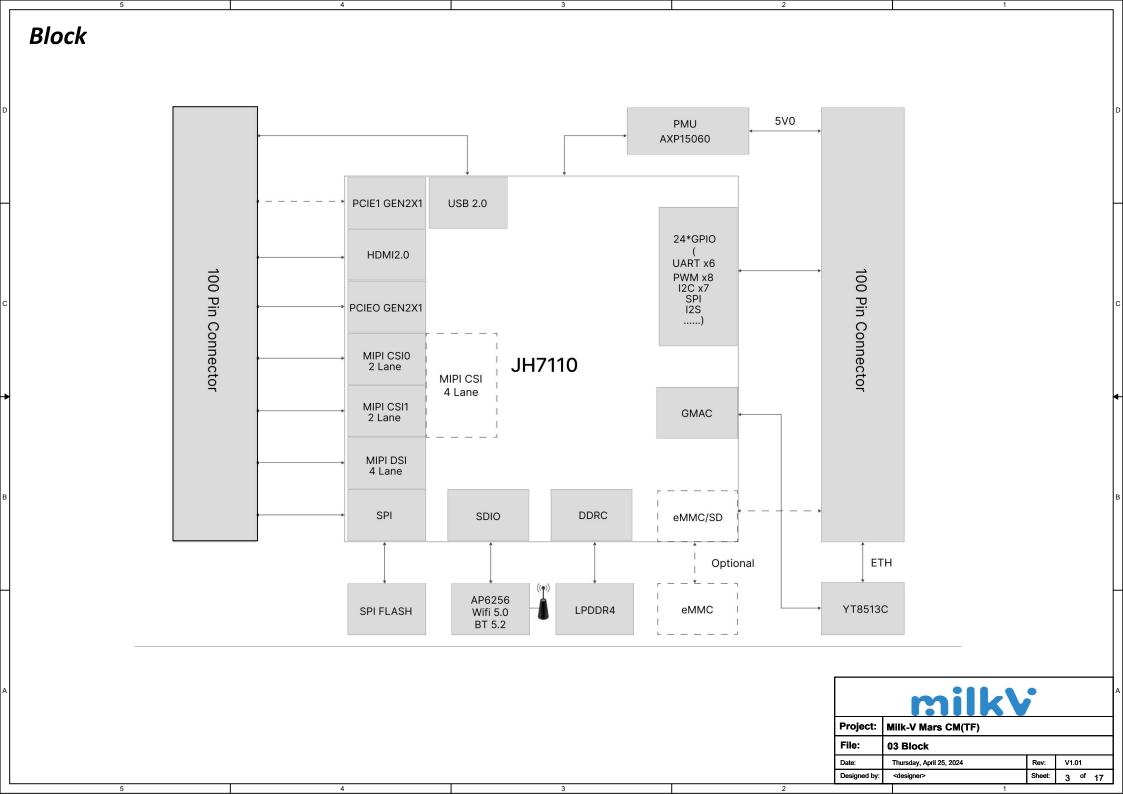
V1.01

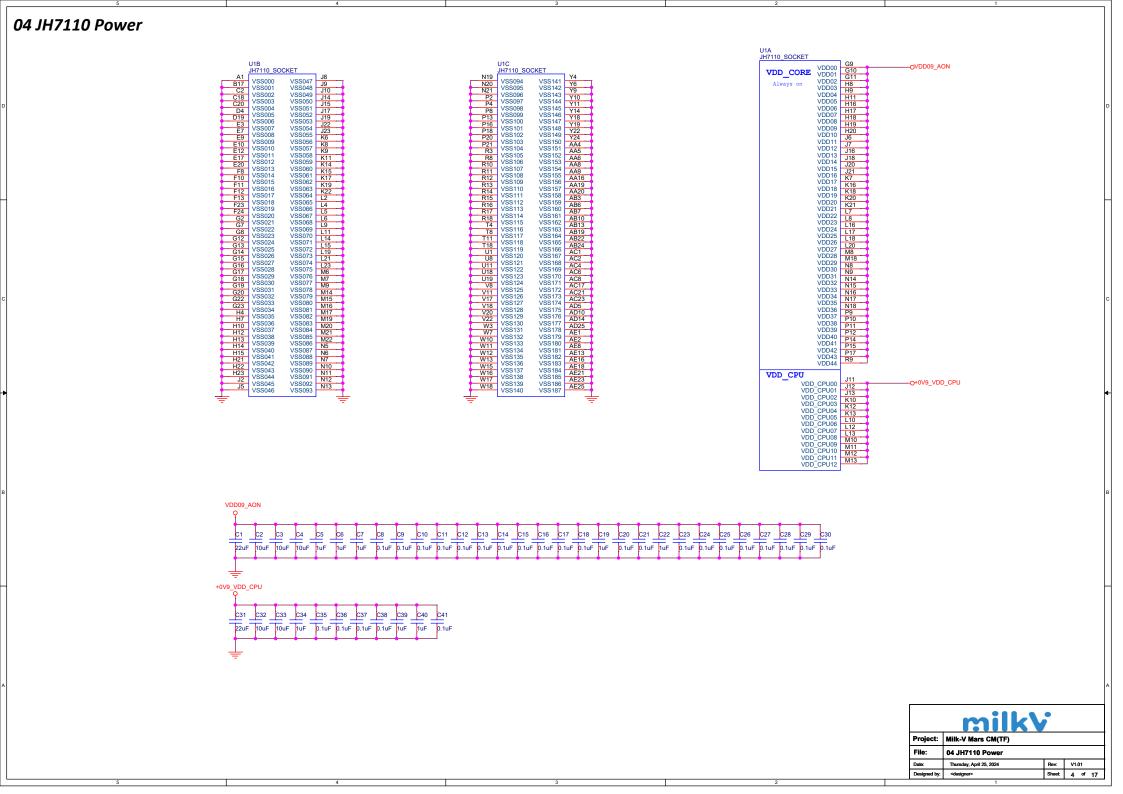
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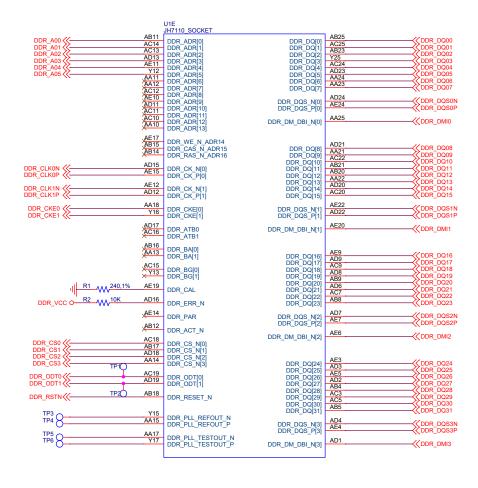
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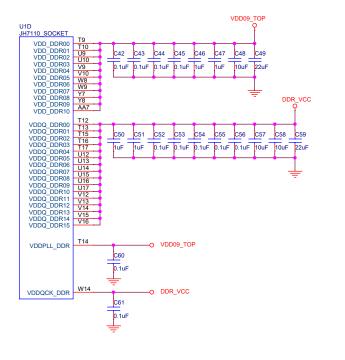
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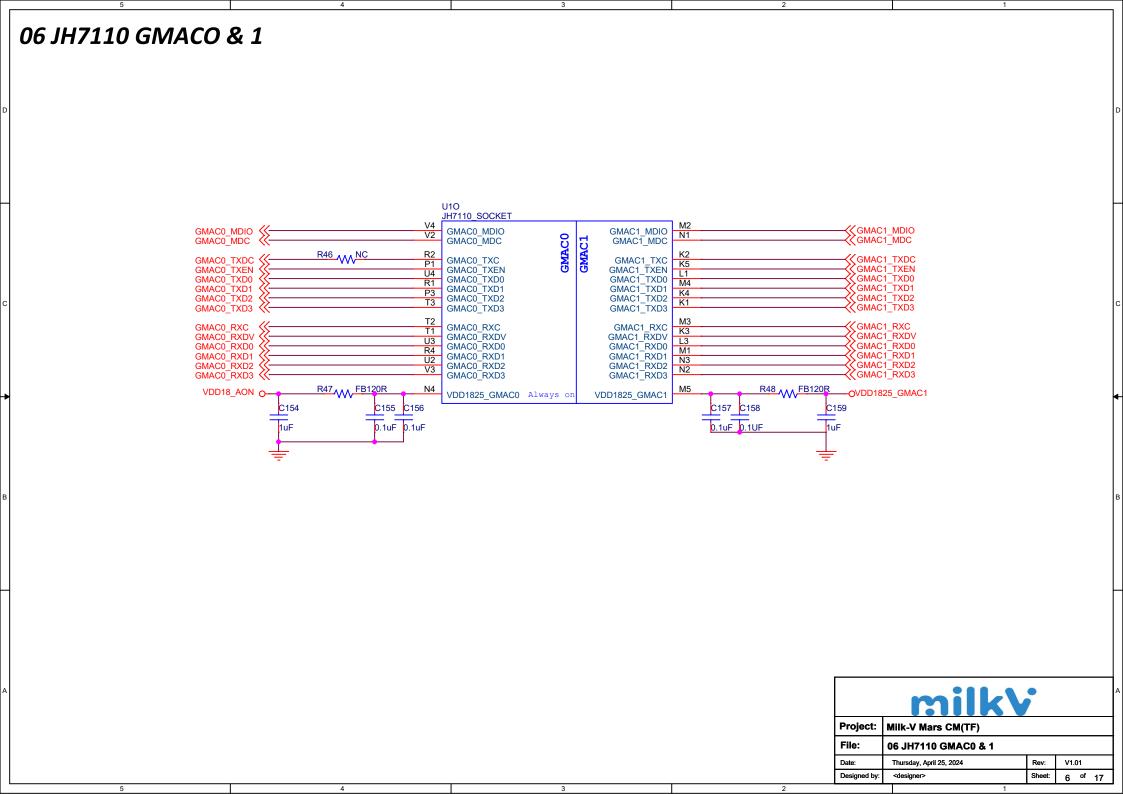


05 JH7110 DDR Ctrl

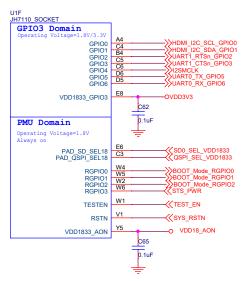


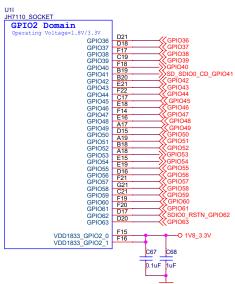


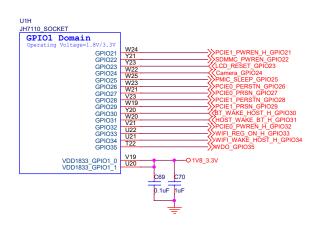
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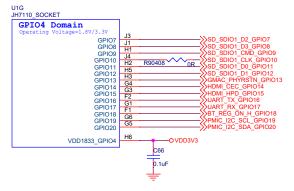


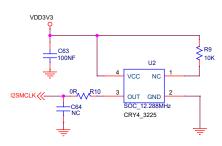
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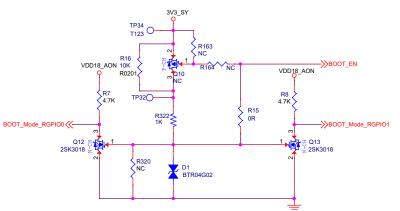


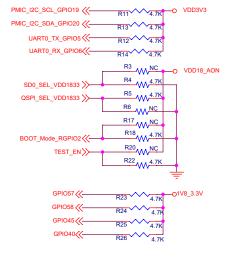




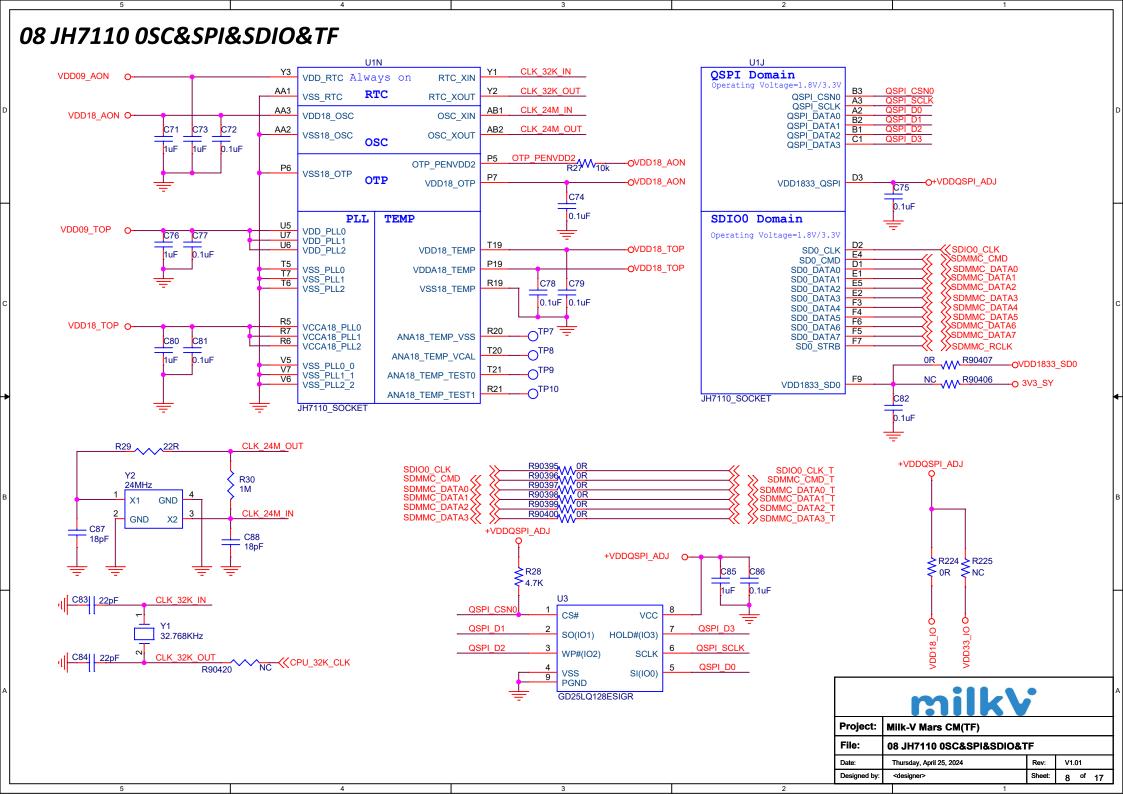


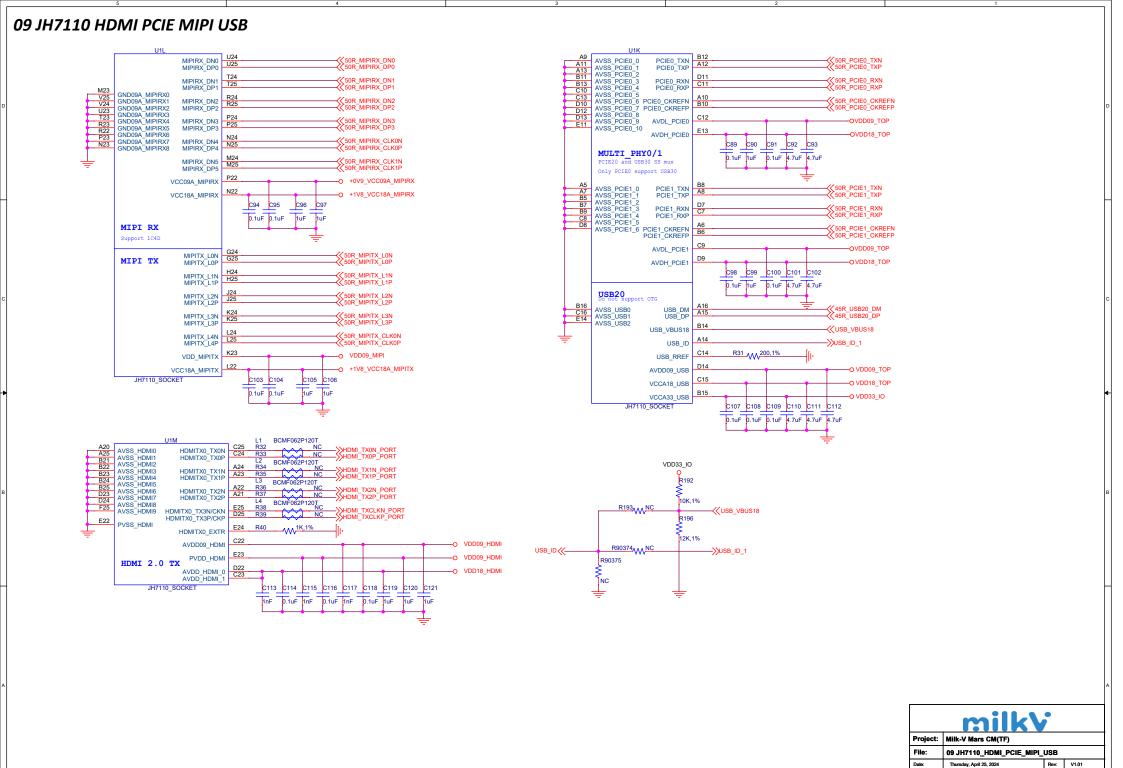






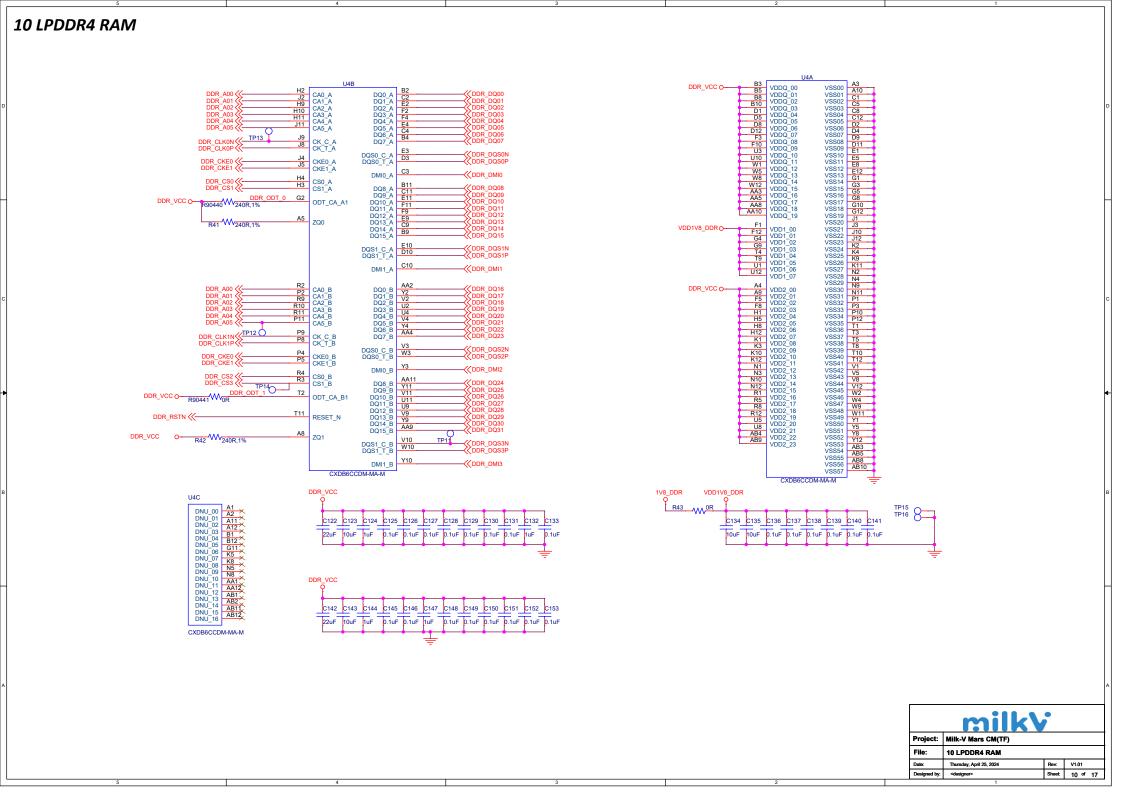
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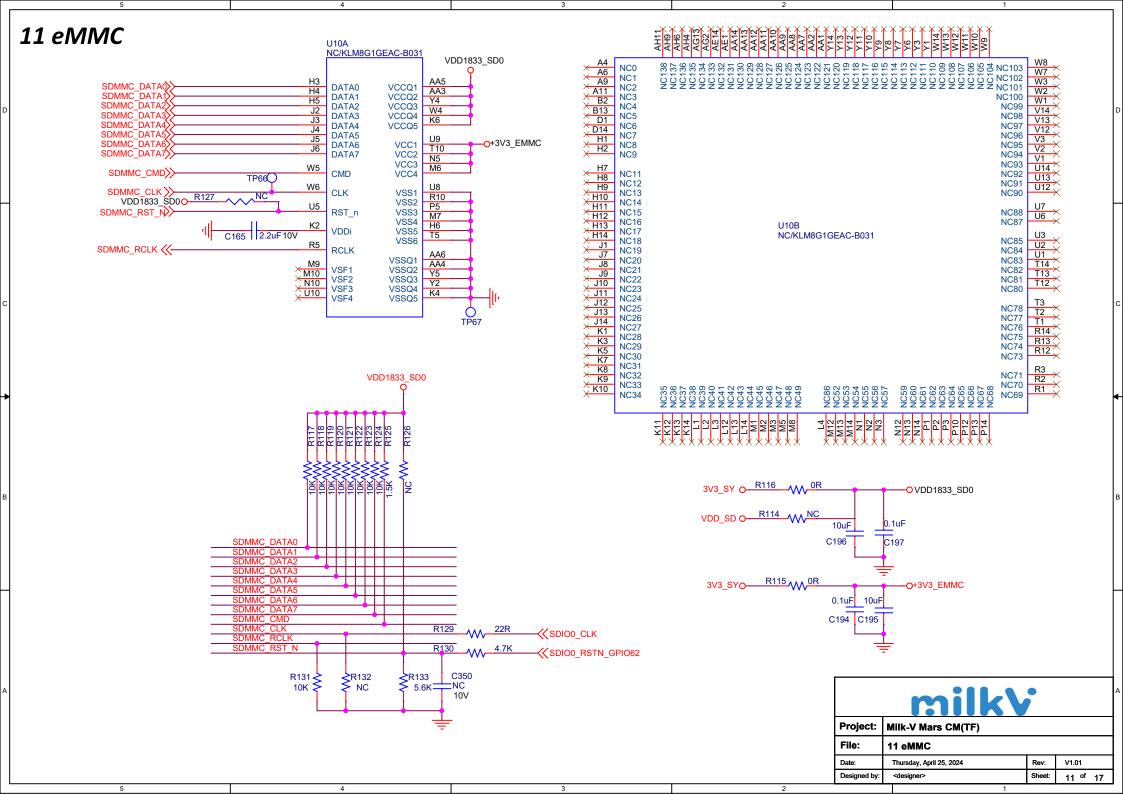


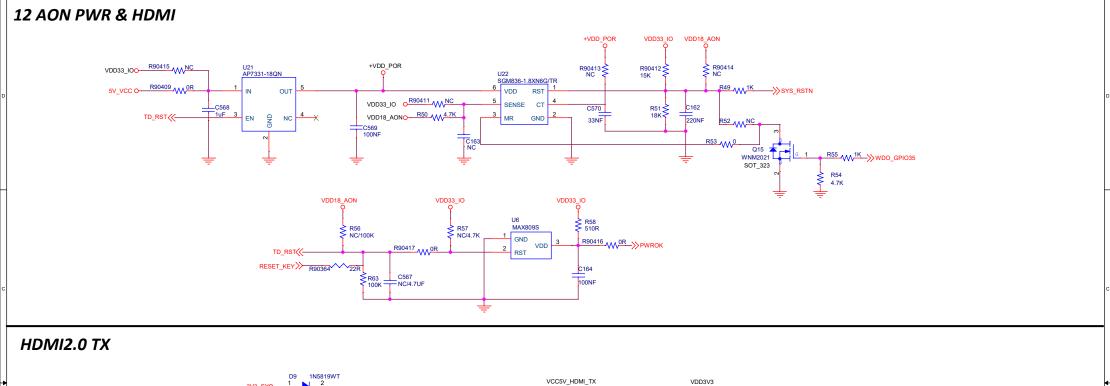


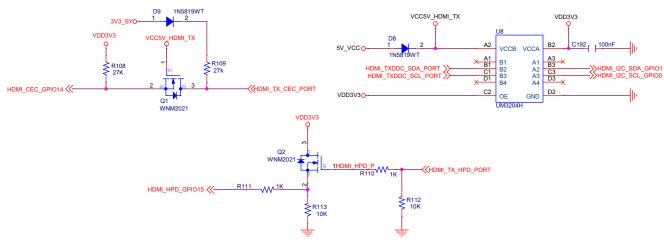
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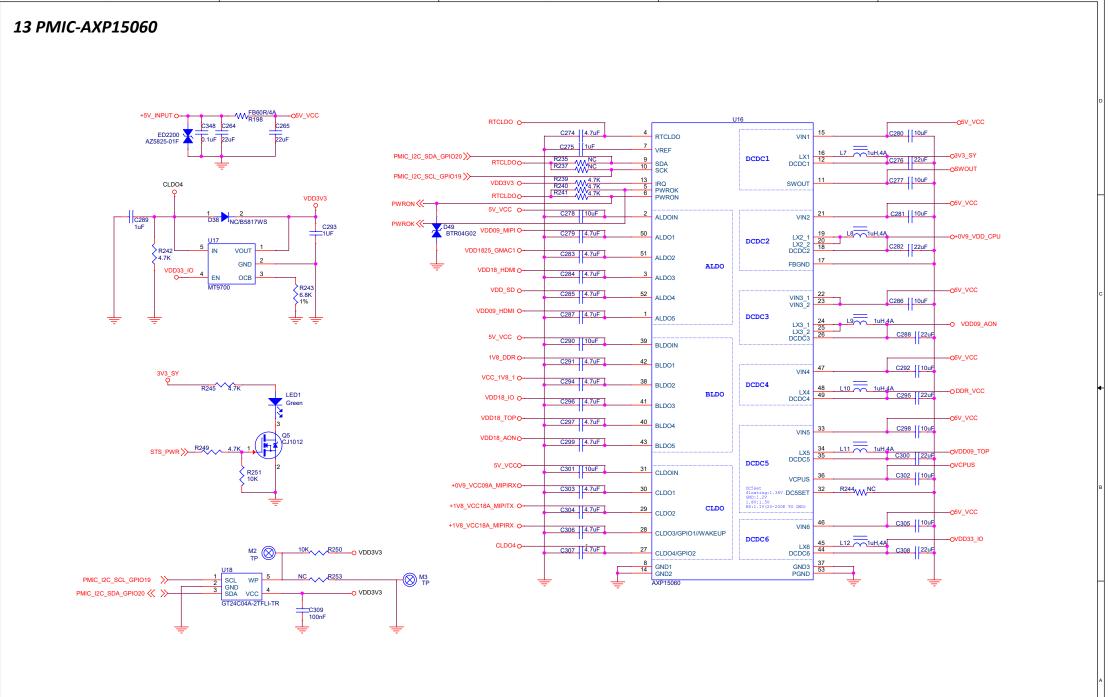




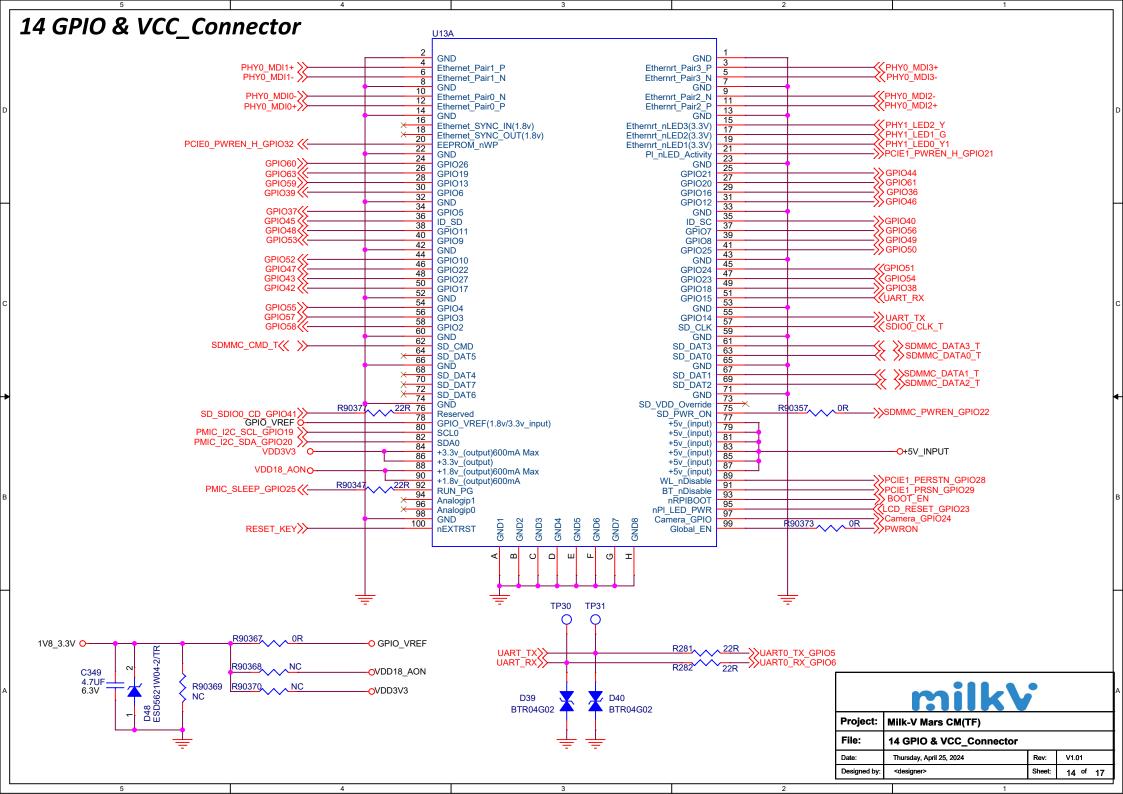


M5 MARK	MARK MARK	M7 MARK
M9	M10	M11
MARK	MARK	MARK

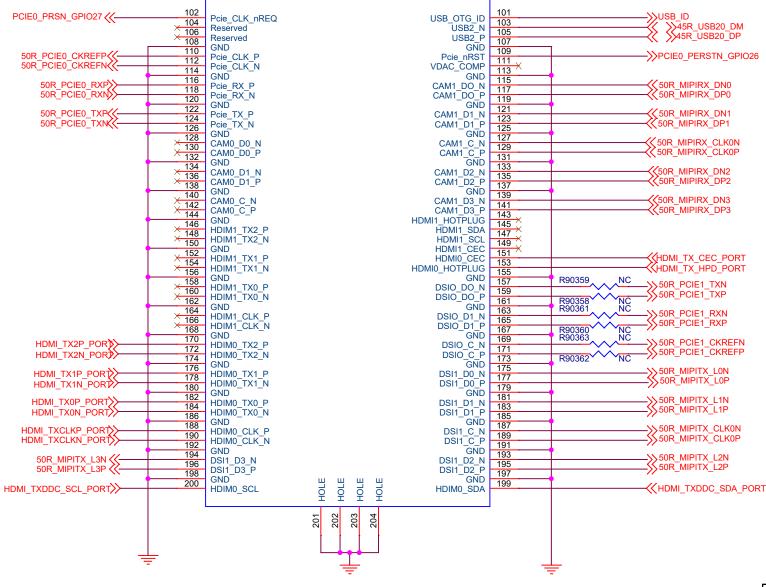
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	milkV		
Project:	Milk-V Mars CM(TF)		
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15 High Speed Serial_Connector PCIE0_PRSN_GPIO27 <<-Pcie CLK nREQ USB OTG ID 104 103 358_USB20_DM 45R_USB20_DP × 106 Reserved USB2 N 105 USB2 P × 108 Reserved GND GND 110 109 50R PCIE0 CKREFP ->>PCIE0 PERSTN GPIO26 Pcie CLK P Pcie nRST 112 111 50R_PCIE0_CKREFN Pcie_CLK_N VDAC_COMP 114 113



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Project:	Project: Milk-V Mars CM(TF)			
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