

Milk-V Mars schematic

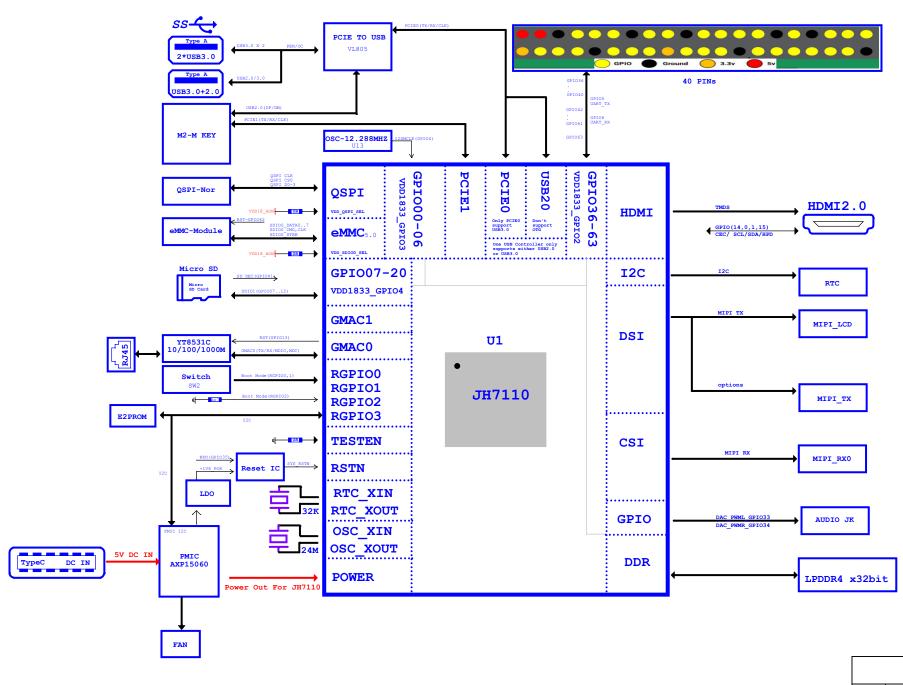


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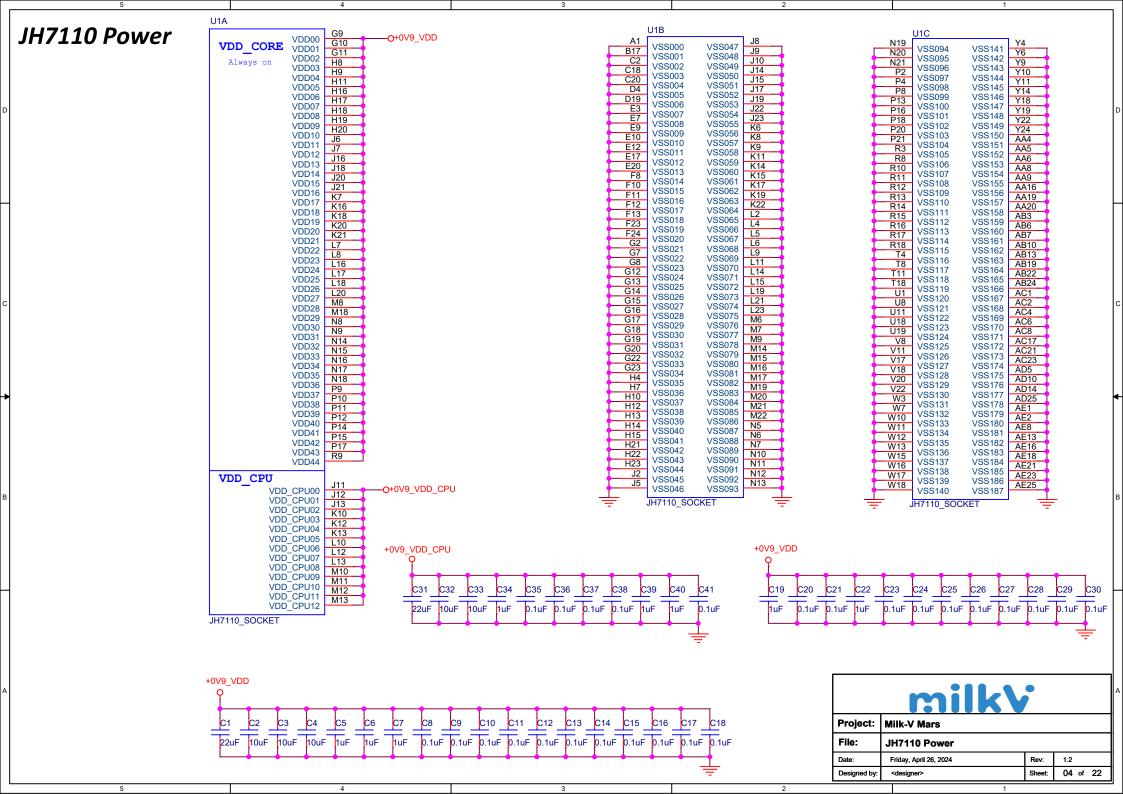
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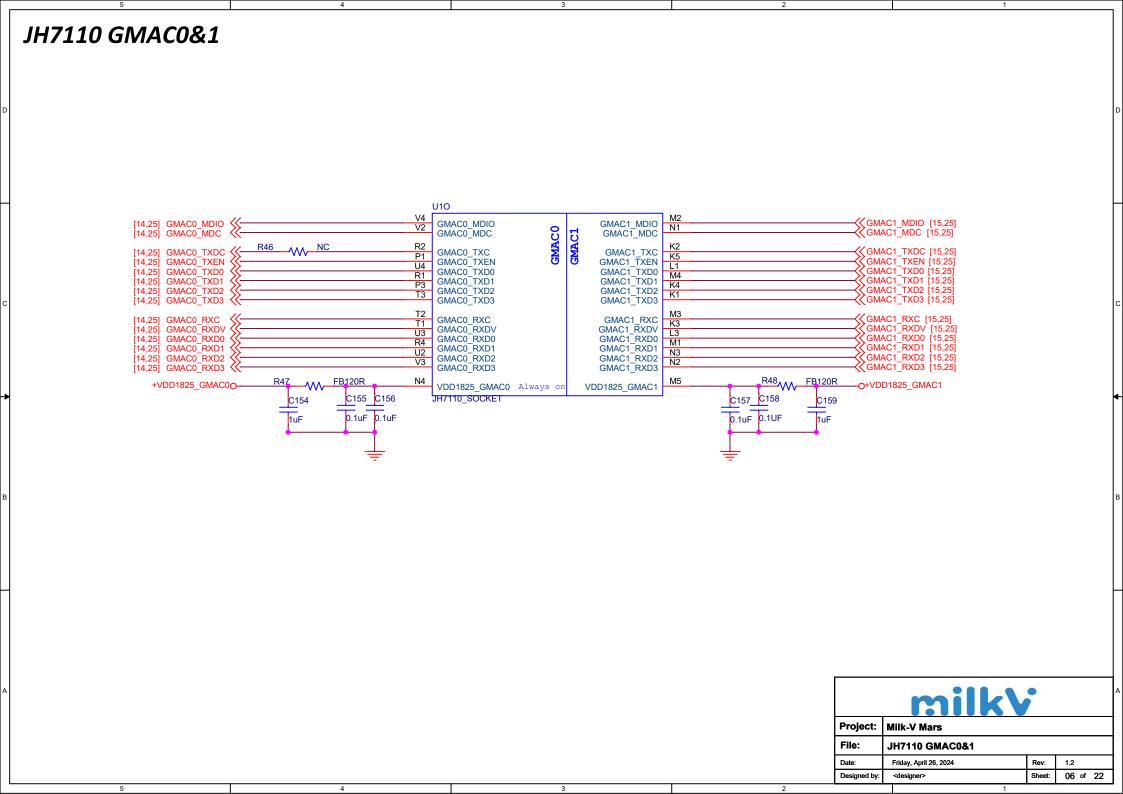
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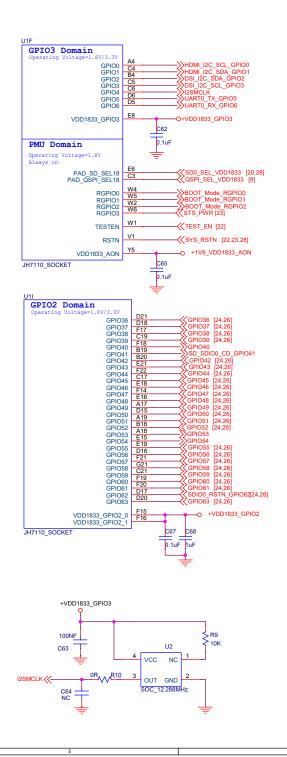
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Project:	Milk-V Mars			
File:	Block Diagram			
Date:	Friday, April 26, 2024	Rev:	1.2	
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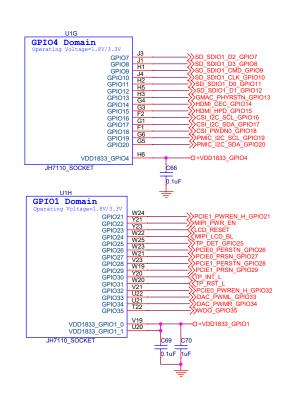


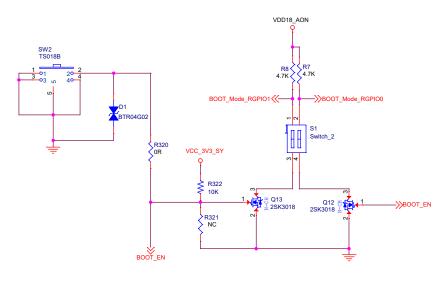
JH7110 DDR Ctrl +0V9 VDD DDR U1E U1D [13] DDR A00 << DDR ADRIO DDR DQ[0] CDDR DQ00 [13] VDD DDR00 AC25 T10 AC14 131 DDR A01 DDR DQ01 DDR_ADR[1] DDR DQ[1] VDD DDR01 C42 C43 AC13 AB23 U9 C44 C45 C46 C47 C48 C49 131 DDR A02 (DDR DQ02 [13] DDR ADR[2] DDR_DQ[2] VDD DDR02 AD13 U10 [13] DDR_A03 << DDR DQ[3] CDDR_DQ03 [13] DDR ADRI3 VDD DDR03 22uF AE11 AC24 V9 0.1uF 0.1uF 0.1uF 0.1uF 1uF 1uF DDR_DQ04 [13] DDR_DQ05 [13] DDR_DQ06 [13] 10uF [13] DDR A04 DDR ADRI4 DDR DQ[4] VDD DDR04 Y12 AD23 V10 [13] DDR_A05 << DDR ADR[5 DDR_DQ[5 VDD_DDR05 AA11 AA24 W8 DDR ADR[6] DDR_DQ[6] VDD DDR06 AA12 AA23 W9 DDR DQ07 [13] DDR ADRI7 DDR DQ[7] VDD DDR07 AC12 Y7 DDR ADRI8 VDD DDR08 Y8 AE10 AD24 ⟨CDDR DQS0N [13] DDR ADR[9] DDR DQS N[0] VDD DDR09 +1V1 VDDQ DDR AD11 AE24 AA7 DDR DQS0P [13] DDR_DQS_P[0] VDD_DDR10 DDR_ADR[10] AC11 DDR ADR[11] AC10 T12 ✓ DDR_DMI0 [13] DDR ADR[12] DDR DM DBI N[0] VDDQ DDR00 T13 AA10 DDR_ADR[13] VDDQ_DDR01 C58 T15 C50 C51 C52 C53 C55 C56 C57 VDDQ DDR02 T16 DDR WE N ADR14 VDDQ DDR03 1uF 0.1uF 0.1uF 0.1uF 0.1uF 10uF 10uF 22uF AB15 T17 DDR_DQ08 [13] DDR_DQ09 [13] DDR_DQ10 [13] DDR_CAS_N_ADR15 DDR_DQ[8] VDDQ DDR04 AB14 U12 AA21 DDR RAS N ADR16 DDR DQi9i VDDQ DDR05 AC22 U13 DDR_DQ[10] VDDQ DDR06 AB21 U14 DDR_DQ10 [13] DDR_DQ11 [13] DDR_DQ12 [13] [13] DDR CLKON << DDR CK N[0] DDR DQ[11] VDDQ DDR07 AB20 U15 AE15 DDR_CK_P[0] [13] DDR CLKOP (DDR_DQ[12] VDDQ_DDR08 U16 AA22 DDR_DQ13 [13] DDR_DQ14 [13] DDR_DQ15 [13] DDR_DQ[13] VDDQ DDR09 AD20 U17 VDDQ_DDR10 V12 [13] DDR CLK1N ((DDR CK N[1] DDR DQ[14] AD12 VDDQ_DDR11 V13 [13] DDR_CLK1P < DDR_CK_P[1] DDR_DQ[15] VDDQ DDR12 V14 AA18 VDDQ_DDR13 V15 DDR_DQS1N [13] DDR_DQS1P [13] [13] DDR_CKE0 (13] DDR_CKE1 DDR CKE[0] DDR DQS N[1] Y16 AD22 VDDQ_DDR14 V16 DDR_CKE[1] DDR_DQS_P[1] VDDQ DDR15 AD17 √CDDR_DMI1 [13] DDR ATB0 DDR DM DBI N[1] AC16 DDR ATB1 T14 -O +0V9 VDDPLL DDR VDDPLL DDR DDR BA[0] C60 0.1uF AA13 AE9 DDR BA[1] DDR DQ[16] AD9 ODR DQ17 [13] DDR DQ[17] AC9 AC15 DDR_BG[0] DDR DQ18 [13] DDR_DQ[18] Y13 AD8 DDR BG[1] DDR DQ[19] CDDR DQ19 [13] AB9 DDR DQ20 [13] DDR DQ[20] AD6 W14 DDR_DQ21 [13] DDR_DQ22 [13] -O +1V1 VDDQCK DDR DDR CAL DDR DQ[21] VDDQCK DDR AC7 DDR_DQ[22] JH7110 SOCKET C61 +1V1_VDDQ_DDR O__R2_\\\\\\\\\\\ AB8 ODR DQ23 [13] DDR ERR N DDR DQ[23] 0.1uF DDR_DQS_N[2] DDR_DQS_P[2] (DDR_DQS2N [13] DDR PAR AE7 DDR DQS2P [13] AB12 DDR_ACT_N DDR DM DBI N[2] ⟨⟨DDR DMI2 [13] AC18 [13] DDR CS0 << DDR CS N[0] AB17 131 DDR CS1 < DDR CS N[1] AD18 13 DDR CS2 DDR CS N[2] AA14 AE3 CDDR_DQ24 [13] [13] DDR_CS3 << DDR_CS_N[3] DDR_DQ[24] AD3 DDR DQ[25] AE5 [13] DDR ODT0< DDR ODTI01 DDR DQ[26] AD19 AD2 DDR DQ27 13 DDR ODT1 DDR_DQ[27] DDR_ODT[1] AB4 DDR_DQ28 [13] DDR_DQ29 [13] DDR DQ[28] AC3 TP2 AB18 DDR_DQ[29] [13] DDR_RSTN**<<-**DDR RESET N AC5 ₩DDR_DQ30 CDDR_DQ30 [13] CDDR_DQ31 [13] DDR_DQ[30] AB5 DDR DQ[31] Y15 DDR_PLL_REFOUT N DDR PLL REFOUT P AA15 DDR PLL REFOUT P DDR DQS N(3) ≪DDR DQS3N [13] AE4 DDR_DQ33N [13] DDR_DQS_P[3] DDR PLL TESTOUT N AA17 DDR PLL_TESTOUT_N DDR_PLL_TESTOUT_P ✓ DDR DMI3 [13] DDR PLL TESTOUT P DDR DM DBI N[3] milkV JH7110 SOCKET Project: Milk-V Mars File: JH7110 DDR Ctrl Date Friday, May 10, 2024 Rev. 1.2 05 of 22 Designed by <designer> Sheet:

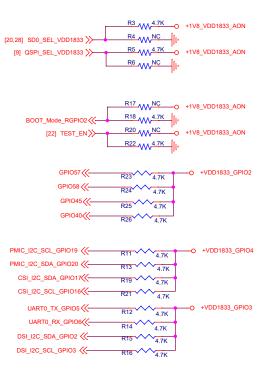


JH7110 GPIOs

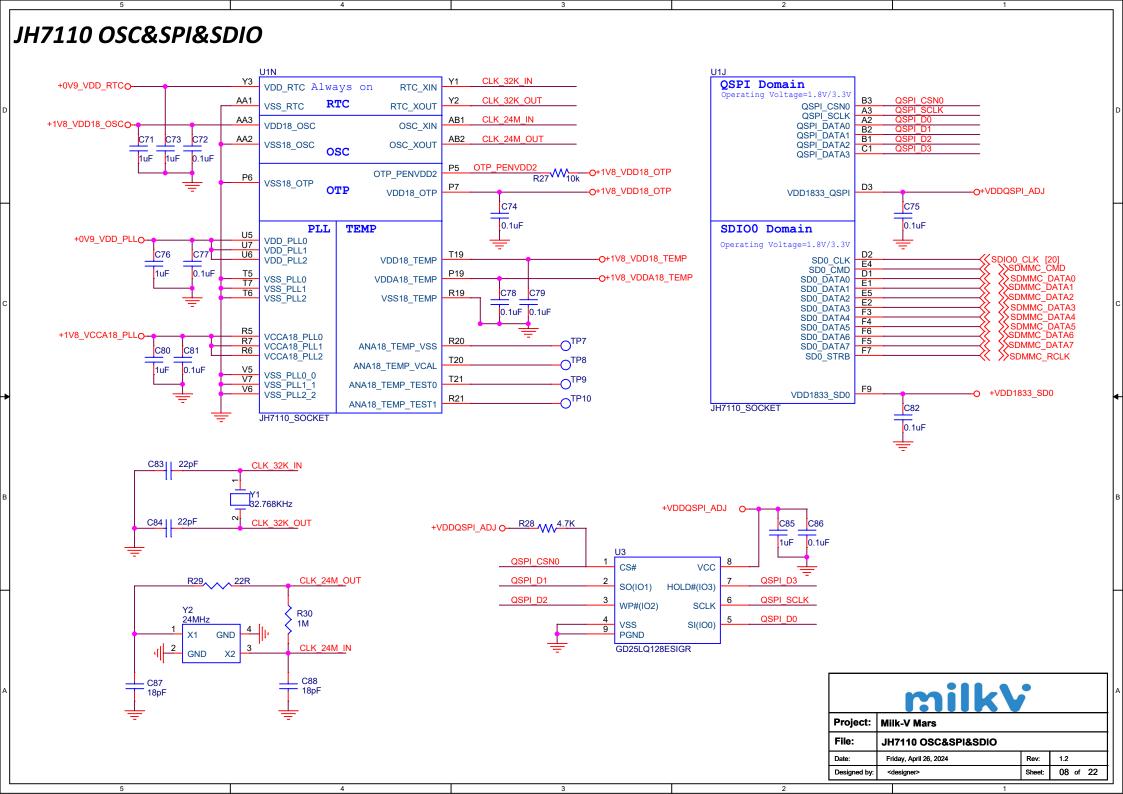


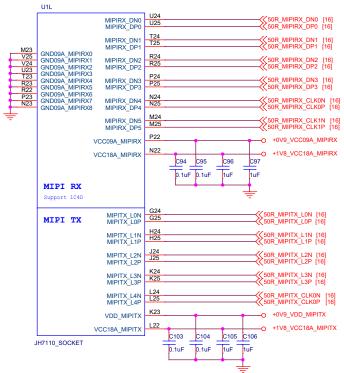


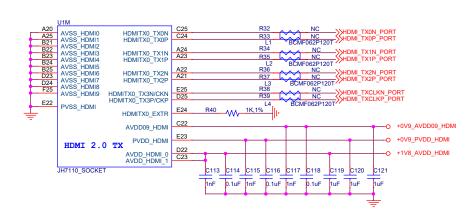


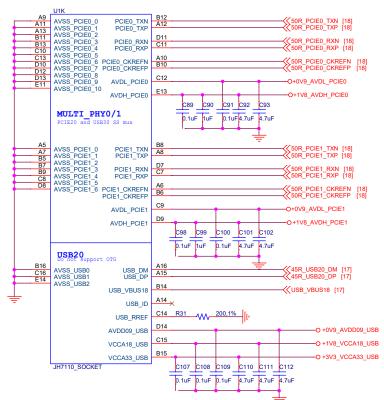


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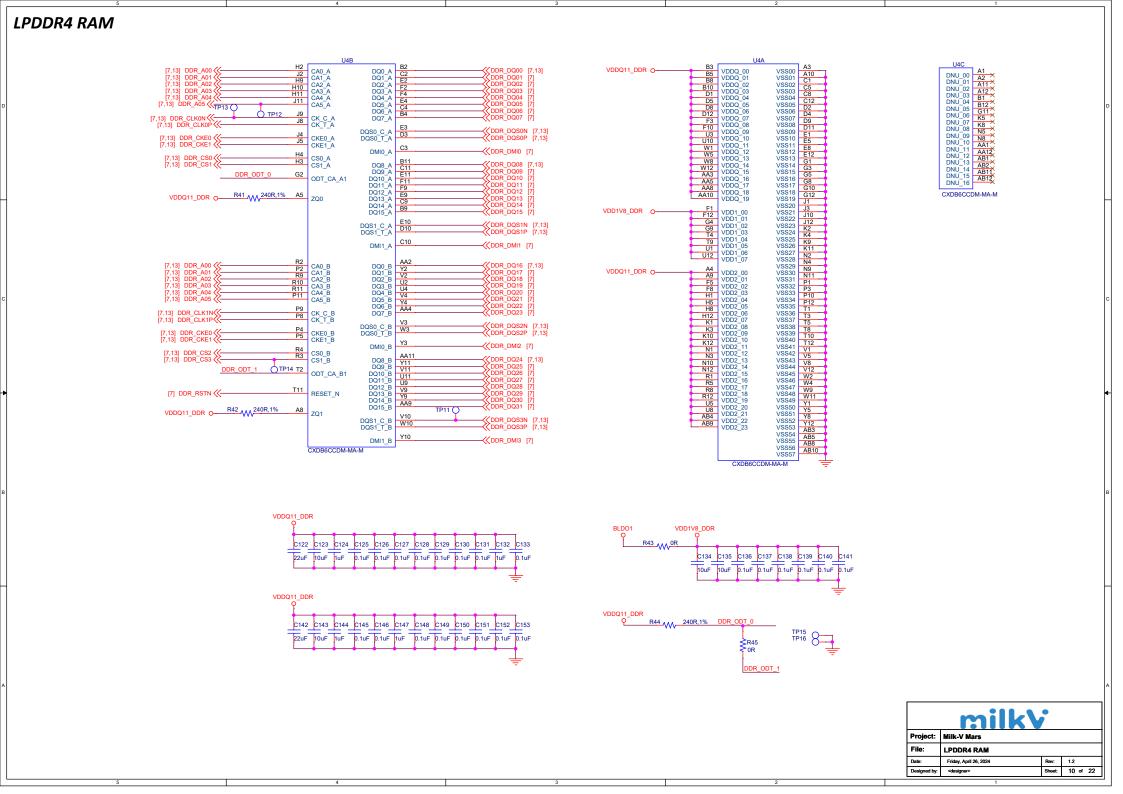


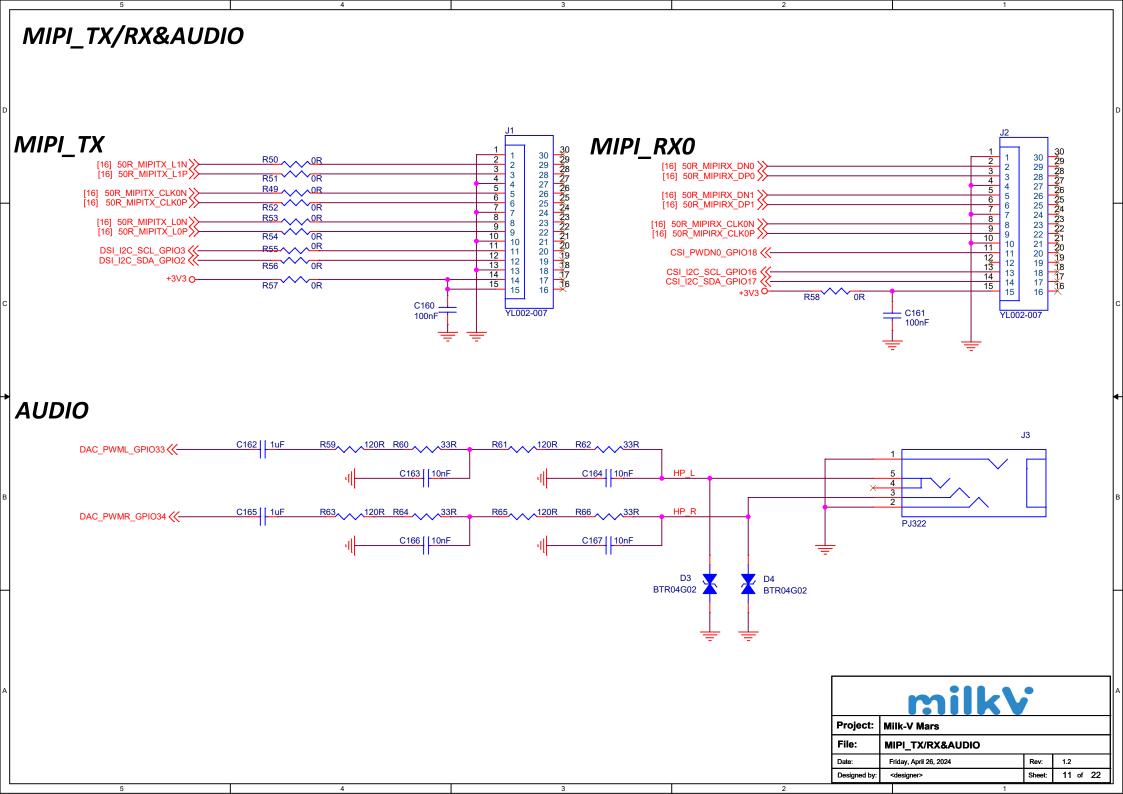


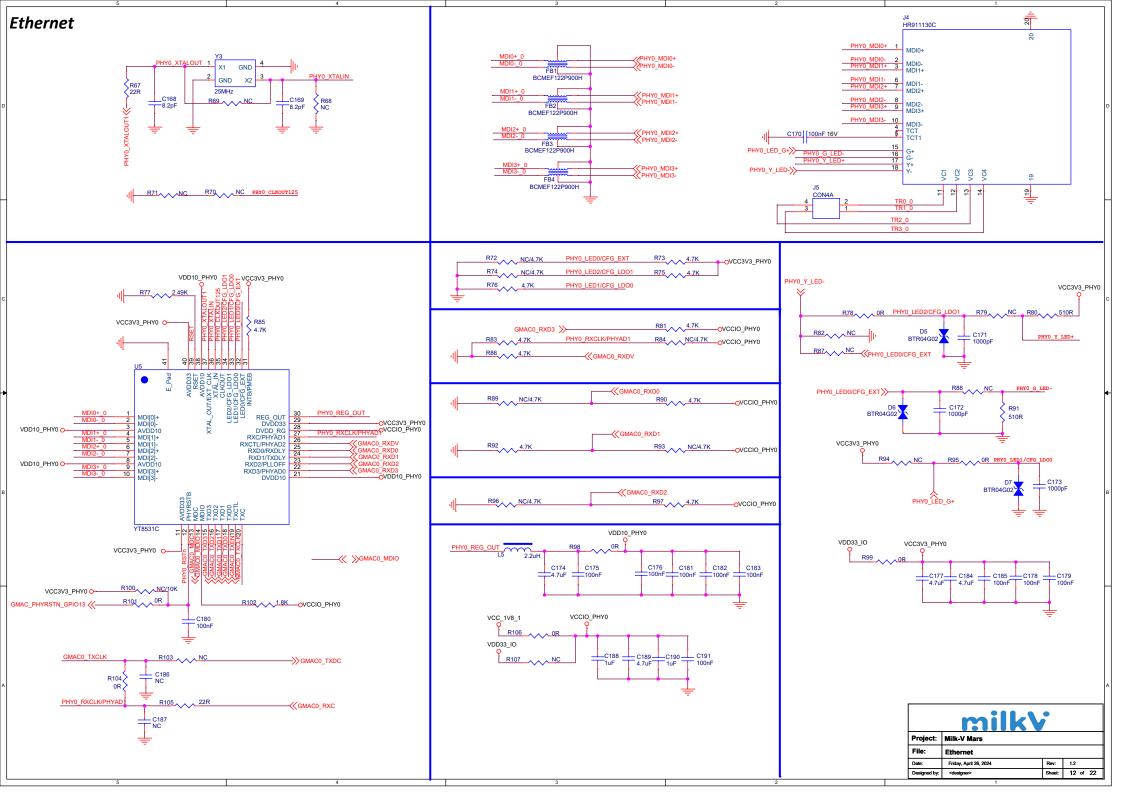


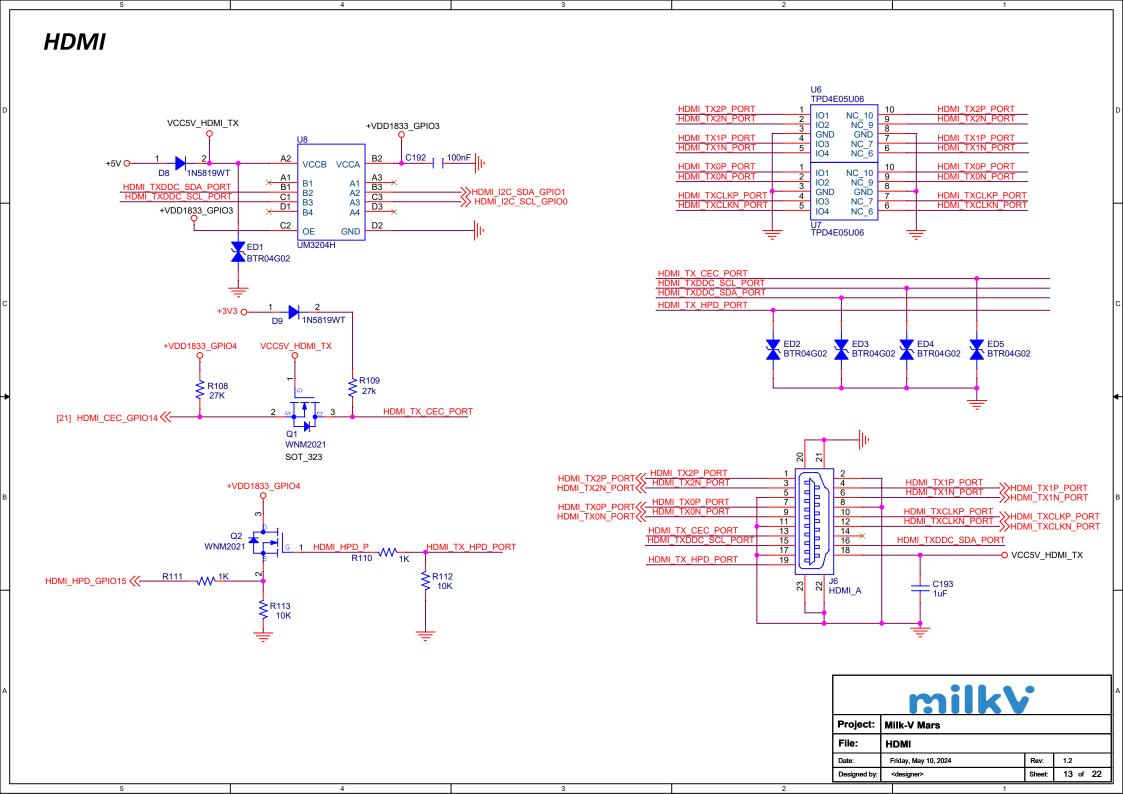


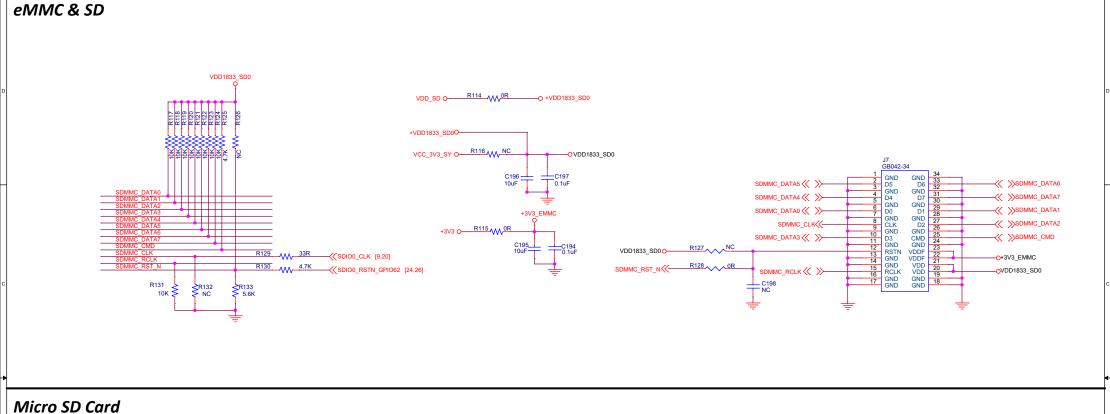
milkV			
Project: Milk-V Mars			
File: JH7110_HDMI_PCIE_MIPI_USB			
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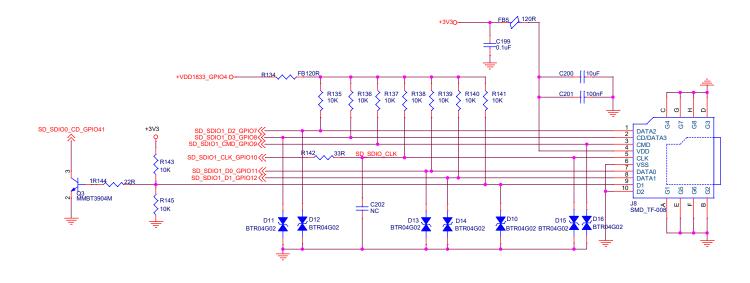












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