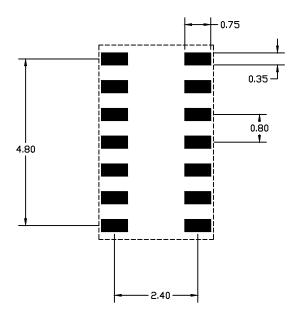
- 1



PKG CDDES

[F143A5MK+1], [F143A5+1], [F143B5+1], [F143C5+1], [F143D5+1]

NDTES:

- REFERENCE PKG. DUTLINE: 21-1048, 21-100114
- 2. LAND PATTERN COMPLIES TO: IPC7351A.
- 3. TOLERANCE: +/- 0.02 MM.
- 4. ALL DIMENSIONS APPLY TO PHFREE (+) PKG. CODE ONLY.
- 5. ALL DIMENSIONS IN MM.

-DRAWING NOT TO SCALE-

This document (including dimensions, notes & specs) is a recommendaton based on typical circuit board manufacturing parameters. Since land pattern design depend on many factors unknown to Maxim (eg. user's board manufacturing specs), user must determine suitability for use. This document is subject to change without notice.

Contact technical support at http://www.maxim-ic.com/support for further questions.



maxim integrated_™

| TITLE: | PACKAGE LAND PATTERN, | 14L DLGA, 3.3X5.6mm

APPROVAL DOCUMENT CONTROL NO.

90-0602

EV. 1