

```
module
       comparator31
        input [2:0] A;
                                          a 15 (c[x], c[ 12], c[14], c[ 9]),
                                     Or
        input [ 2:0] B;
                                          a x + ( 9t > c[x1]), c[x1]),
        input
                                          9 YOU It, C[Y], C[YY]);
                     1.
                                          316 (c[il], c[18], c[11),c[11]);
        input
                     9,4
                                     endmodule
        output
                      It.
        output
                      et,
        output
                      9t
);
    wire
           CT 44:07:
    Xnor
            91 ( cco7, A (07, B(0)),
            92 ( C1), A (1), B(1)),
            93 ( cc Y] , ACY] , B[Y]);
    not
            9 17 ( CT 17), BCY]),
            918 (ccf), BC13),
            919 ( cca7, BEOT),
            920 ( C[4], A[Y]),
            921 ( CEV), ACIT),
            922 ( CEA], A[O]);
  and
            911 ( c[10], c[r], c[10]);
            912 ( C[14], C[1], C[1],
             913 ( c[1V], c[17], c[r]);
            914 ( CEINT , CCIT, ( [Y] , CEIF]),
             95 ( cca7, ccr1, Acr7),
             96 (c(10), c(+7, A(1)),
             97 ( CEN], CEA], ACO]),
            98 (
                   C[17], C[4], B[Y]),
            99 ( CEIT], CEV], BEIT),
            910 ( <[14], (CN], B(O)),
             94 ( c[n], ([0], c[1], c[Y]),
             926 ( ((21), ((19), 9),
             927 ( c[23], c[19], (1))
              923(et, c[19], e); (2)
```