

## Audio

TITLE: mainboard

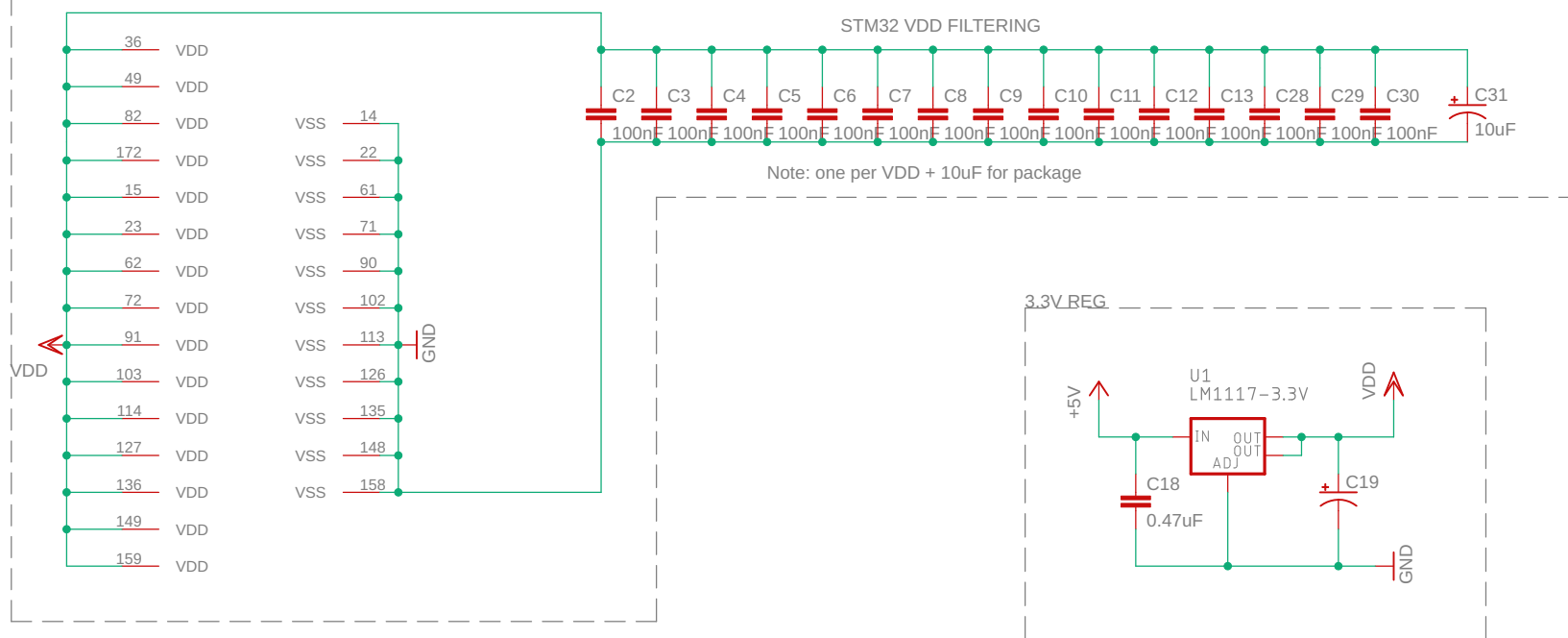
Document Number:

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Date: 2019-12-05 9:23 PM

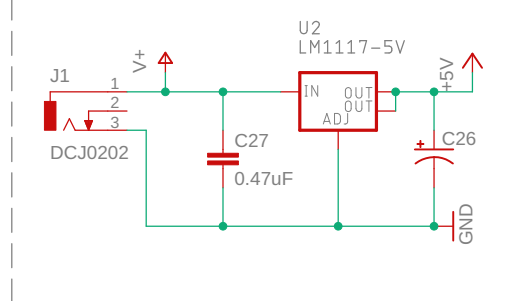
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# STM32.PWR



Note on power signals:  
- VDD is 3.3V @ 1A regulated from 5V  
- +5V is 5V @ 1A regulated from V+  
- V+ is 5V unregulated  
- GND is ground

# 5V REGULATOR



## Power

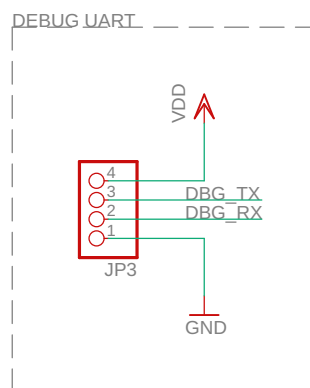
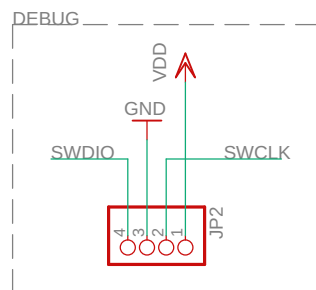
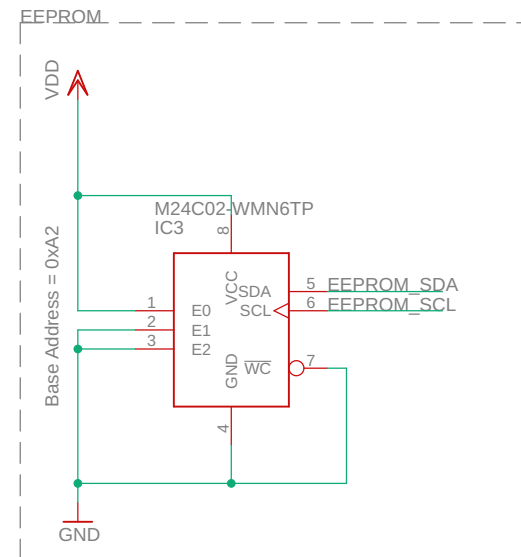
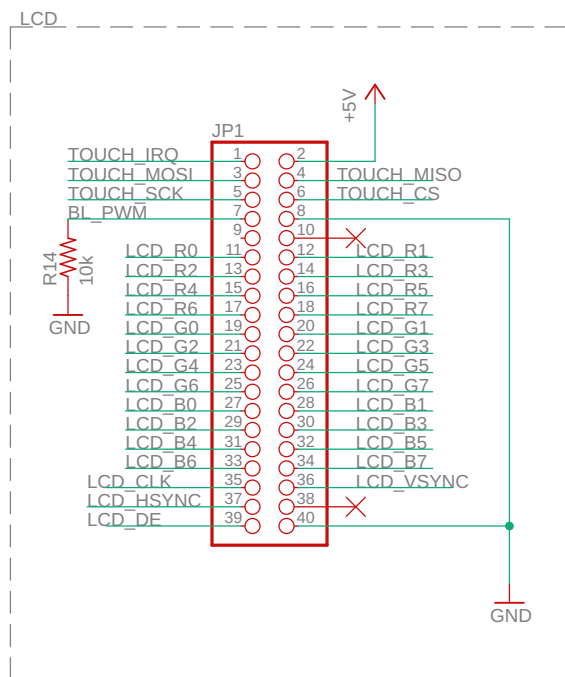
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## Headers + EEPROM

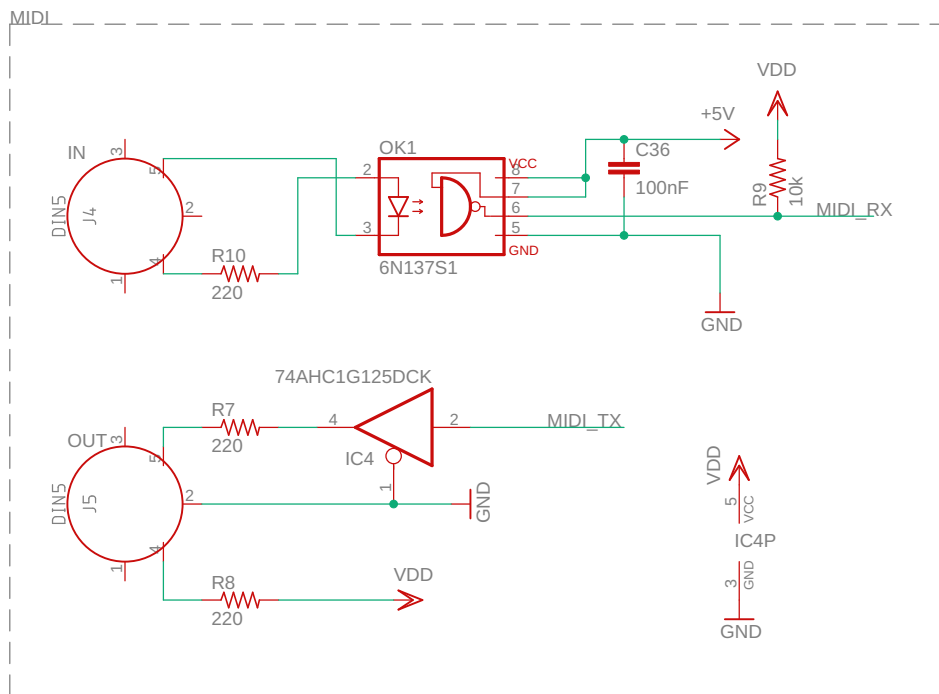
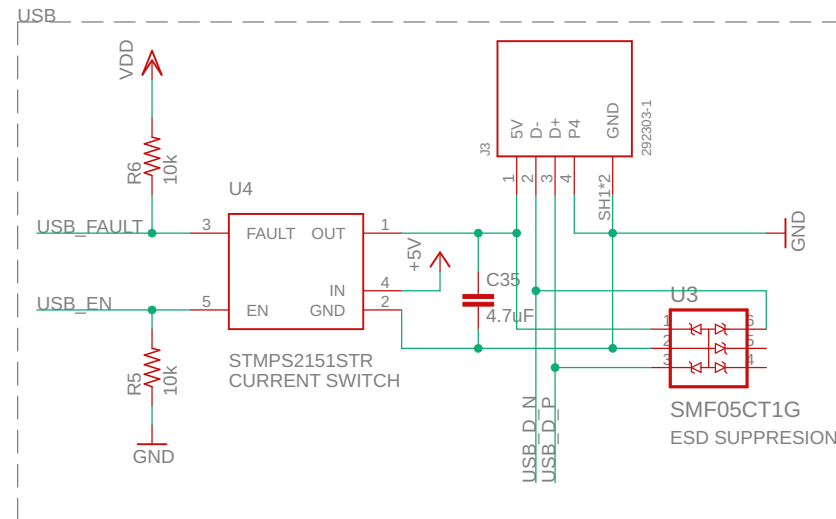
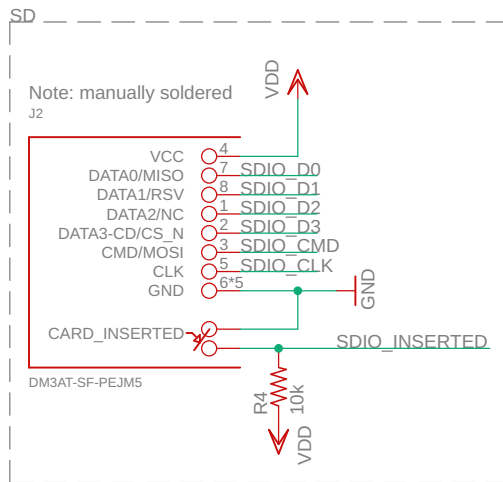
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## Connectors

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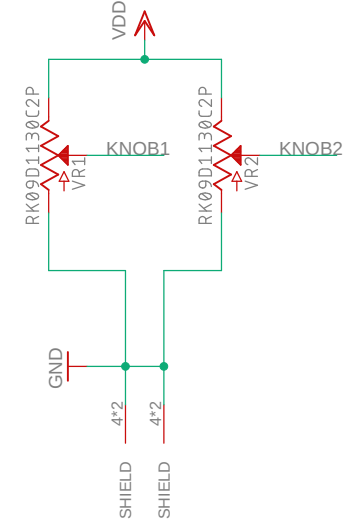
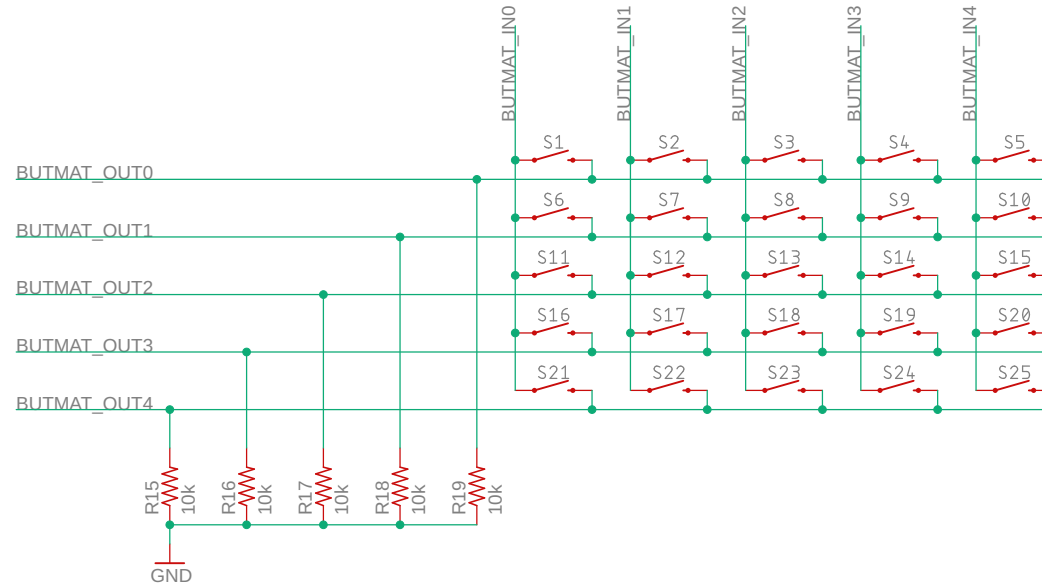
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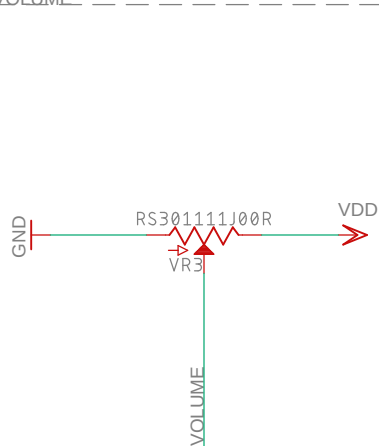
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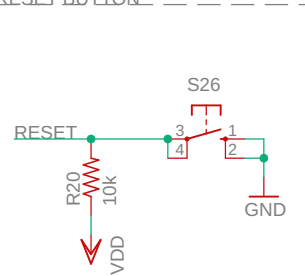
BUTTON MATRIX



VOLUME



RESET BUTTON



## Panel Interface

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