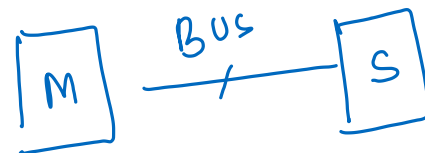
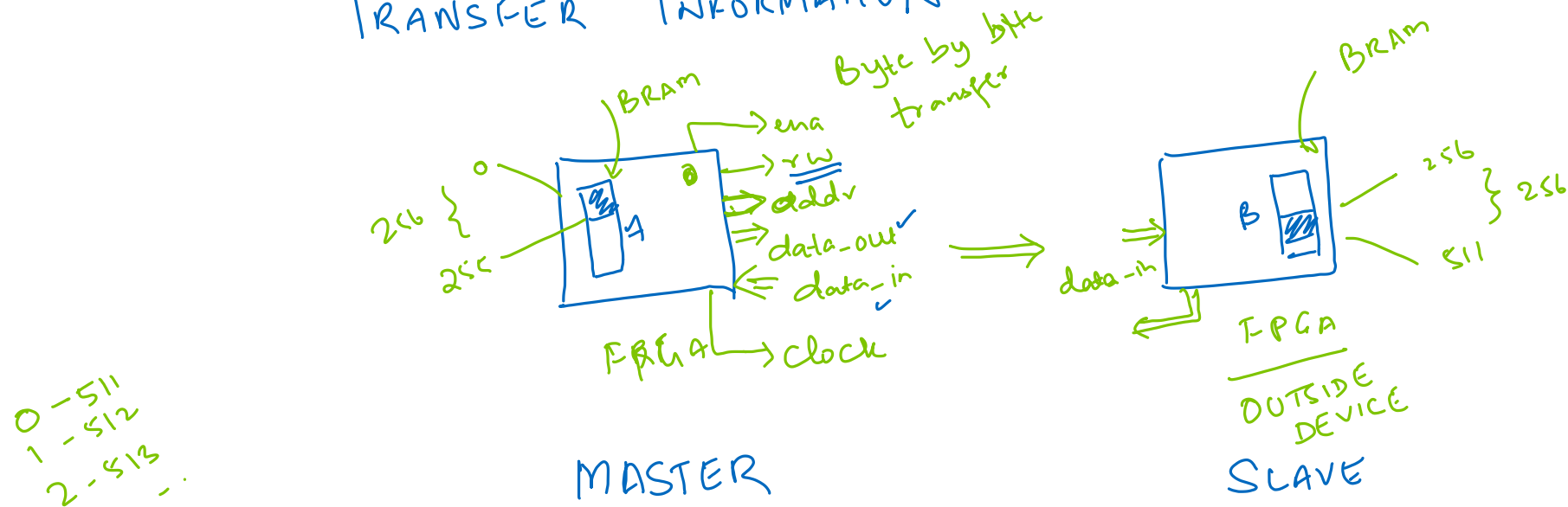
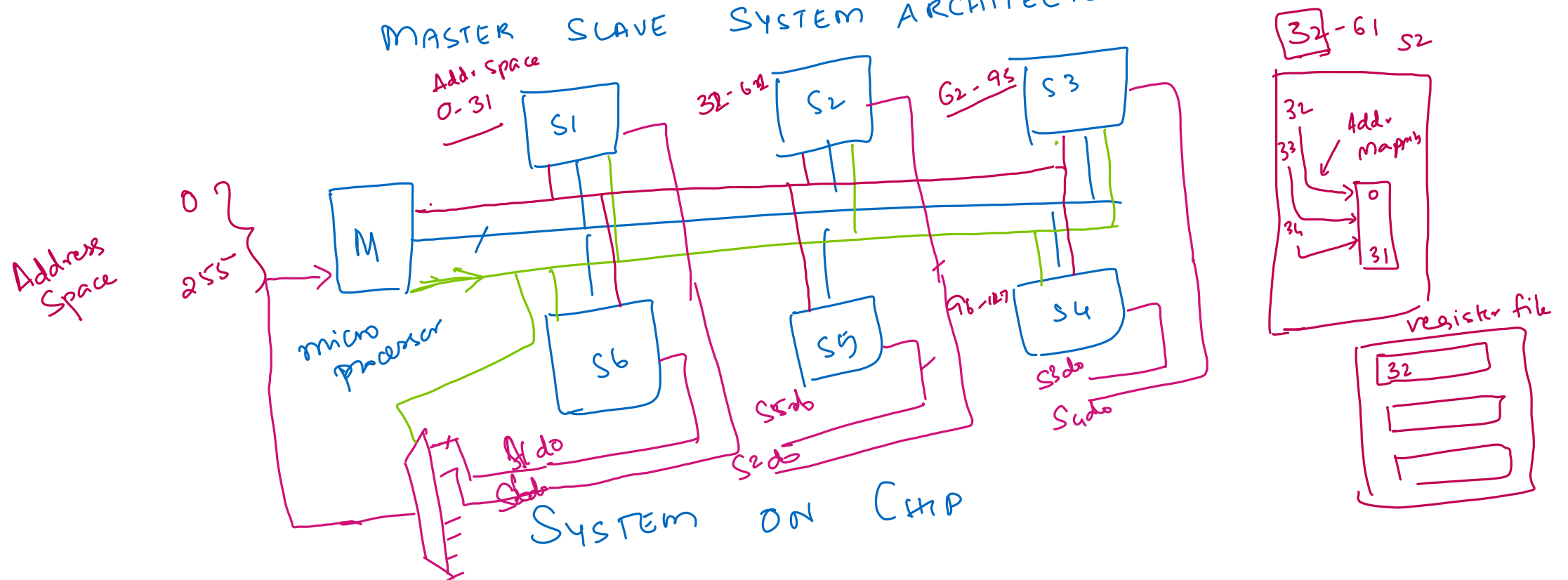


Bus Systems:

TRANSFER INFORMATION



MASTER SLAVE SYSTEM ARCHITECTURE



CLASS EXERCISE

$$(a+b)^2$$

Use only Xilinx
IP Cores

