

Micro Processor System

ISA → Instruction Set Architecture

Specification for developing microprocessor

Micro Architecture

Verilog / VHDL

Flow

MicroPROCESSOR

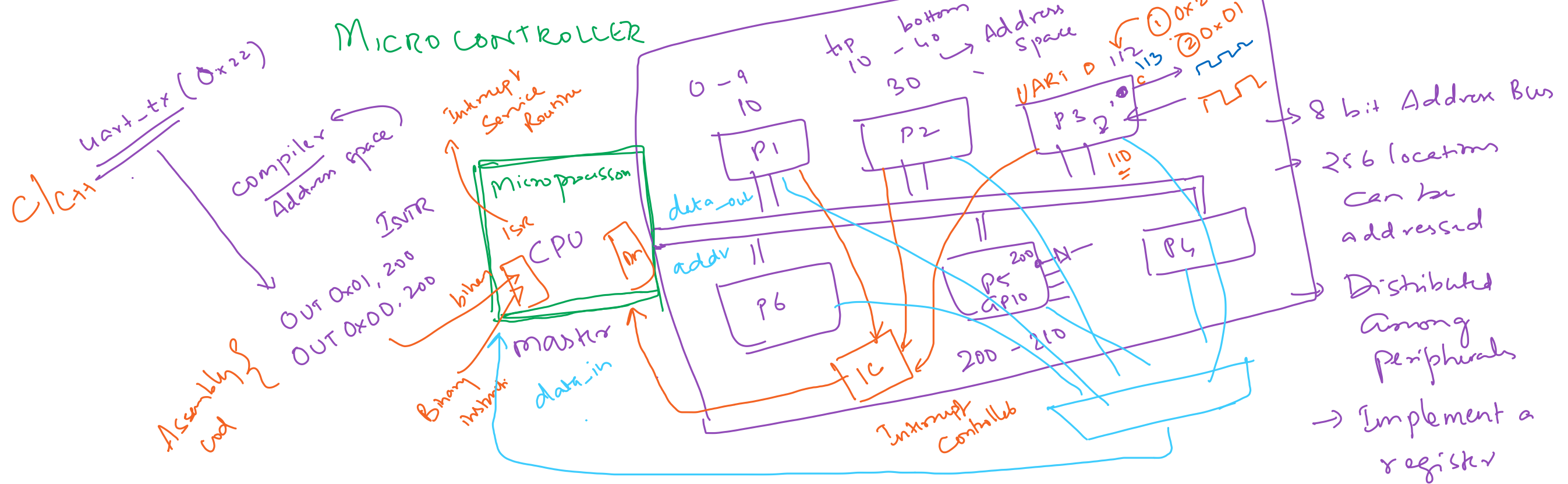
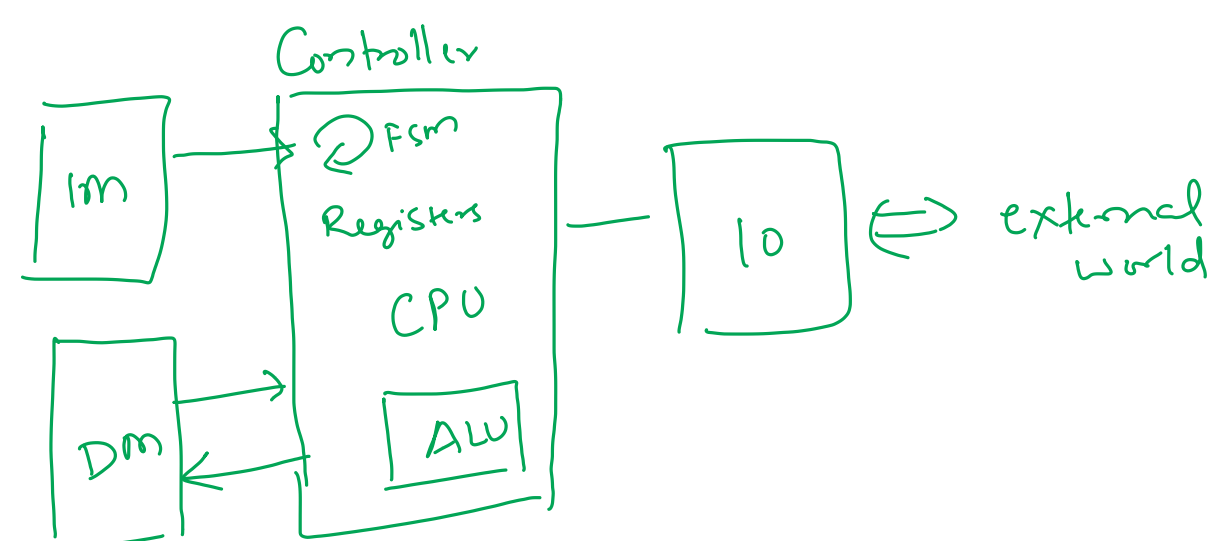
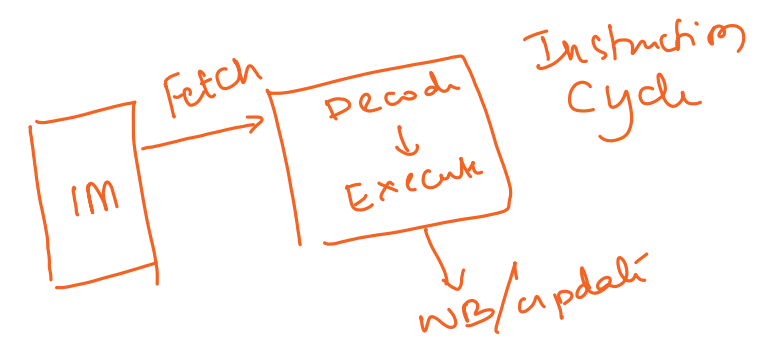
MIPS

VLIW

RISC

Execute a program

Group of instructions intended to perform a task



The above is the general architecture of any microcontroller system (Formula)

→ By writing to a register you can move an operation in a peripheral

