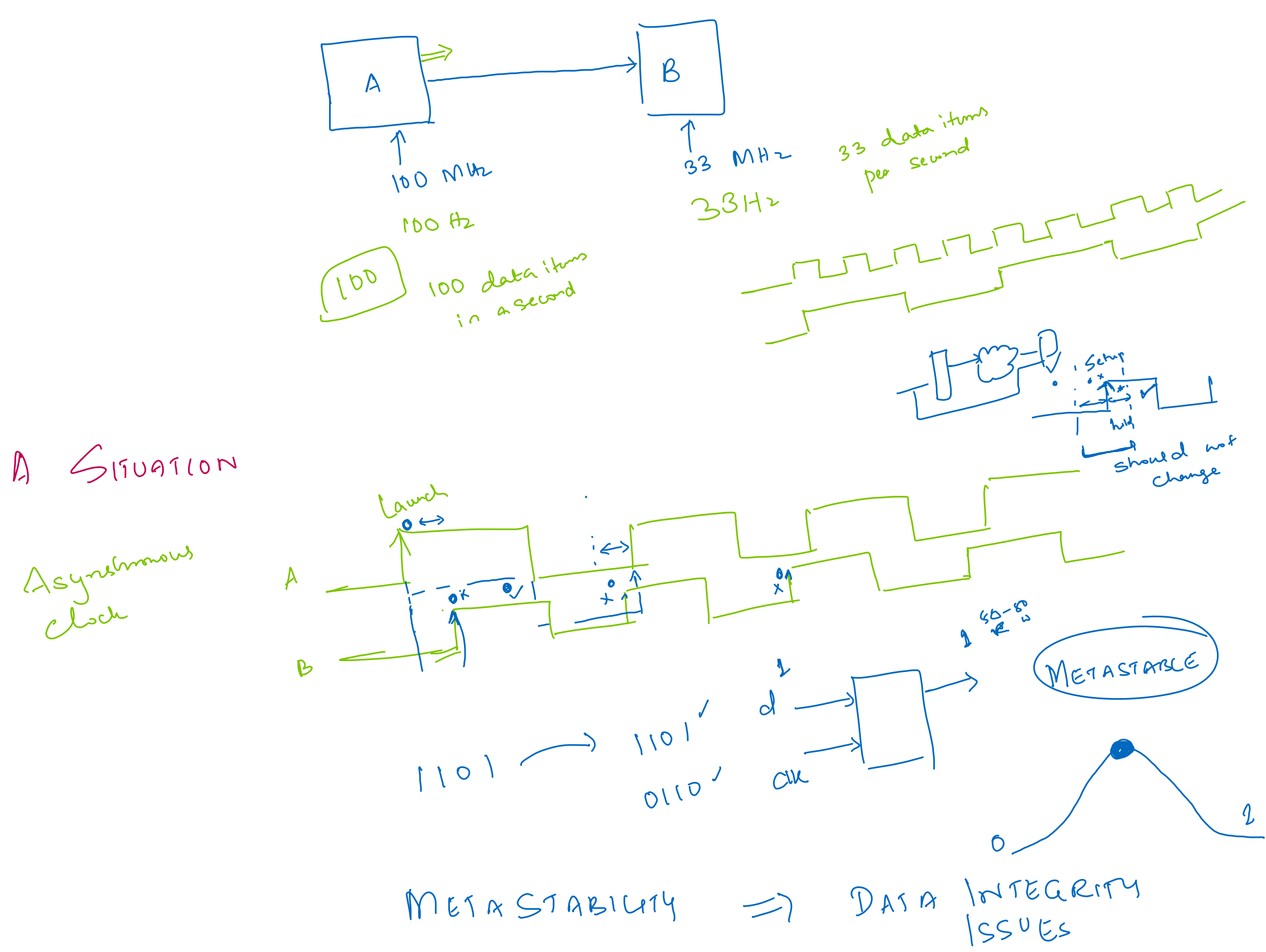
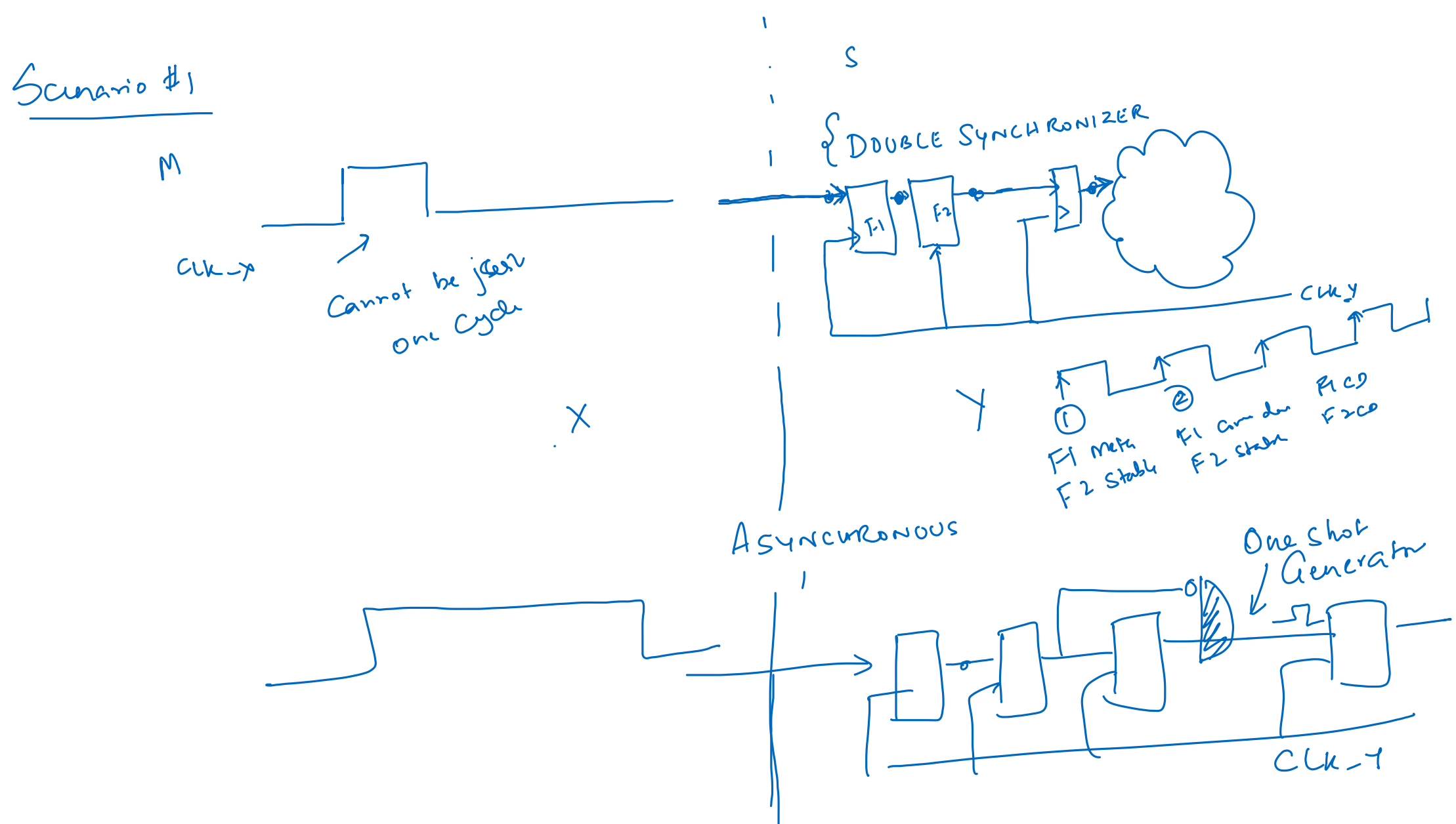


Cross Clock Domain Designs

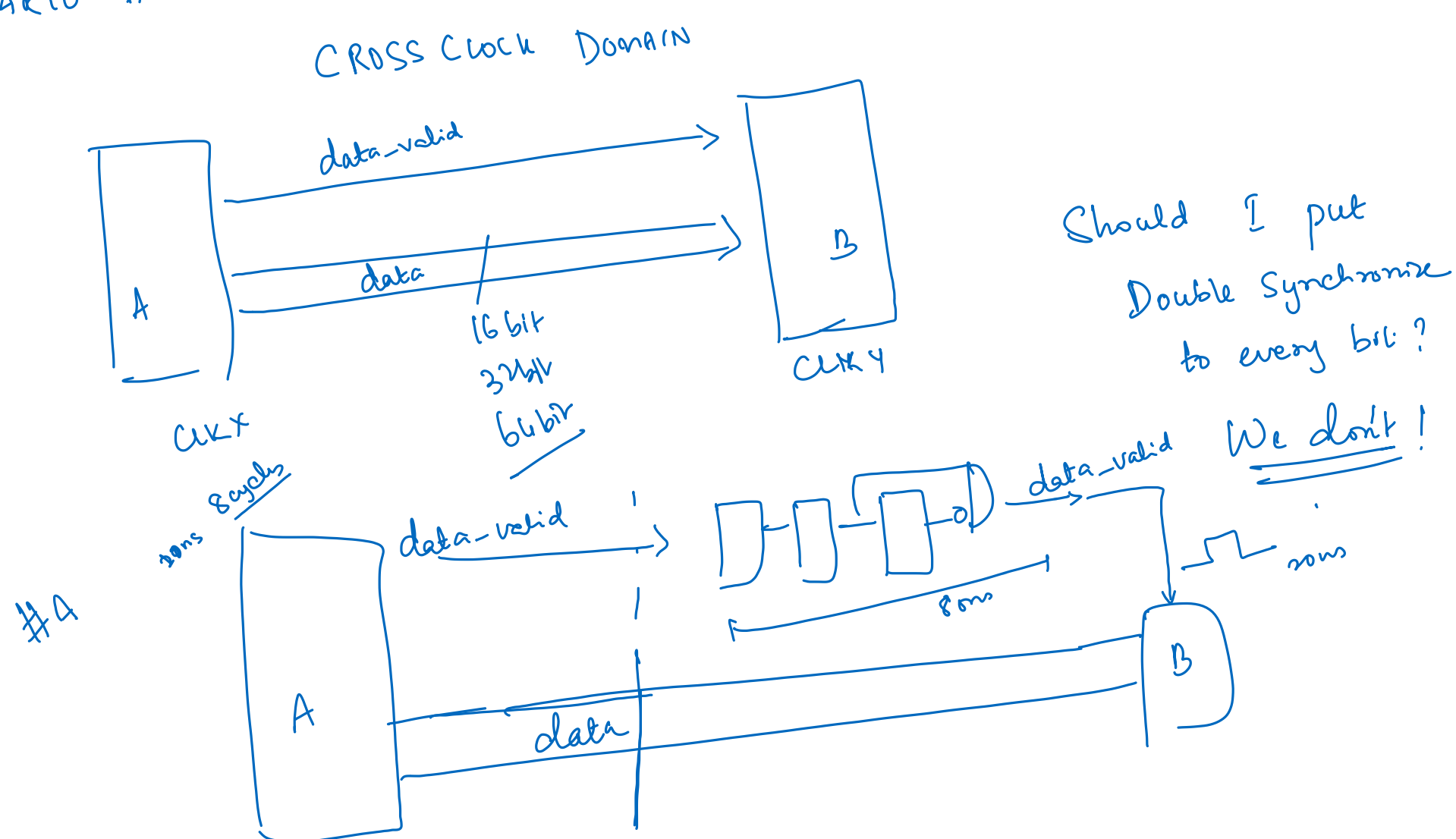


SIGNALS

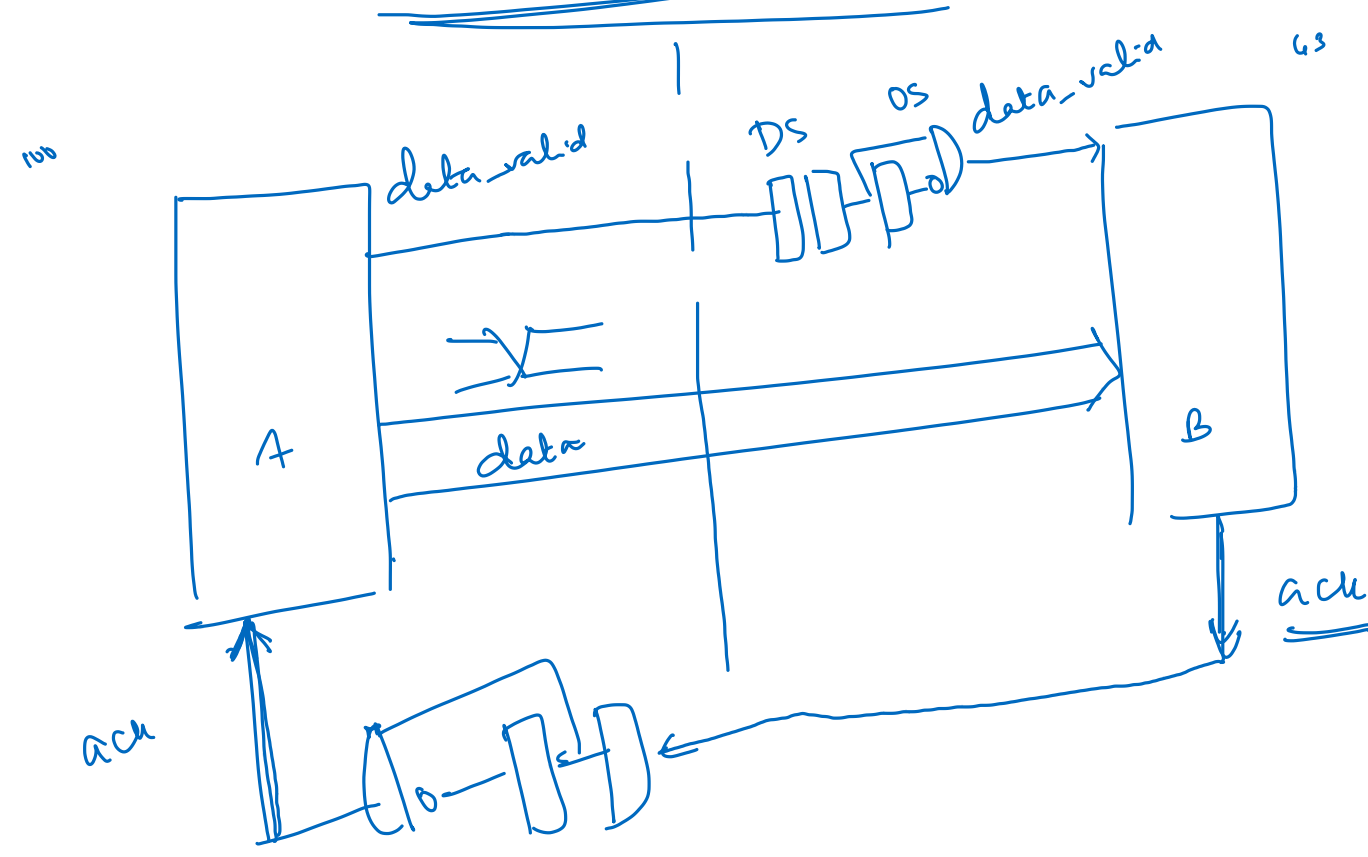
Two types $\begin{cases} \text{Control signals : } \text{rw}, \text{enw} \\ \text{Data signals : } \text{addr}, \text{data} \end{cases}$



SCENARIO #2



CREATING A HANDSHAKE SIGNAL



Scenario #3

BURST MODE TRANSACTION

