

COMMAND BYTE (HEX)	REGISTER	PROTOCOL	POWER-UP DEFAULT
0x00	CAP_Button Status	Read byte	0000 0000
0x01	CAP_Button Long press Status	Read byte	0000 0000
0x02	Mute Status	Read byte	0000 0000
0x03	FW Version	Read byte	xxxx xxxx

**Register 0 CAP\_Button status is used to monitor current CAP\_Button status,1 means CAP\_Button press and 0 means release (See Table 1.1)**

**Table 1.1** Command Byte:0x00(Register 0 CAP\_Button Status)

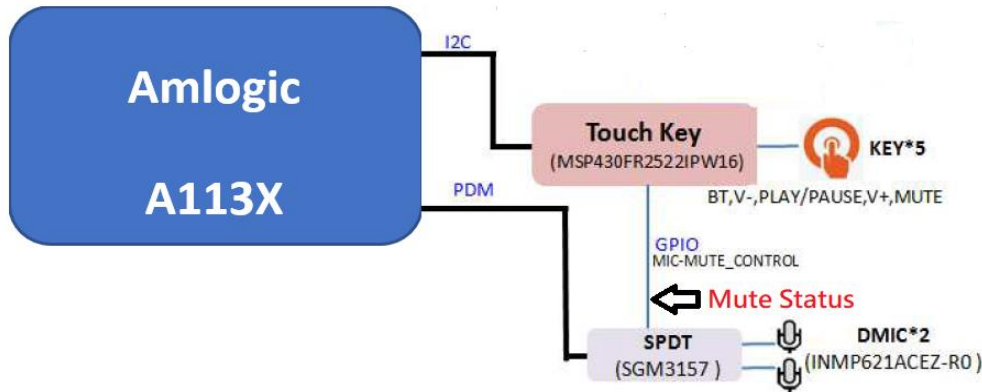
	BIT 7	BIT 6	BIT 5	BIT 4	BIT 3	BIT 2	BIT 1	BIT 0
Cap_Button Status	Reserved	Reserved	Reserved	BT Pairing 1:Press 0:Release	Volume Up 1:Press 0:Release	Play/Pause 1:Press 0:Release	Volume Down 1:Press 0:Release	Mute 1:Press 0:Release

**Register 1 CAP\_Button long press status is used to monitor CAP\_Button long press status,1 means CAP\_Button long press and 0 means release (See Table 1.2)**

**Table 1.2** Command Byte:0x01(Register 1 CAP\_Button Long press Status)

	BIT 7	BIT 6	BIT 5	BIT 4	BIT 3	BIT 2	BIT 1	BIT 0
Cap_Button long press status	Reserved	Reserved	Reserved	BT Pairing 1:Long Press 0:Release	Volume Up 1:Long Press 0:Release	Play/Pause 1:Long Press 0:Release	Volume Down 1:Long Press 0:Release	Mute 1:Long Press 0:Release

**Register 2 MIC mute Status is used to represent MIC physical circuit is enable or disable (See Table 1.3)**



**Table 1.3** Command Byte:0x02(Register 2 MIC mute Status)

	BIT 7	BIT 6	BIT 5	BIT 4	BIT 3	BIT 2	BIT 1	BIT 0
Mute Status	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	Reserved	1:Mute Enable 0:Mute Disable

## I2C Read pattern:

I2C slave device address (7 bits) : 0x1F

I2C bus max speed : 400 kbit/s

