FYDP – ANALOG MATRIX MULTIPLIER
UNIVERSITY OF WATERLOO

BLOCK DIAGRAM
File: Block_Diagran.kicad_sch

USBC RECEPTACLE
File: USBC_Receptacle.kicad_sch

POWER
File: power.kicad_sch

DAC INPUTS
File: DAC Inputs.kicad_sch

DECODER 1
File: Decoder 1.kicad_sch

DECODER 2
File: Decoder 2.kicad_sch

ANALOG MULTIPLIER 1
File: Analog Multiplier 1.kicad_sch

ANALOG MULTIPLIER 2
File: Analog Multiplier 3.kicad_sch

ANALOG MULTIPLIER 3
File: Analog Multiplier 5.kicad_sch

ANALOG MULTIPLIER 4
File: Analog Multiplier 7.kicad_sch

ADC
File: ADC.kicad_sch

Sheet: / File: fydp.kicad_sch		
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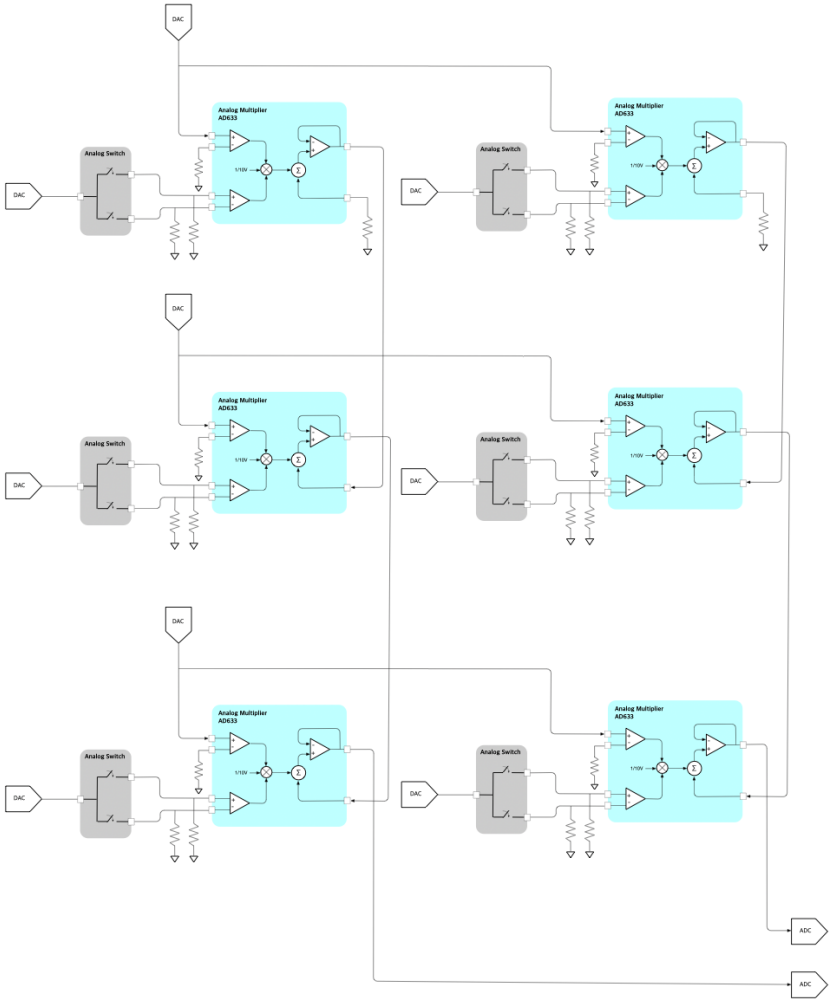
BLOCK DIAGRAM

BASIC BLOCK DIAGRAM FOR A 3X2 MATRIX.
THIS PROJECT USES THE SAME IDEA AS THIS BLOCK DIAGRAM BUT EXPANDS IT TO A 4X2 CASE.



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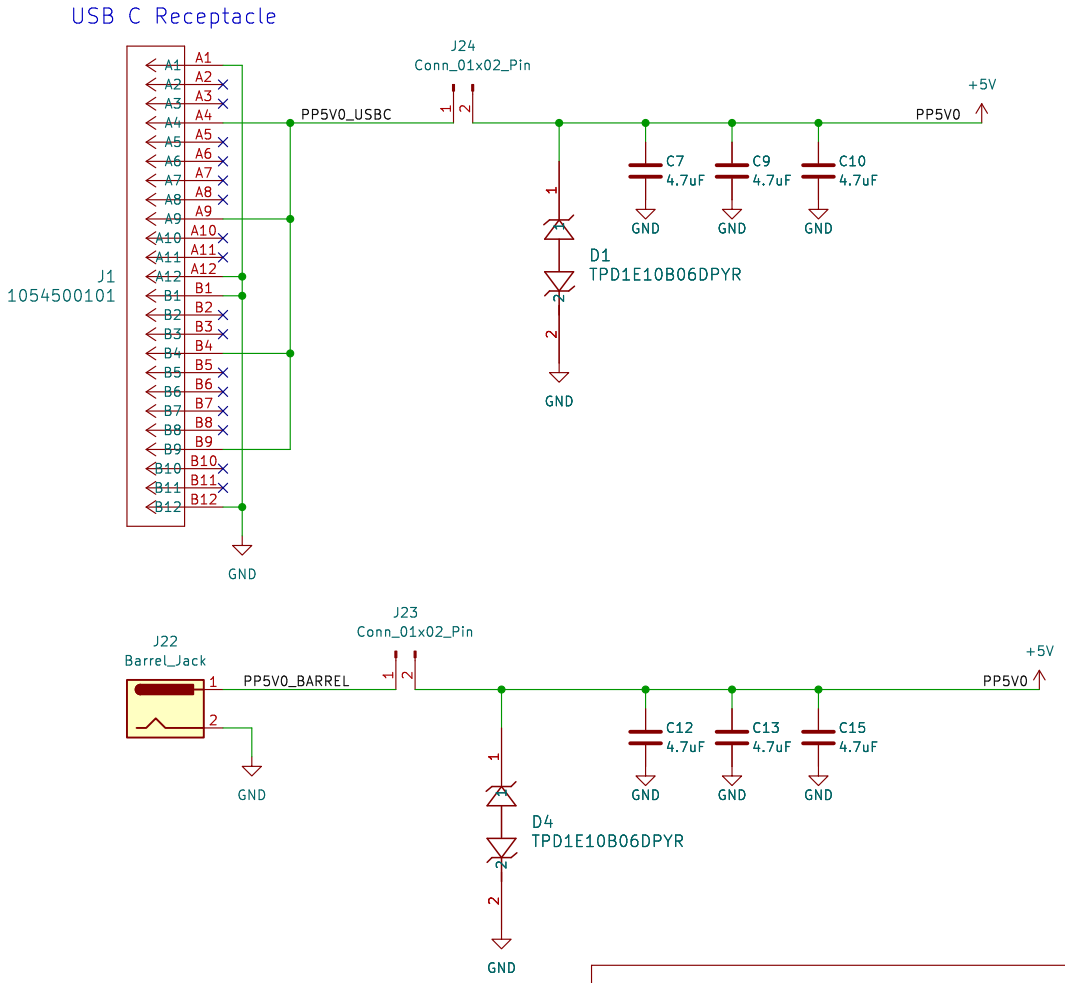
USB C Receptacle



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File: USBC_Receptacle.kicad_sch

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Size: A4	Date:	**Rev:**
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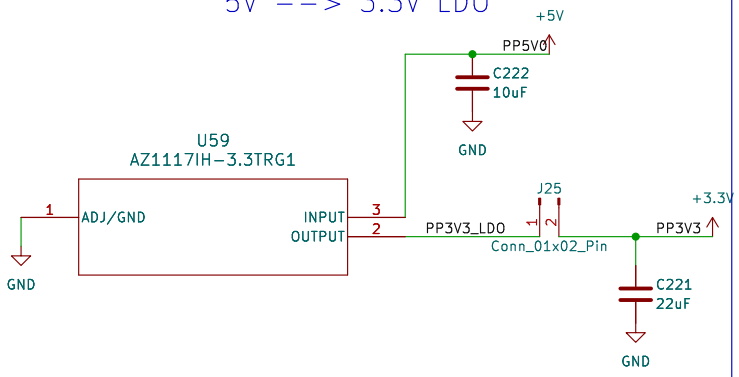
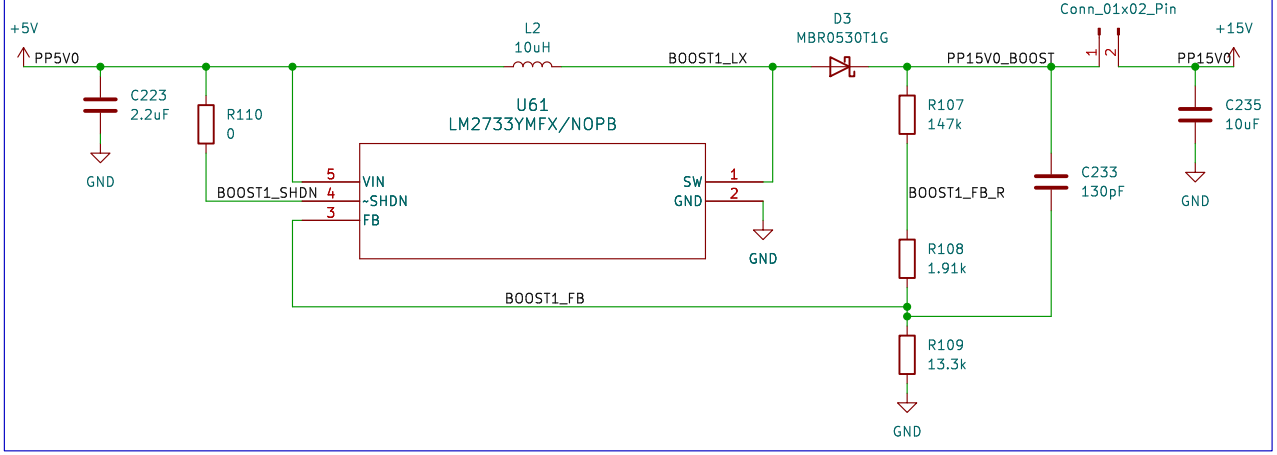
POWER

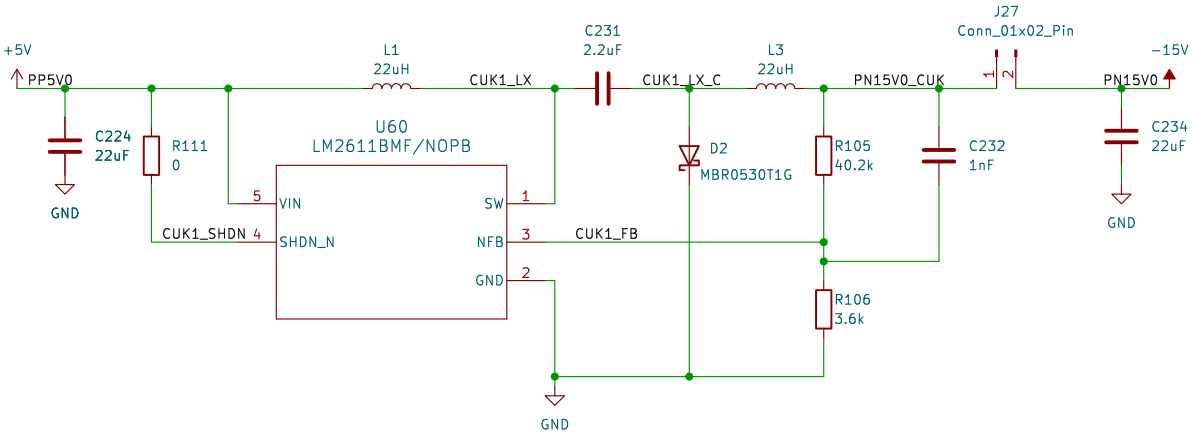
5V --> 3.3V LDO



5V --> 15V Boost Converter



5V --> -15V Cuk Converter



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Title:		
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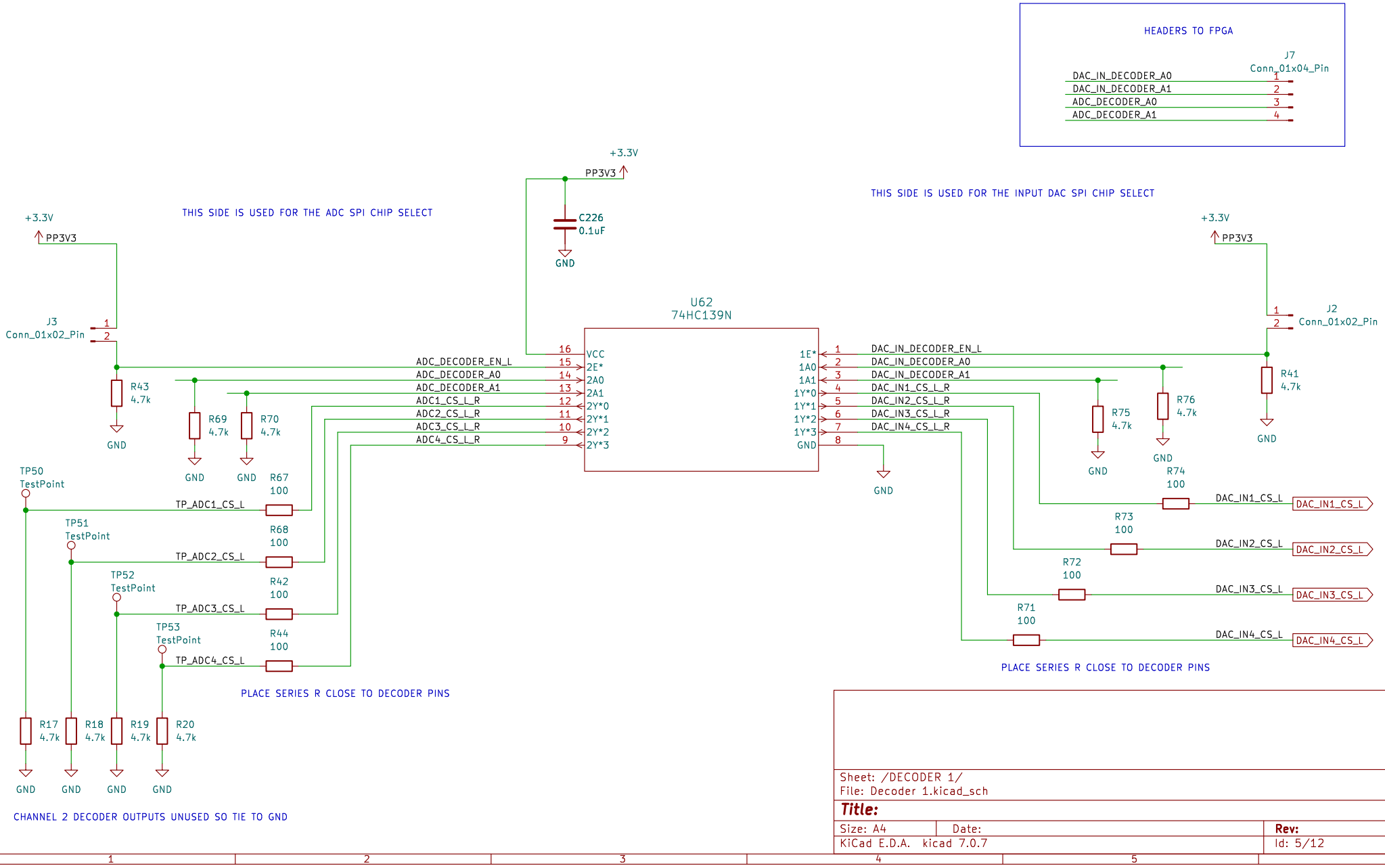
DAC INPUTS DECODER & ADC OUTPUT DECODER (UNUSED)



Sheet: /DECODER 1/		
File: Decoder 1.kicad_sch		
Title:		
Size: A4	Date:	**Rev:**
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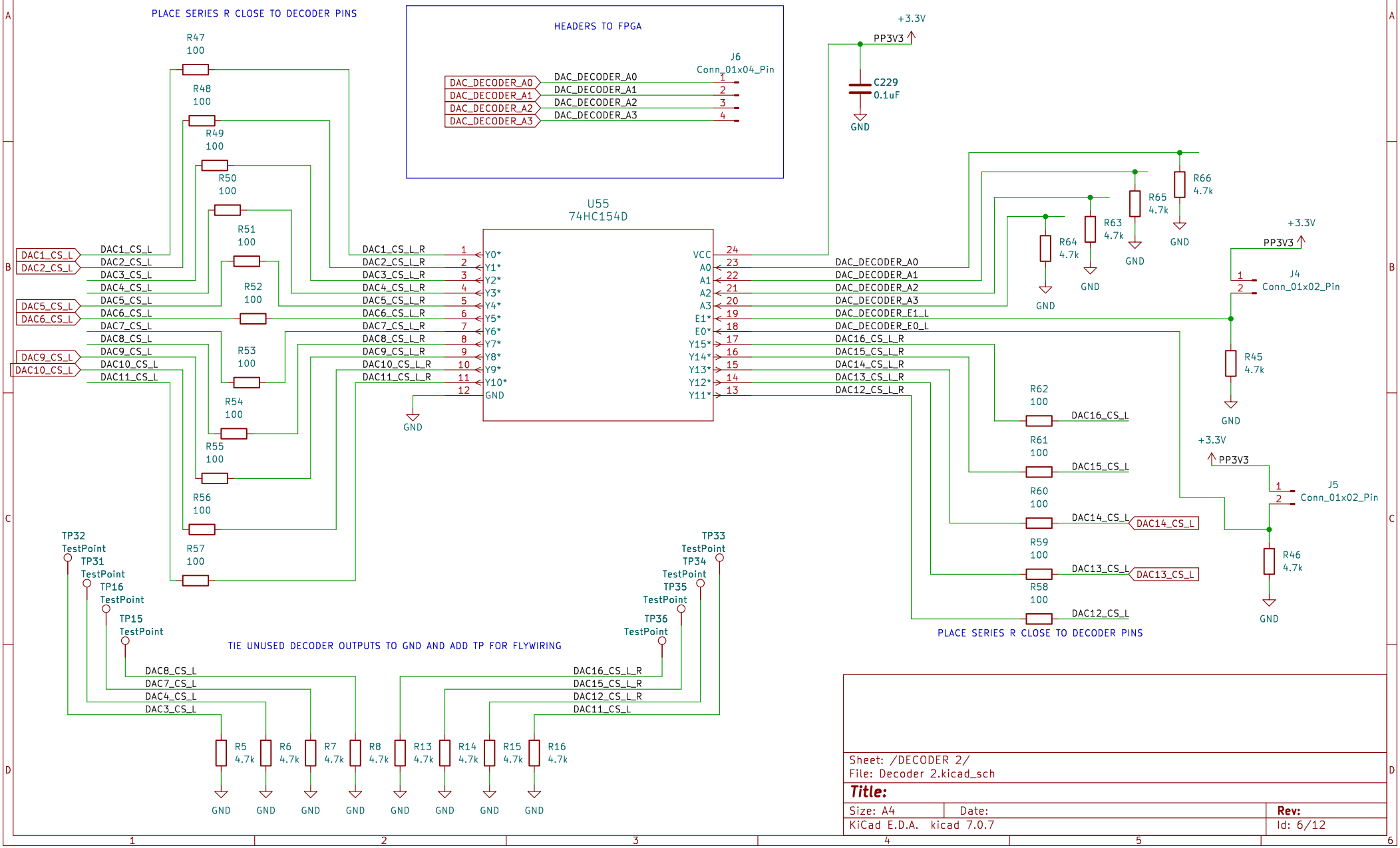
DAC WEIGHTS DECODER



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File: Decoder 2.kicad_sch		
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Size: A4	Date:	**Rev:**
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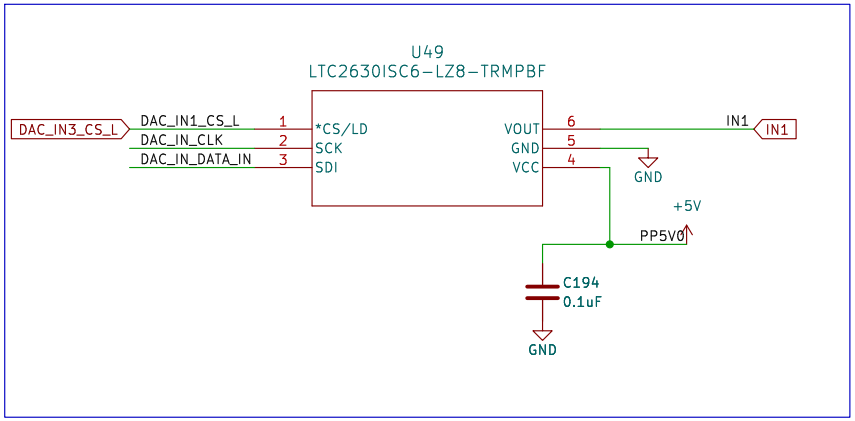
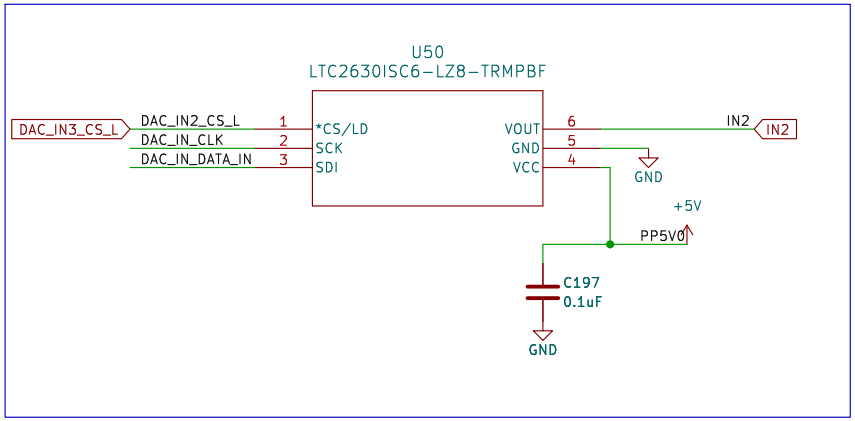
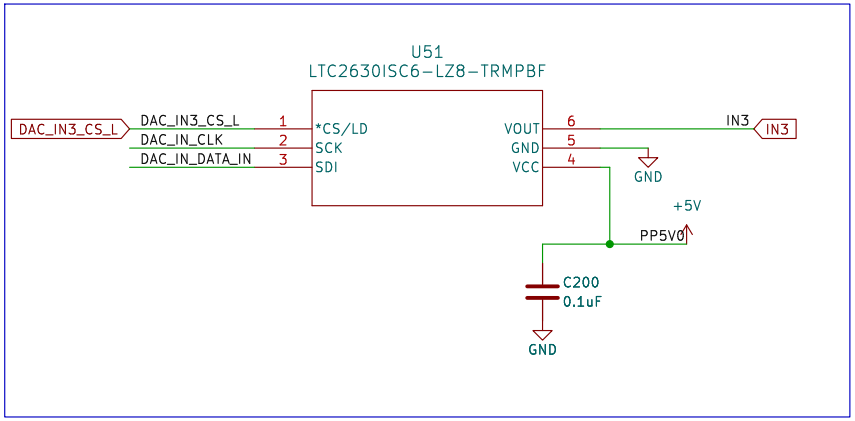
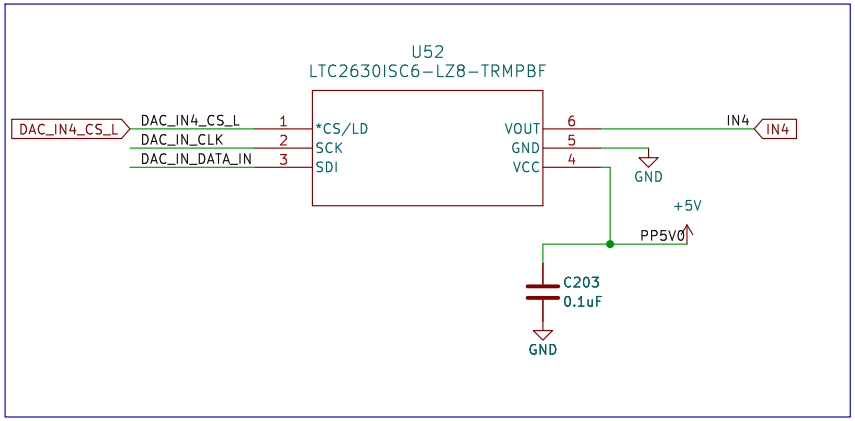
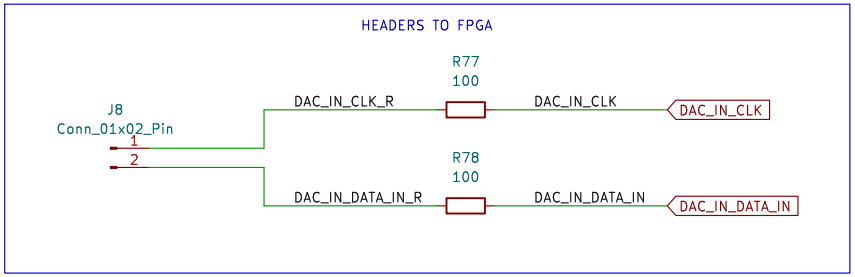
DAC INPUTS











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File: DAC Inputs.kicad_sch

Title:

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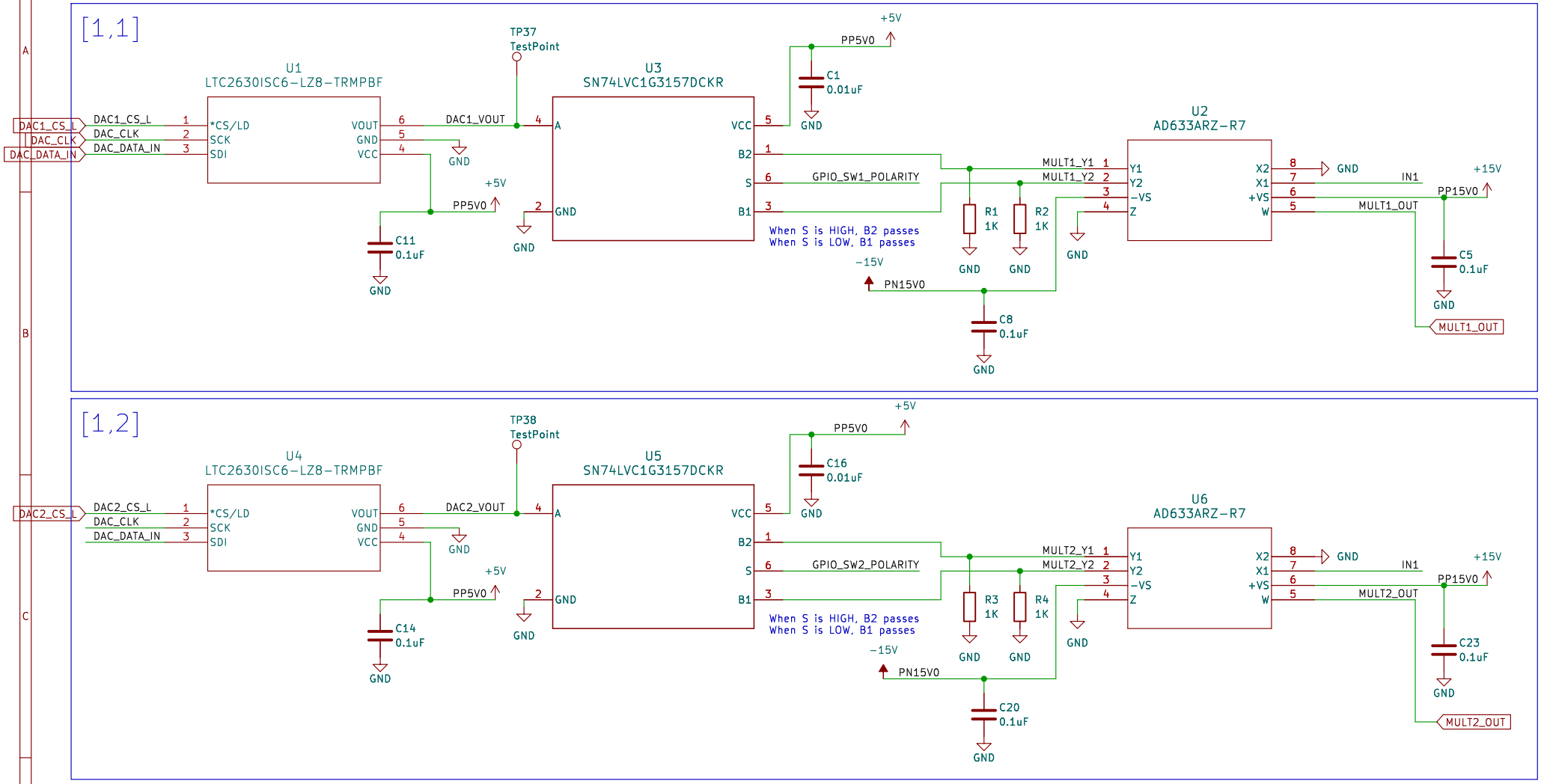
ANALOG MULTIPLIER ([1,1] & [1,2])

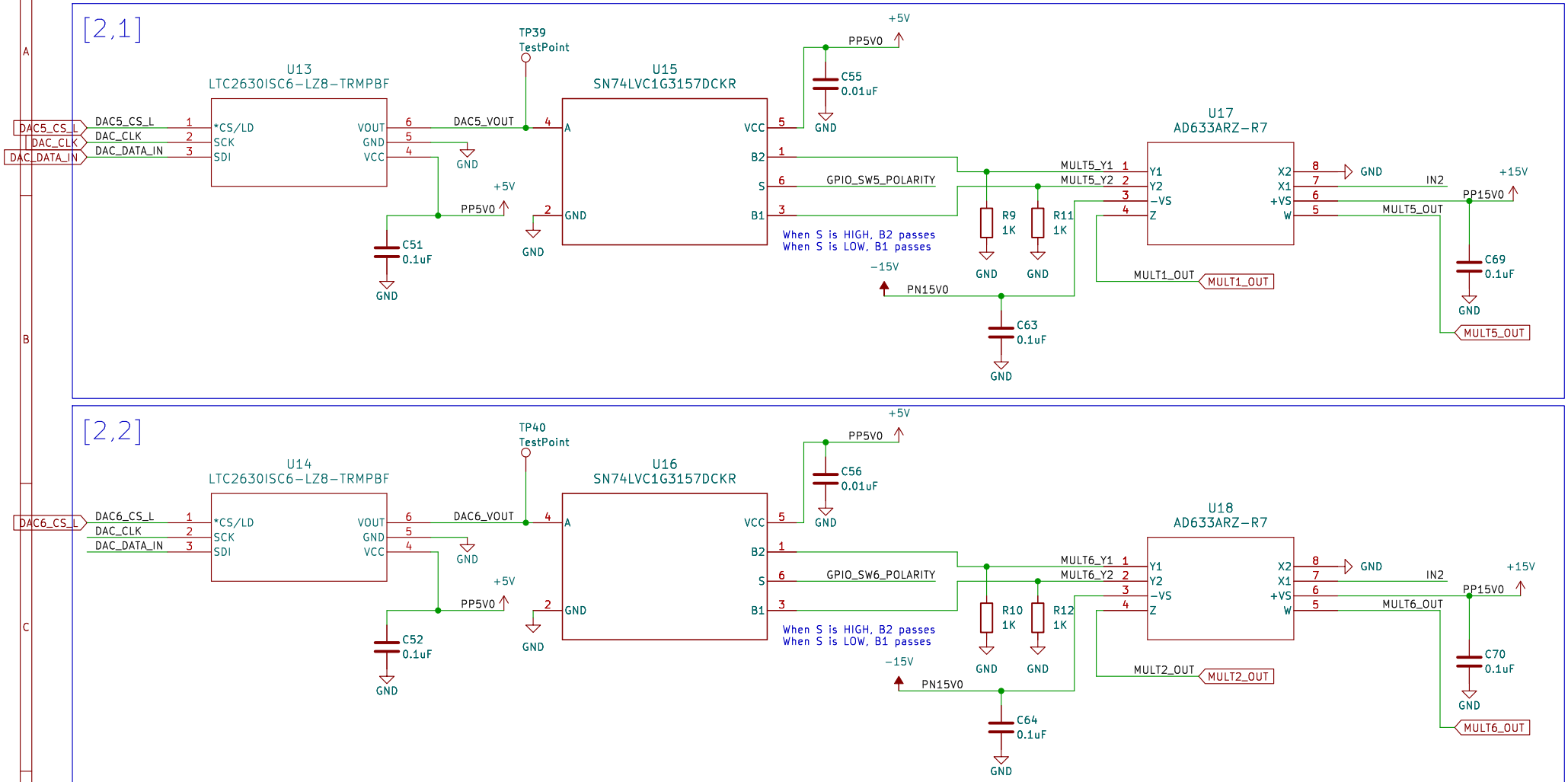


Sheet: /ANALOG MULTIPLIER 1/
File: Analog Multiplier 1.kicad_sch

Title:

Size: A4	Date:	**Rev:**
KiCad E.D.A. kicad 7.0.7		Id: 8/12

ANALOG MULTIPLIER (([2,1] & [2,2]))



Sheet: /ANALOG MULTIPLIER 2/
File: Analog Multiplier 3.kicad_sch

Title:

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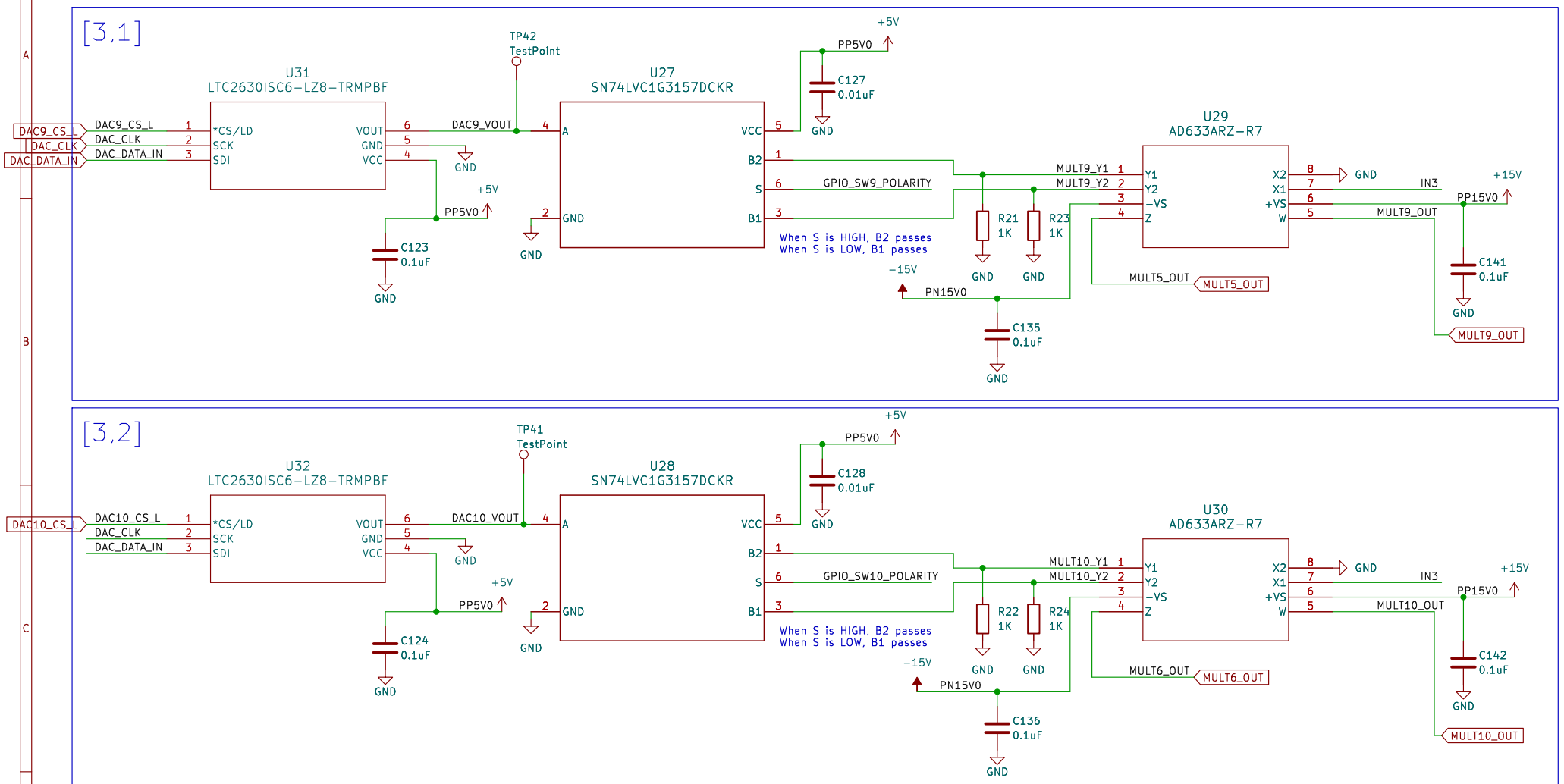
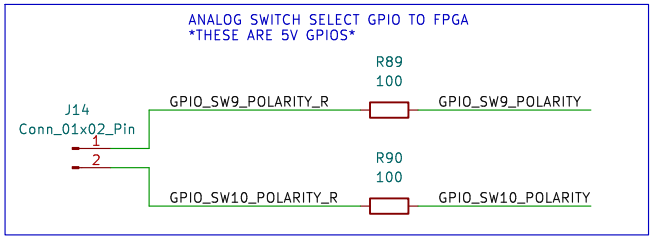
KiCad E.D.A. kicad 7.0.7		Id: 9/12

ANALOG MULTIPLIER (([3,1] & [3,2]))



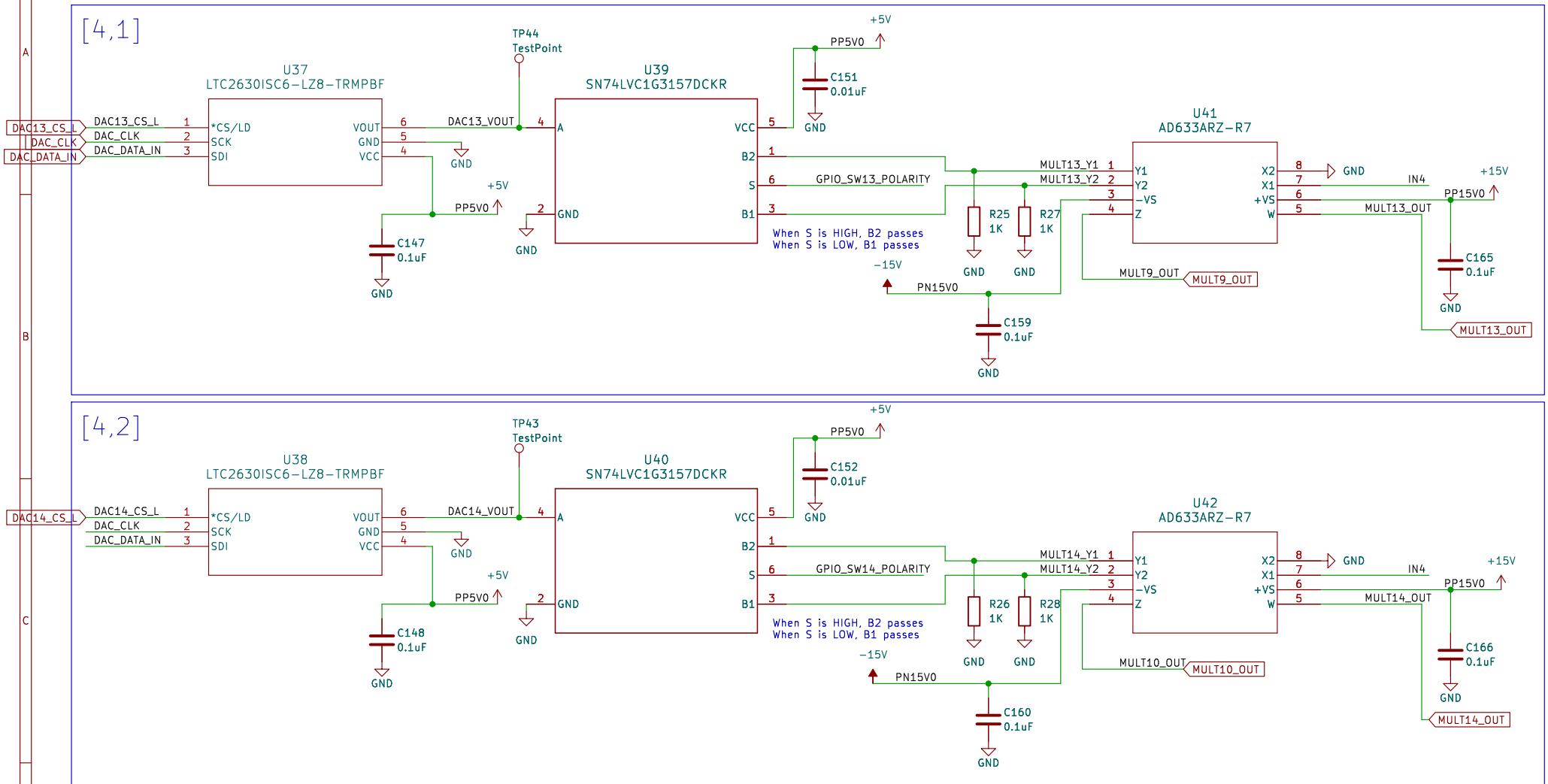


Sheet: /ANALOG MULTIPLIER 3/
File: Analog Multiplier 5.kicad_sch

Title:

Size: A4	Date:	**Rev:**
KiCad E.D.A. kicad 7.0.7		Id: 10/12

ANALOG MULTIPLIER (([4,1] & [4,2]))

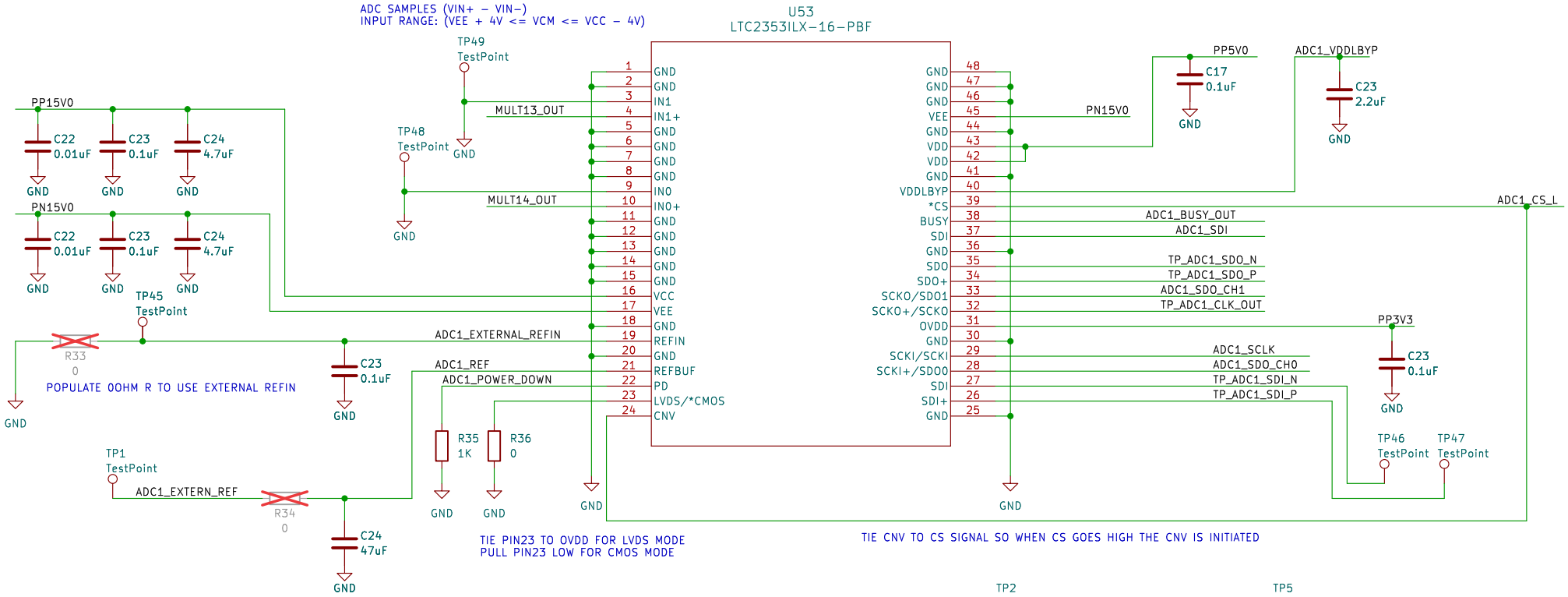


Sheet: /ANALOG MULTIPLIER 4/
File: Analog Multiplier 7.kicad_sch

Title:

Size: A4	Date:	**Rev:**
KiCad E.D.A. kicad 7.0.7		Id: 11/12

ADC – 16-BIT DUAL CHANNEL DIFFERENTIAL



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