FYDP — ANALOG MATRIX MULTIPLIER UNIVERSITY OF WATERLOO

BLOCK DIAGRAM

File: Block_Diagran.kicad_sch

USBC RECEPTACLE

File: USBC_Receptacle.kicad_sch

POWER

File: power.kicad_sch

DAC INPUTS

File: DAC Inputs.kicad_sch

DECODER 1

File: Decoder 1.kicad_sch

DECODER 2

File: Decoder 2.kicad_sch

ANALOG MULTIPLIER 1

File: Analog Multiplier 1.kicad_sch

ANALOG MULTIPLIER 2

File: Analog Multiplier 3.kicad_sch

ANALOG MULTIPLIER 3

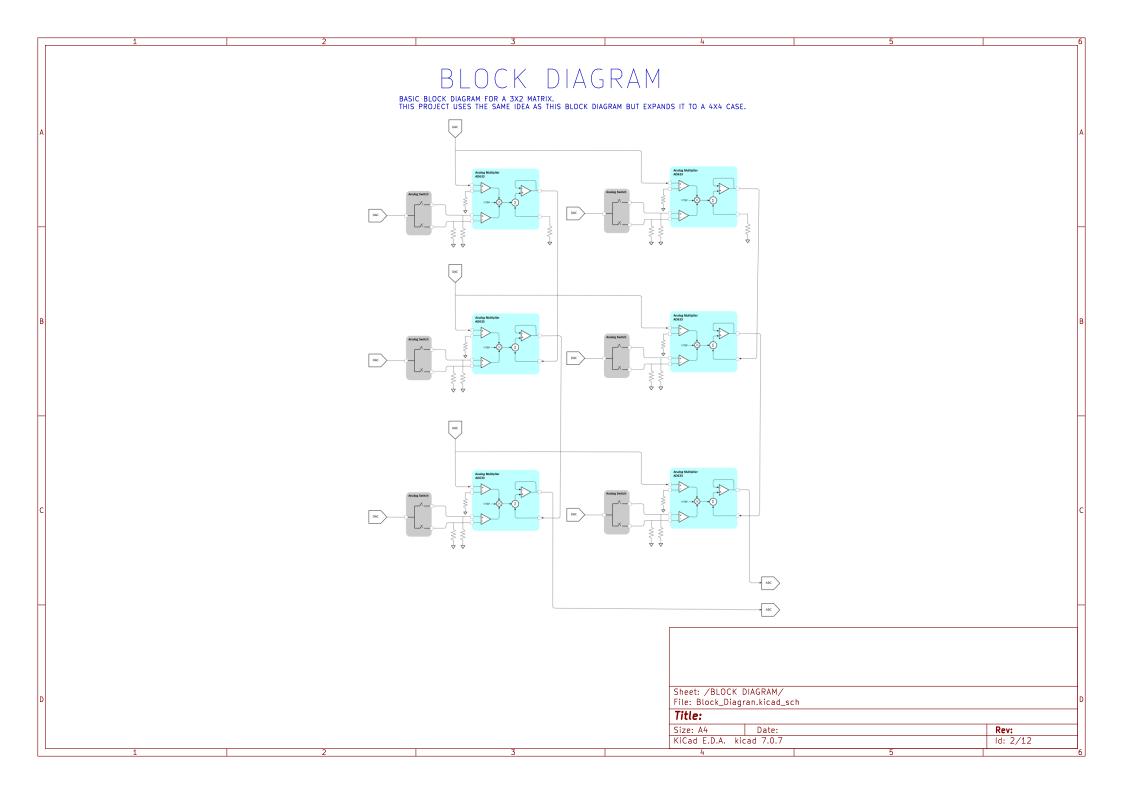
File: Analog Multiplier 5.kicad_sch

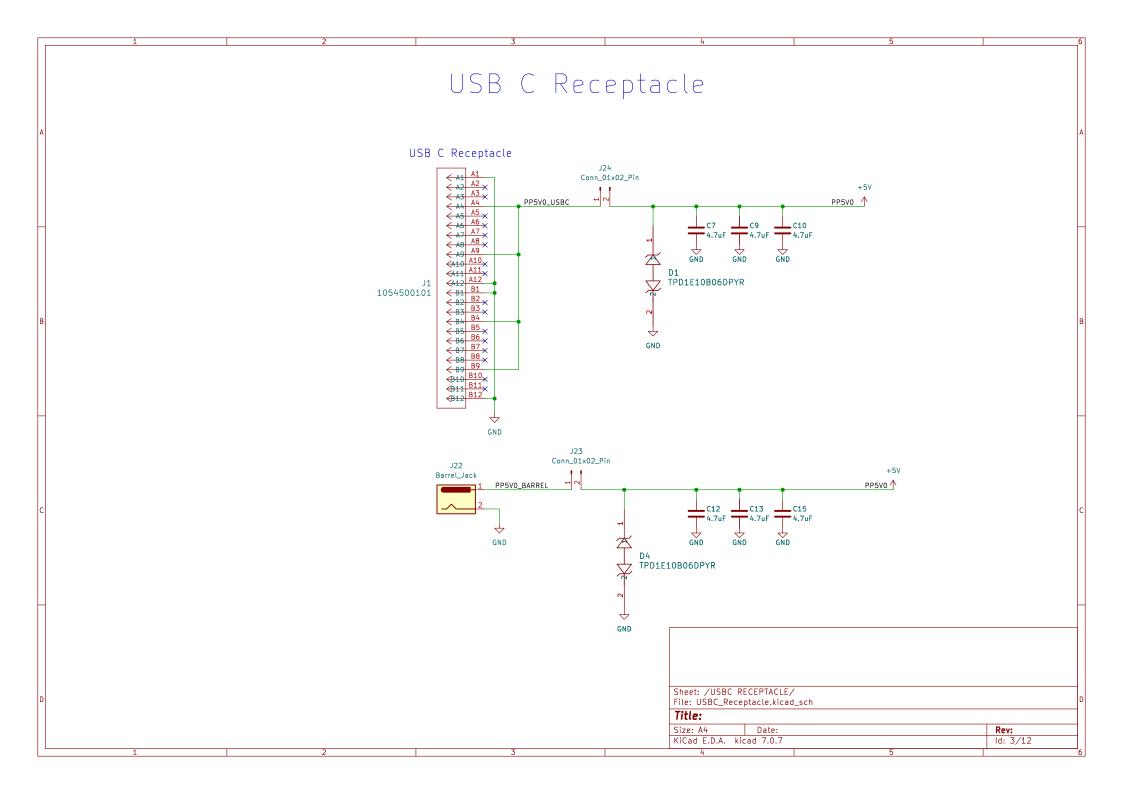
ANALOG MULTIPLIER 4

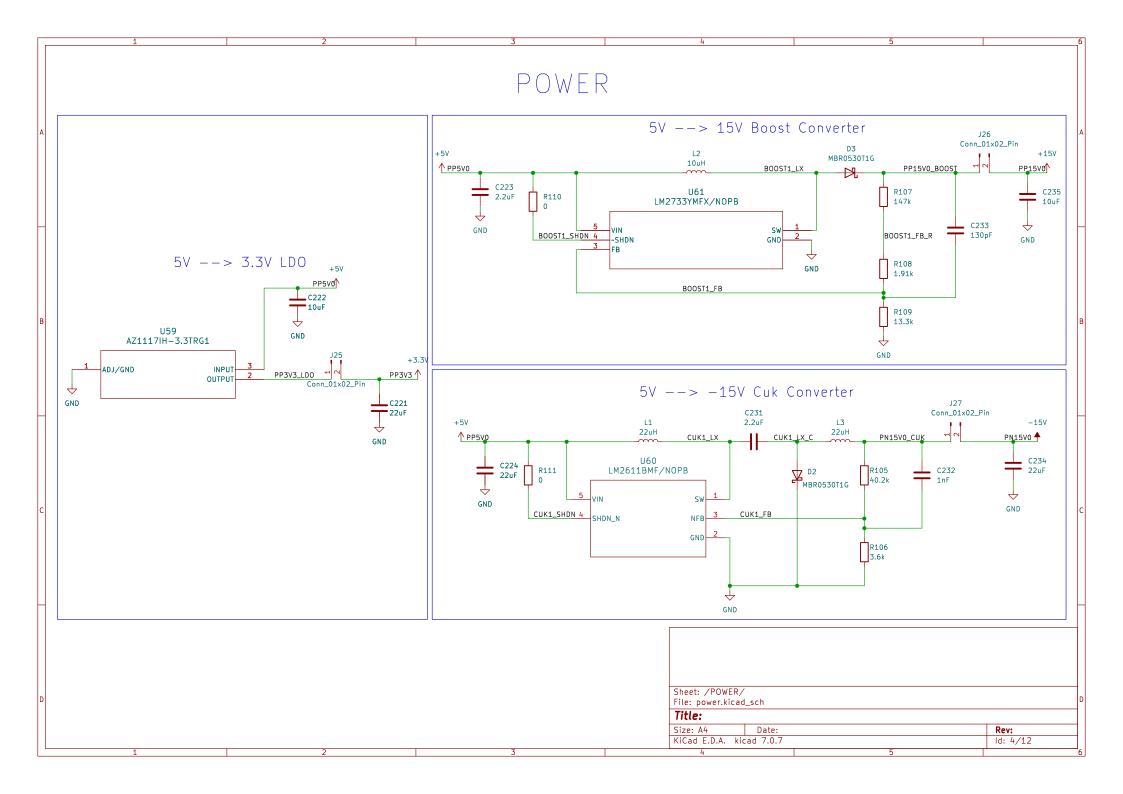
File: Analog Multiplier 7.kicad_sch

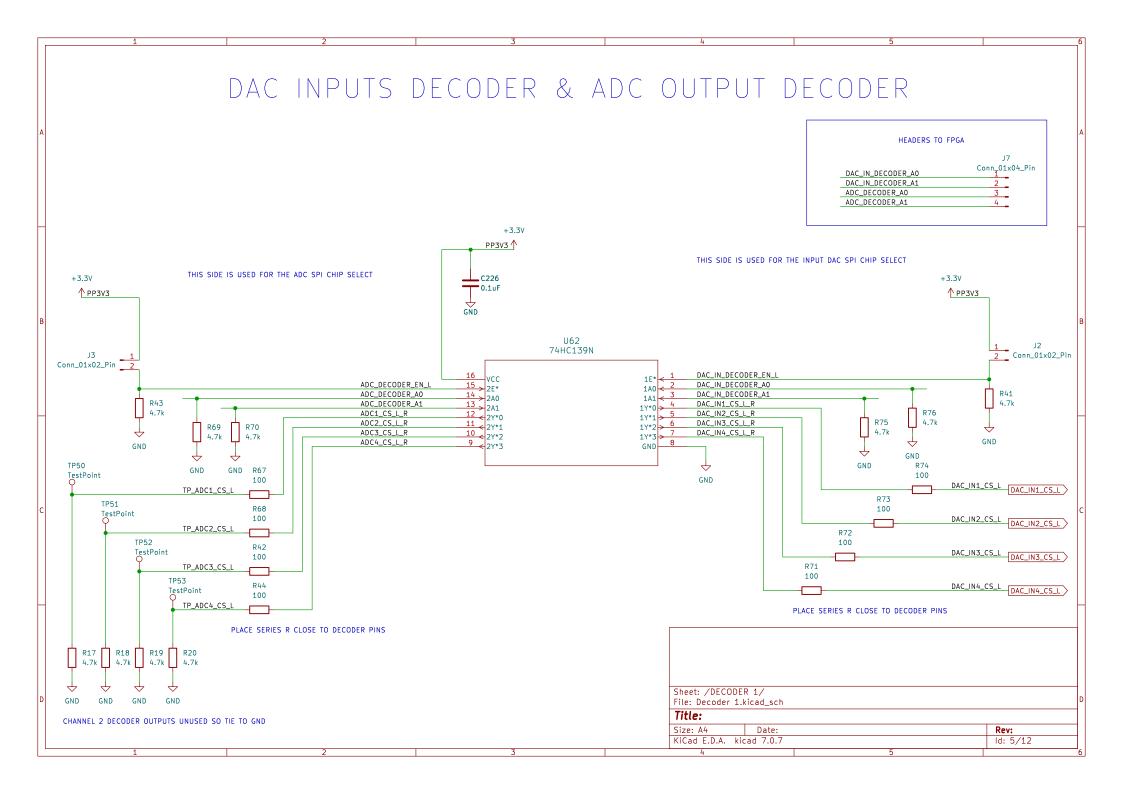
ADC

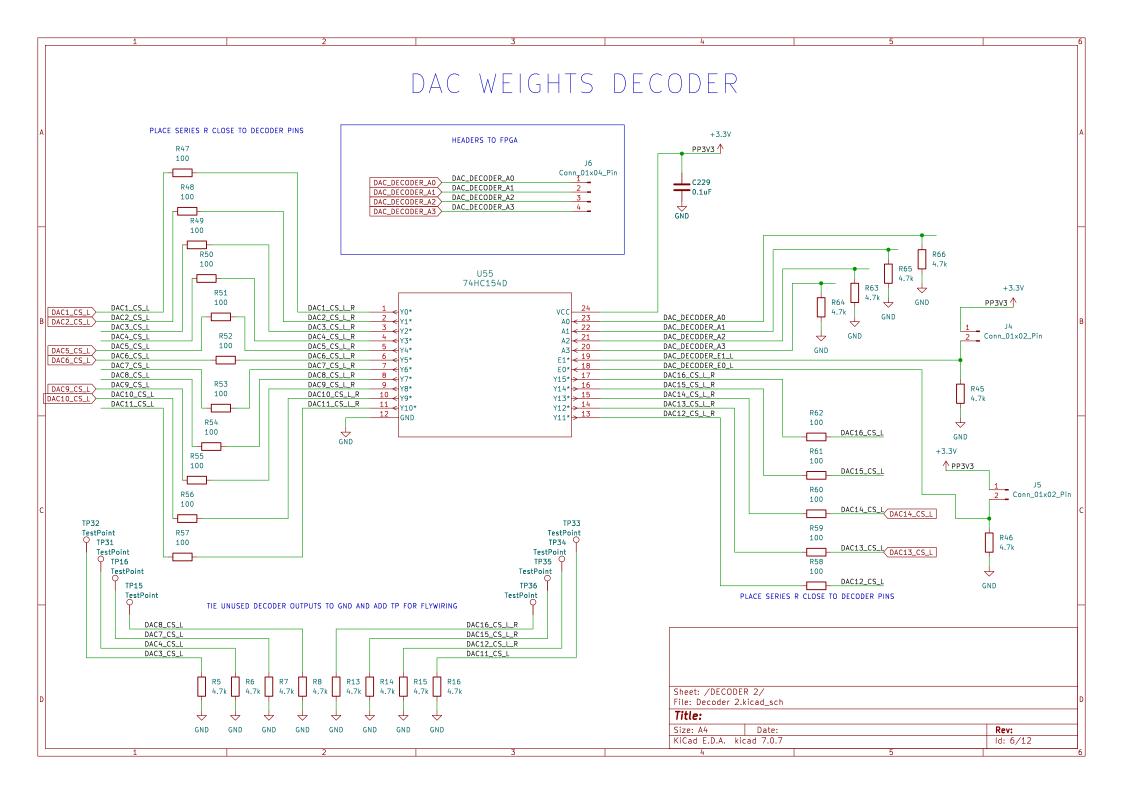
File: ADC.kicad_sch



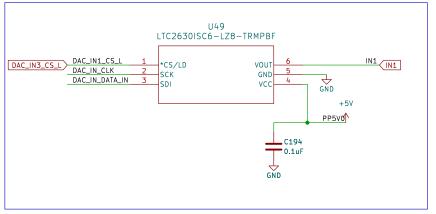


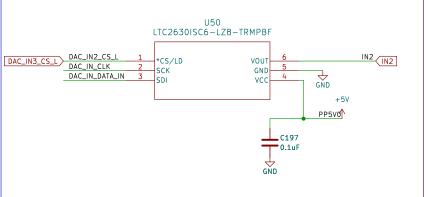


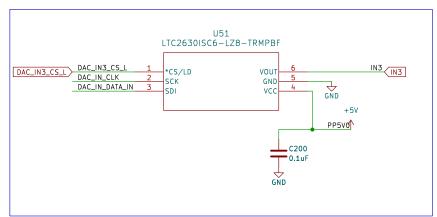


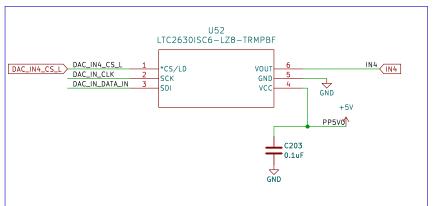


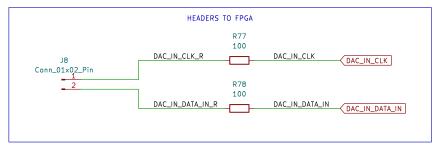
DAC INPUTS











T*11 -	1
File: DAC Inputs.kicad_sch	D
Sheet: /DAC INPUTS/	
	ш

File: DAC Inputs.kicad_sch				
Title:				
Size: A4	Date:		Rev:	
KiCad E.D.A. kicad 7.0.7			ld: 7/12	

