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Question 11

Not yet answered

Marked out of 1.00

A page fault occurs when

- ☐ a. the Segmentation starts
- ☐ b. the Deadlock happens
- ☐ c. the page is found in the memory
- ☐ d. the page is not found in the memory

Question 12

Not yet answered

Marked out of 1.00

Operating System maintains the page table for

- ☐ a. each address
- ☐ b. each thread
- ☐ c. each instruction
- ☐ d. each process

Question 13

Not yet answered

Marked out of 1.00

A computer with 32 bits virtual address uses a two-level page table. Virtual addresses are split into a 9 bits top-level page table field, a 10 bits second-level page table field, and an offset. How many are there in the address space?

- ☐ a. 12 bits
- ☐ b. 13 bits
- ☐ c. 11 bits
- ☐ d. 10 bits

Question 14

Not yet answered

Marked out of 1.00

:24::In which type of allocation method each file occupies a set of contiguous blocks on the disk?

- ☐ a. contiguous allocation
- ☐ b. dynamic-storage allocation
- ☐ c. indexed allocation
- ☐ d. linked allocation

Question 15

Not yet answered

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Consider that a TLB (Translation Lookaside Buffer) is given by the following table.

Valid	Virtual page	Modified	Protection	Page frame number
1	144	1	RW	35
1	25	0	R X	45
1	130	1	RW	52
1	129	1	RW	55
1	29	0	R X	44
1	30	0	R X	33
1	890	1	RW	75
1	891	1	RW	15

What will be happened if the instruction is trying to write on the virtual page number 130th?

- ☐ a. A protection fault is generated
- ☐ b. The page frame 52 will be referenced
- ☐ c. A page fault occurs
- ☐ d. The page frame 52 will be modified

Question **16**

Not yet answered

Marked out of 1.00

A process refers to 5 pages, A, B, C, D, E in the order : A, B, C, D, A, B, E, A, B, C, D, E. If the page replacement algorithm is FIFO, the number of page transfers with an empty internal store of 3 frames is?

- ☐ a. 10
- ☐ b. 7
- ☐ c. 8
- ☐ d. 9

Question **17**

Not yet answered

Marked out of 1.00

Purpose of DMA (Direct Memory Access) controller applying for block devices to ____

- ☐ a. increase Input/Output speed .
- ☐ b. connect the CPU, memory, and disk devices.
- ☐ c. do not waste the CPU's time transfer data from/to disk and memory.
- ☐ d. connect the CPU, memory, and the I/O devices.

Question **18**

Not yet answered

Marked out of 1.00

The request and release of resources are _____

- ☐ a. command line statements
- ☐ b. interrupts
- ☐ c. system calls
- ☐ d. special programs

Question **19**

Not yet answered

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Fixed partitions

- ☐ a. are very common in current operating systems
- ☐ b. are most used on large mainframe operating systems
- ☐ c. are very inefficient in memory utilization
- ☐ d. are very efficient in memory utilization

Question **20**

Not yet answered

Marked out of 1.00

A system with 5 processes P0 through P4 and three resource types A, B, C has A with 10 instances, B with 5 instances, and C with 7 instances. At time t0, the following snapshot has been take :

Process

P0

P1

P2

P3

P4

Allocation (process-wise : P0 through P4 top T0 bottom)

A	B	C
0	1	0
2	0	0
3	0	2
2	1	1
0	0	2

MAX (process-wise : P0 through P4 top T0 bottom)

A	B	C
7	5	3
3	2	2
9	0	2
2	2	2
4	3	3

Available

A	B	C
3	3	2

The sequence <P1, P3, P4, P2, P0> leads the system to :

- ☐ a. a protected state
- ☐ b. a deadlock
- ☐ c. an unsafe state
- ☐ d. a safe state

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