

VIETNAM NATIONAL UNIVERSITY, HO CHI MINH CITY
UNIVERSITY OF TECHNOLOGY



OPERATING SYSTEMS

Assignment

Simple Operating System

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1 Scheduler

1.1 Question - Priority Feedback Queue

• **Question:** What is the advantage of using priority feedback queue in comparison with other scheduling algorithms you have learned?

In operating systems course, there are 7 scheduling algorithms that students have learned.

- First-Come, First-Served(FCFS) Scheduling
- Shortest-Job-First (SJF) Scheduling
- Shortest-Remaining-Time-First (SRTF)
- Round Robin (RR) Scheduling
- Priority Scheduling
- Multilevel Queue
- Multilevel Feedback Queue

Compared to those algorithms above, there are some advantages of using Priority Feedback Queue:

- Since the CPU runs processes in round-robin style, each process is allowed to run up to a given period of time. Therefore, every process gets an equal share of the CPU.
- Starvation could be prevented. Once the process has finished its given time in run_queue and move to ready_queue, it must wait all process in run_queue finish their turn before moving back to run_queue.

1.2 Result of Gantt Diagrams

• **Requirement:** Draw Gantt diagram describing how processes are executed by the CPU.

The following diagrams are Gantt diagram of executed processes in two test case.

In test 0, the CPU execute two processes P1 and P2.

Test 0:

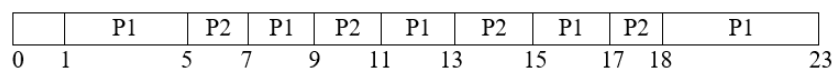


Figure 1: Gantt diagram – test 0

In test 1, the CPU execute four processes P1, P2, P3 and P4.

Test 1:

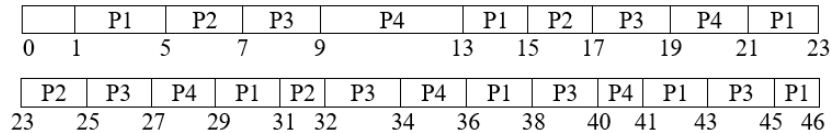


Figure 2: Gantt diagram – test 1

1.3 Implementation

1.3.1 Priority Queue

The following are two functions: **enqueue** and **dequeue** which are implemented to put a new process to the queue and to take the highest priority process out of the queue respectively.

```

1 void enqueue(struct queue_t * q, struct pcb_t * proc) {
2     /* TODO: put a new process to queue [q] */
3     int curSize = q->size;
4     if (curSize == MAX_QUEUE_SIZE) return;
5     else {
6         q->proc[curSize++] = proc;
7         q->size++;
8     }
9 }
10
11 struct pcb_t * dequeue(struct queue_t * q) {
12     /* TODO: return a pcb whose priority is the highest
13      * in the queue [q] and remember to remove it from q
14      */
15     if (empty(q)) return NULL;
16     else{
17         int removePosition = 0;
18         for (int i=0; i < (q->size - 1); i++){
19             if (q->proc[i+1]->priority < q->proc[i]->priority){
20                 removePosition = i+1;
21             }
22         }
23         struct pcb_t * temp = q->proc[removePosition];
24         for (int i = removePosition + 1; i < q->size; i++){
25             q->proc[i-1] = q->proc[i];
26         }
27         q->size--;
28
29         return temp;
30     }
31 }

```

1.3.2 Scheduler

The following is **get_proc** function. The task of this function is to get a process from **ready_queue**. If ready queue is empty, push all processes in **run_queue** back to **ready_queue** and return the highest priority one.

```
1 struct pcb_t * get_proc(void) {
2     struct pcb_t * proc = NULL;
3     /*TODO: get a process from [ready_queue]. If ready queue
4      * is empty, push all processes in [run_queue] back to
5      * [ready_queue] and return the highest priority one.
6      * Remember to use lock to protect the queue.
7      */
8     pthread_mutex_lock(&queue_lock);
9     if (empty(&ready_queue)) {
10         while(!empty(&run_queue))
11             enqueue(&ready_queue, dequeue(&run_queue));
12     }
13     proc = dequeue(&ready_queue);
14     pthread_mutex_unlock(&queue_lock);
15     return proc;
16 }
```

2 Memory Management

2.1 Question - Segmentation with Paging

Question: What is the advantage and disadvantage of segmentation with paging.

— **Advantage:**

- Exploit good properties of paging: no external fragmentation and faster allocation.
- Exploit good properties of segmentation: sharing and protection.
- Save and use memory efficiently.

— **Disadvantage:**

- Internal fragmentation of paging still exists.

2.2 Status of RAM

Requirement: Show the status of RAM after each memory allocation and deallocation function call.

The following are status of RAM in two test cases.

Test 0

```
1 -----Allocation-----
2 000: 00000-003ff - PID: 01 (idx 000, nxt: 001)
3 001: 00400-007ff - PID: 01 (idx 001, nxt: 002)
4 002: 00800-00bff - PID: 01 (idx 002, nxt: 003)
5 003: 00c00-00fff - PID: 01 (idx 003, nxt: 004)
6 004: 01000-013ff - PID: 01 (idx 004, nxt: 005)
7 005: 01400-017ff - PID: 01 (idx 005, nxt: 006)
8 006: 01800-01bff - PID: 01 (idx 006, nxt: 007)
9 007: 01c00-01fff - PID: 01 (idx 007, nxt: 008)
10 008: 02000-023ff - PID: 01 (idx 008, nxt: 009)
11 009: 02400-027ff - PID: 01 (idx 009, nxt: 010)
12 010: 02800-02bff - PID: 01 (idx 010, nxt: 011)
13 011: 02c00-02fff - PID: 01 (idx 011, nxt: 012)
```



```
14 012: 03000-033ff - PID: 01 (idx 012, nxt: 013)
15 013: 03400-037ff - PID: 01 (idx 013, nxt: -01)
16 -----==Allocation=====
17 000: 00000-003ff - PID: 01 (idx 000, nxt: 001)
18 001: 00400-007ff - PID: 01 (idx 001, nxt: 002)
19 002: 00800-00bff - PID: 01 (idx 002, nxt: 003)
20 003: 00c00-00fff - PID: 01 (idx 003, nxt: 004)
21 004: 01000-013ff - PID: 01 (idx 004, nxt: 005)
22 005: 01400-017ff - PID: 01 (idx 005, nxt: 006)
23 006: 01800-01bff - PID: 01 (idx 006, nxt: 007)
24 007: 01c00-01fff - PID: 01 (idx 007, nxt: 008)
25 008: 02000-023ff - PID: 01 (idx 008, nxt: 009)
26 009: 02400-027ff - PID: 01 (idx 009, nxt: 010)
27 010: 02800-02bff - PID: 01 (idx 010, nxt: 011)
28 011: 02c00-02fff - PID: 01 (idx 011, nxt: 012)
29 012: 03000-033ff - PID: 01 (idx 012, nxt: 013)
30 013: 03400-037ff - PID: 01 (idx 013, nxt: -01)
31 014: 03800-03bff - PID: 01 (idx 000, nxt: 015)
32 015: 03c00-03fff - PID: 01 (idx 001, nxt: -01)
33 -----==Deallocation=====
34 014: 03800-03bff - PID: 01 (idx 000, nxt: 015)
35 015: 03c00-03fff - PID: 01 (idx 001, nxt: -01)
36 -----==Allocation=====
37 000: 00000-003ff - PID: 01 (idx 000, nxt: 001)
38 001: 00400-007ff - PID: 01 (idx 001, nxt: -01)
39 014: 03800-03bff - PID: 01 (idx 000, nxt: 015)
40 015: 03c00-03fff - PID: 01 (idx 001, nxt: -01)
41 -----==Allocation=====
42 000: 00000-003ff - PID: 01 (idx 000, nxt: 001)
43 001: 00400-007ff - PID: 01 (idx 001, nxt: -01)
44 002: 00800-00bff - PID: 01 (idx 000, nxt: 003)
45 003: 00c00-00fff - PID: 01 (idx 001, nxt: 004)
46 004: 01000-013ff - PID: 01 (idx 002, nxt: 005)
47 005: 01400-017ff - PID: 01 (idx 003, nxt: 006)
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50 015: 03c00-03fff - PID: 01 (idx 001, nxt: -01)
51 000: 00000-003ff - PID: 01 (idx 000, nxt: 001)
52 003e8: 15
53 001: 00400-007ff - PID: 01 (idx 001, nxt: -01)
54 002: 00800-00bff - PID: 01 (idx 000, nxt: 003)
55 003: 00c00-00fff - PID: 01 (idx 001, nxt: 004)
56 004: 01000-013ff - PID: 01 (idx 002, nxt: 005)
57 005: 01400-017ff - PID: 01 (idx 003, nxt: 006)
58 006: 01800-01bff - PID: 01 (idx 004, nxt: -01)
59 014: 03800-03bff - PID: 01 (idx 000, nxt: 015)
60 03814: 66
61 015: 03c00-03fff - PID: 01 (idx 001, nxt: -01)
```

Test 1

```
1 -----==Allocation=====
2 000: 00000-003ff - PID: 01 (idx 000, nxt: 001)
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6 004: 01000-013ff - PID: 01 (idx 004, nxt: 005)
7 005: 01400-017ff - PID: 01 (idx 005, nxt: 006)
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11 009: 02400-027ff - PID: 01 (idx 009, nxt: 010)
12 010: 02800-02bff - PID: 01 (idx 010, nxt: 011)
```



```
13 011: 02c00-02fff - PID: 01 (idx 011, nxt: 012)
14 012: 03000-033ff - PID: 01 (idx 012, nxt: 013)
15 013: 03400-037ff - PID: 01 (idx 013, nxt: -01)
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17 000: 00000-003ff - PID: 01 (idx 000, nxt: 001)
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26 009: 02400-027ff - PID: 01 (idx 009, nxt: 010)
27 010: 02800-02bff - PID: 01 (idx 010, nxt: 011)
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31 014: 03800-03bff - PID: 01 (idx 000, nxt: 015)
32 015: 03c00-03fff - PID: 01 (idx 001, nxt: -01)
33 -----==Deallocation=====
34 014: 03800-03bff - PID: 01 (idx 000, nxt: 015)
35 015: 03c00-03fff - PID: 01 (idx 001, nxt: -01)
36 -----==Allocation=====
37 000: 00000-003ff - PID: 01 (idx 000, nxt: 001)
38 001: 00400-007ff - PID: 01 (idx 001, nxt: -01)
39 014: 03800-03bff - PID: 01 (idx 000, nxt: 015)
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41 -----==Allocation=====
42 000: 00000-003ff - PID: 01 (idx 000, nxt: 001)
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52 002: 00800-00bff - PID: 01 (idx 000, nxt: 003)
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54 004: 01000-013ff - PID: 01 (idx 002, nxt: 005)
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58 015: 03c00-03fff - PID: 01 (idx 001, nxt: -01)
59 -----==Deallocation=====
60 014: 03800-03bff - PID: 01 (idx 000, nxt: 015)
61 015: 03c00-03fff - PID: 01 (idx 001, nxt: -01)
62 -----==Deallocation=====
```

2.3 Implementation

2.3.1 Search for page table from segment table

In this assignment, we use the first 5 bits for segment index, the next 5 bits for page index and the last 10 bits for offset.

Function receive 5 bits index [index] and segment table [seg_table] and we will find out ap-

appropriate page table from that segment table. In order to find the page table, we will compare the index in segment table [v_index] with the given index [index]. If the [v_index] equals to [index], we will return the appropriate table.

The following is the implementation.

```
1 static struct page_table_t * get_page_table(  
2     addr_t index,    // Segment level index  
3     struct seg_table_t * seg_table) { // first level table  
4     /*  
5      * TODO: Given the Segment index [index], you must go through each  
6      * row of the segment table [seg_table] and check if the v_index  
7      * field of the row is equal to the index  
8      *  
9      */  
10    int i;  
11    for (i = 0; i < seg_table->size; i++) {  
12        // Enter your code here  
13        if(seg_table->table[i].v_index == index){  
14            return seg_table->table[i].pages;  
15        }  
16    }  
17    return NULL;  
18 }
```

2.3.2 Translate virtual address to physical address

Each address contains 20 bits; therefore, to get the physical address , we will concatenate 10 bits of page and segment table with 10 bits of offset.

The following is the implementation of translating virtual address to physical address.

The following is the implementation of this function.

```
1 static int translate(  
2     addr_t virtual_addr, // Given virtual address  
3     addr_t * physical_addr, // Physical address to be returned  
4     struct pcb_t * proc) { // Process uses given virtual address  
5  
6     /* Offset of the virtual address */  
7     addr_t offset = get_offset(virtual_addr);  
8     /* The first layer index */  
9     addr_t first_lv = get_first_lv(virtual_addr);  
10    /* The second layer index */  
11    addr_t second_lv = get_second_lv(virtual_addr);  
12  
13    /* Search in the first level */  
14    struct page_table_t * page_table = NULL;  
15    page_table = get_page_table(first_lv, proc->seg_table);  
16    if (page_table == NULL) {  
17        return 0;  
18    }  
19  
20    int i;  
21    for (i = 0; i < page_table->size; i++) {  
22        if (page_table->table[i].v_index == second_lv) {  
23            /* TODO: Concatenate the offset of the virtual address  
24             * to [p_index] field of page_table->table[i] to  
25             * produce the correct physical address and save it to  
26             */  
27        }  
28    }  
29    *physical_addr = ...  
30 }
```



```
26     * [*physical_addr] */
27     addr_t p_index = page_table->table[i].p_index;
28     *physical_addr = (p_index << OFFSET_LEN) | offset;
29     return 1;
30 }
31 }
32 return 0;
33 }
```

2.3.3 Allocate memory

2.3.3.a Check amount of free memory in virtual address space and physical address space.

We will create a function to check if amount of free memory is large enough to allocate.

In physical address space, we will traverse each page of [_mem_stat] and check whether that page has been used by process. We will count the number of free pages and if the number of free pages are large enough, it means that physical address space are ready to allocate.

In virtual address space, we will check whether the break pointers are over allowed memory.

The following is the implementation of this function.

```
1 int check_amount_memory(struct pcb_t * proc , uint32_t num_pages){
2     uint32_t number_mem_free = 0;
3     for(int i = 0 ; i < NUM_PAGES ; i++){
4         if(_mem_stat[i].proc == 0){
5             if(++number_mem_free == num_pages)
6                 break;
7         }
8     }
9     if(number_mem_free < num_pages) return 0;
10    if(proc->bp + num_pages*PAGE_SIZE >= RAM_SIZE) return 0;
11
12    return 1;
13 }
```

2.3.3.b Allocate memory

To allocate memory, we will create the following function.

In this function, we traverse on the physical memory to search for free pages and then update [proc], [index] and [next] in [_mem_stat]. We use the last_alloc_pages to help update [next].

The following is the implementation of this function.

```
1 void alloc_ret_mem(addr_t ret_mem, uint32_t num_pages, struct pcb_t * proc){
2     int count_pages = 0; //count pages
3     int last_alloc_pages = -1; //get [next] = -1 at last page
4
5     for(int i = 0 ; i < NUM_PAGES ; i++)
```

```

5     {
6         if(_mem_stat[i].proc != 0) continue; //page have used
7         _mem_stat[i].proc = proc->pid;      // the page is used by process

8         _mem_stat[i].index = count_pages;    //index list of allocated pages
9         if(last_alloc_pages > -1){
10            _mem_stat[last_alloc_pages].next = i;    //update last page
11        }
12        last_alloc_pages = i;    //update last page
13        //find and creat virtual page_table
14        addr_t virtual_address = ret_mem + count_pages*PAGE_SIZE;

15        addr_t segment_addr = get_first_lv(virtual_address);
16        addr_t page_addr = get_second_lv(virtual_address);
17        struct page_table_t* v_page_table = get_page_table(segment_addr, proc->
seg_table);
18        if(v_page_table == NULL){
19            proc->seg_table->table[proc->seg_table->size].v_index = segment_addr;
20            v_page_table = proc->seg_table->table[proc->seg_table->size].pages
21            = (struct page_table_t*)malloc(sizeof(struct page_table_t));
22            proc->seg_table->size++;
23        }
24        int idx = v_page_table->size++;
25        v_page_table->table[idx].v_index = page_addr;
26        v_page_table->table[idx].p_index = i;
27        ++count_pages;
28        if(count_pages == num_pages){
29            _mem_stat[i].next = -1; break;
30        }
31    }
32 }
33 }

```

2.3.4 Release memory

The following is implementation of release memory.

```

1 int free_mem(addr_t address, struct pcb_t * proc) {
2     /**
3      * TODO: Release memory region allocated by [proc].
4      * The first byte of this region is indicated by [address].
5      * Tasks to do:
6      * + Set flag [proc] of physical page use by the memory block
7      * + back to zero to indicate that it is free.
8      * + Remove unused entries in segment table and page tables of the process [
proc].
9      * + Remember to use lock to protect the memory from other processes.
10    */
11    pthread_mutex_lock(&mem_lock);
12    addr_t v_address = address; // virtual address to free in process
13    addr_t p_address = 0;    // physical address to free in memory
14    // Find physical page in memory
15    if (!translate(v_address, &p_address, proc)) return 1;
16    // Clear physical page in memory
17    addr_t p_segment_page_index = p_address >> OFFSET_LEN;
18    int num_pages = 0; // number of pages in list
19    int i;
20    for (i=p_segment_page_index; i!=-1; i=_mem_stat[i].next) {
21        num_pages++;
22        _mem_stat[i].proc = 0; // clear physical memory

```

```

23 }
24 // Clear virtual page in process
25 for (i = 0; i < num_pages; i++) {
26     addr_t v_addr = v_address + i * PAGE_SIZE;
27     addr_t v_segment = get_first_lv(v_addr);
28     addr_t v_page = get_second_lv(v_addr);
29     struct page_table_t * page_table = get_page_table(v_segment, proc->seg_table);
30     if (page_table == NULL) {
31         continue;
32     }
33     int j;
34     for (j = 0; j < page_table->size; j++) {
35         if (page_table->table[j].v_index == v_page) {
36             int last = --page_table->size;
37             page_table->table[j] = page_table->table[last];
38             break;
39         }
40     }
41     if (page_table->size == 0) {
42         remove_page_table(v_segment, proc->seg_table);
43     }
44 }
45 // Update break pointer
46 proc->bp = proc->bp - num_pages*PAGE_SIZE;
47 pthread_mutex_unlock(&mem_lock);
48 return 0;
49 }

```

3 Overall

After finishing all implementation, we will put all the code together and run completely simple operating system. The following is the running process of each CPU in two different cases which are performed in Gantt diagram.

Test 0:

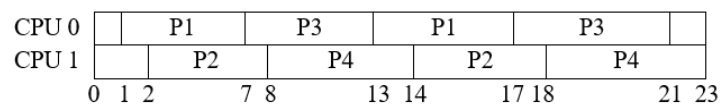
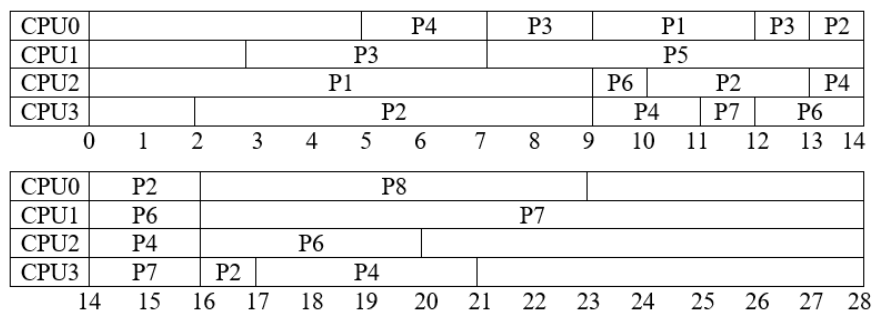


Figure 1: Gantt diagram – test 0



Test 1:



Firgue 2: Gantt diagram – test 1