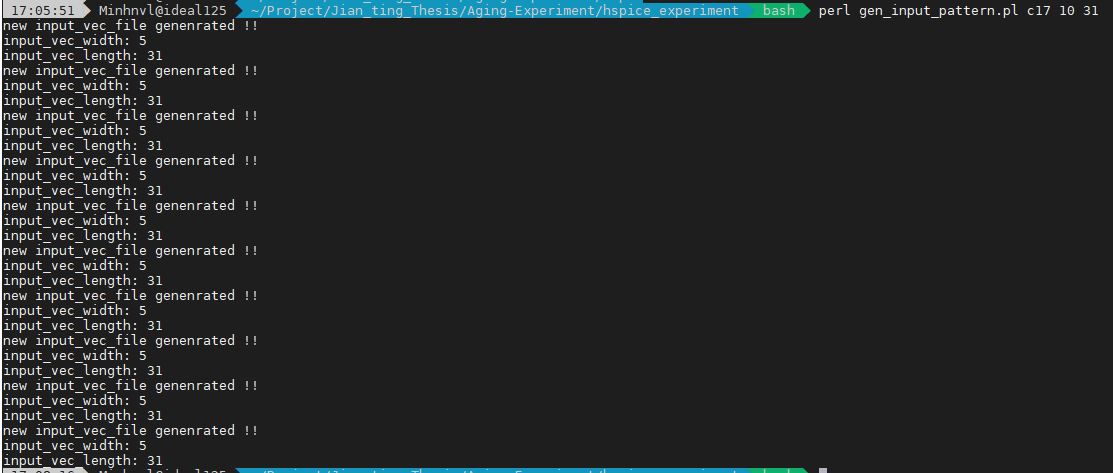
HOMEWORK 2

Student ID: P76087099 Name: Nguyen Vu Le Minh

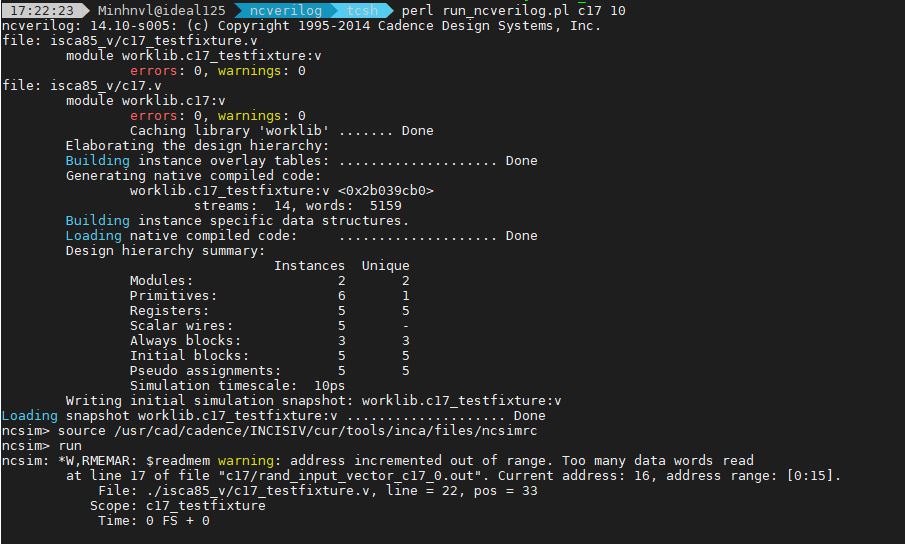
The first step is an input pattern set of a random production circuit.

Execute command: gen\_input\_pattern.pl

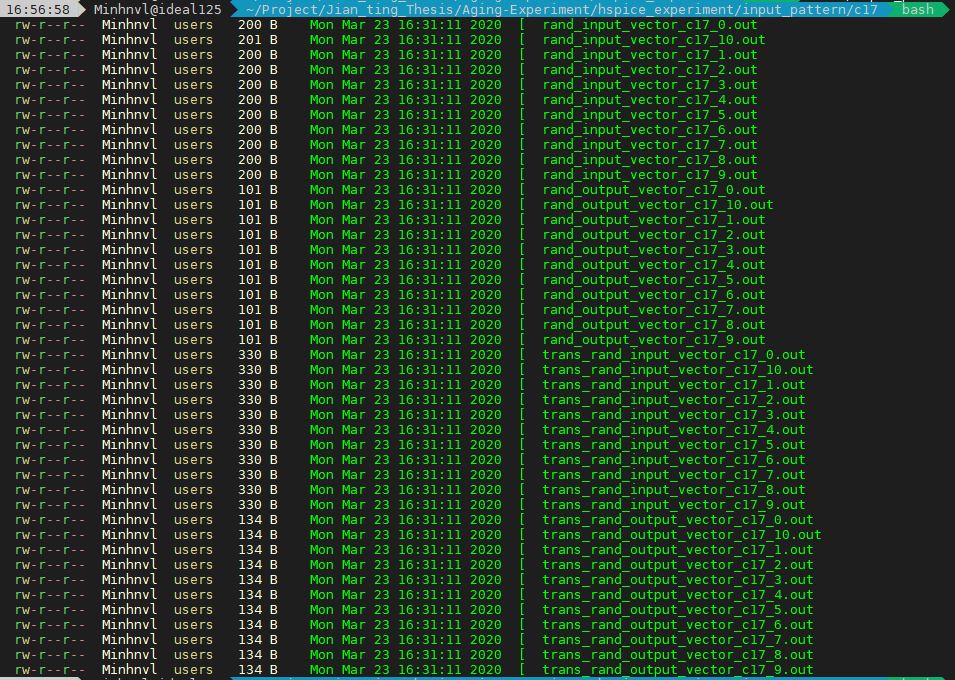


The following describes how to use ncverilog to simulate the output pattern of the circuit according to the input pattern.

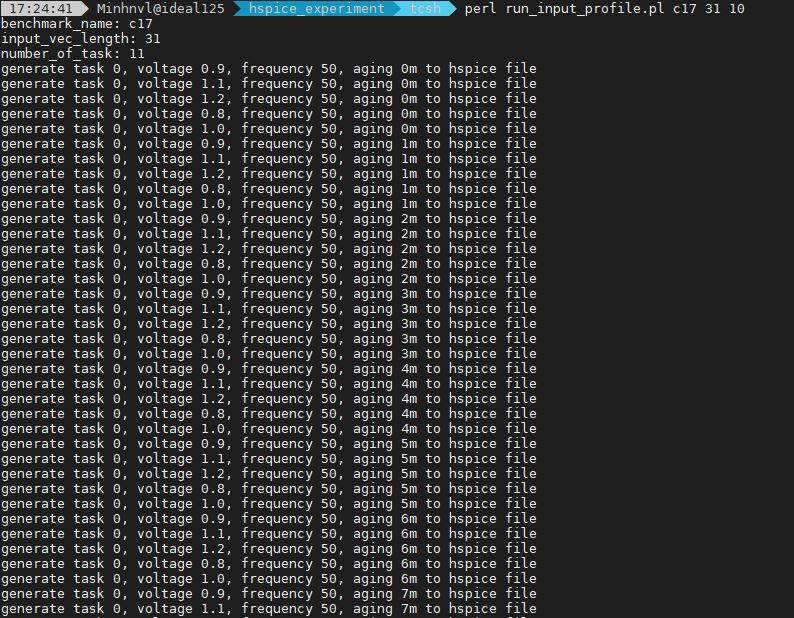
Execute command run\_ncverilog.pl



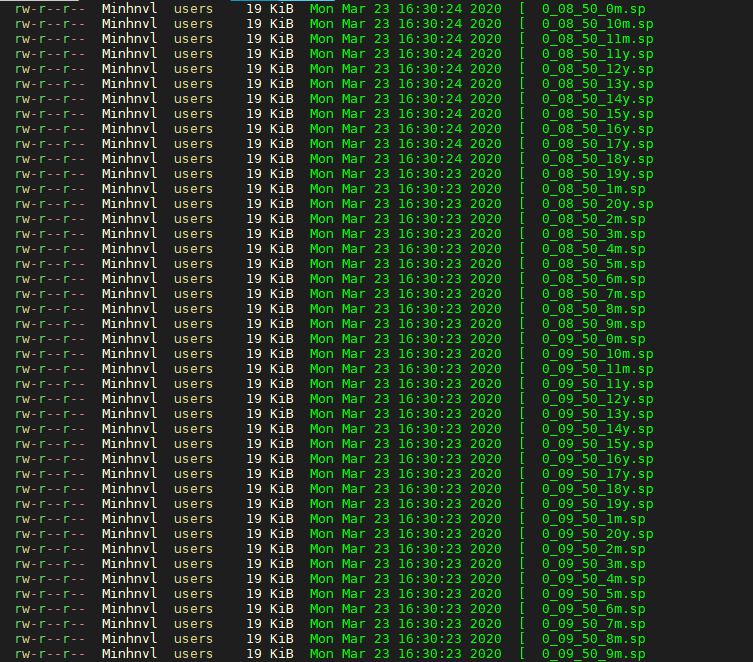
We will find that there are more files related to output pattern in the directory of input pattern.



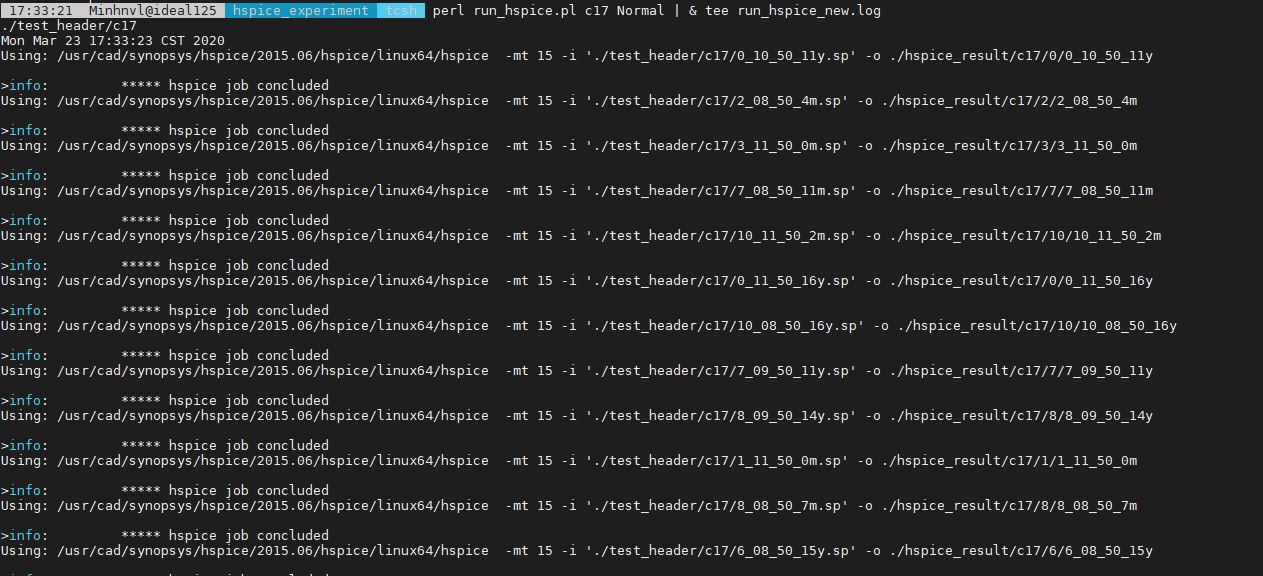
We go back to the folder “hspice\_experiment” and execute command run\_input\_profile.pl



You will see file corresponding to the circuit in test\_header directory



In hspice\_experiment direction, execute run\_hispice.pl



Finally, we run command run\_simulate.pl the multicore lifetime and energy simulation results will be generated.

