

Date Printed: 5 February 2023

TEST IDENTIFICATION

TEST NAME

Resistance Value, R1

TEST NO*	T4	UUT PN	04A-005
LAST TEST NO	Т3	TEST TYPE	STATIC

COMPONENTS UNDER TEST

REF DES	TYPE	PARAMETER	FAILURE MODE	TEST LIMIT
R1	DESISTOR (CENERAL)	RESISTANCE	Out of Tolerance: HIGH	UPPER
	RESISTOR (GENERAL)	RESISTANCE	Out of Tolerance: LOW	LOWER
	N/A	N/A	N/A	N/A
	N/A		N/A	N/A
	N/A	N/A	N/A	N/A
	N/A		N/A	N/A
	N/A	N/A	N/A	N/A
	N/A	N/A	N/A	N/A
	N/A	N/A	N/A	N/A
	N/A		N/A	N/A
	N/A	N/A	N/A	N/A
	N/A		N/A	N/A
	N1/A	N/A	N/A	N/A
	N/A		N/A	N/A
	N/A	N/A	N/A	N/A
	N/A		N/A	N/A
	N/A	N/A	N/A	N/A
	N/A		N/A	N/A
	N/A	N/A	N/A	N/A
	N/A	N/A	N/A	N/A
	21/2	N/A	N/A	N/A
	N/A		N/A	N/A
	N1/A	N/A	N/A	N/A
	N/A		N/A	N/A
	N1/A	N/A	N/A	N/A
	N/A		N/A	N/A
	N1/A	N1/A	N/A	N/A
	N/A	N/A	N/A	N/A



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TEST PROCEDURE

PURPOSE		
Verify resistor is within expected resistance toleran	nce.	
, ,		
SCOPE		
Handheld multimeter measurement at test points.		
nanuneia maitimeter measurement at test points.	•	
	N/A	
	IN/ A	



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POWER

None

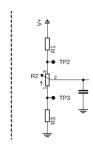
STIMULI

None

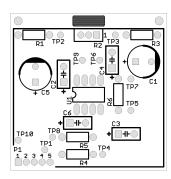
MEASUREMENT

Measure resistance across the resistor.

SIGNAL PIN TP9



RETURN PIN TP2





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ACCEPTANCE CRITERIA

PARAMETER	LOWER LIMIT	TARGET VALUE	UPPER LIMIT	UNITS
RESISTANCE	98	100	102	Ω

TEST RESULTS

RESULT	NEXT STEP	ADJUST	REPLACE
IN TOLERANCE	T5	N/A	N/A
OOT HIGH	END FAIL	N/A	R1
OOT LOW	END FAIL	N/A	R1
OOT HIGH AND UNABLE TO ADJUST	N/A	N/A	N/A
OOT LOW AND UNABLE TO ADJUST	N/A	N/A	N/A



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Save Save and Close Close Without Saving Delete Log Entry Key 20230205190355

Computer ID desktop-jti13j1
User ID nolan

Date Time 20230205190355

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