

ECO 1006 Date Printed: 06 September 2022

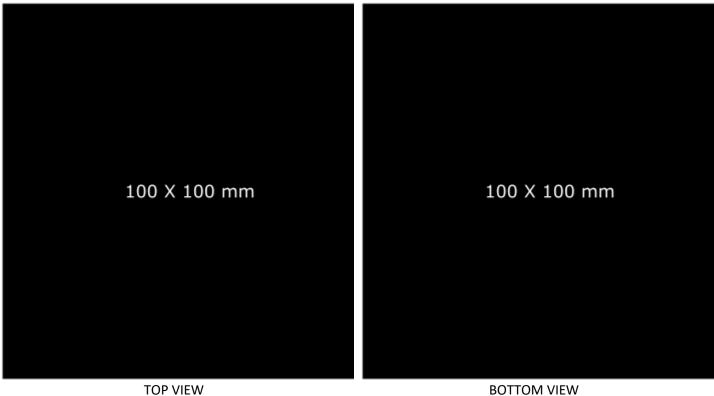
# Window Comparator

PART NUMBER	08A-005
GROUP NAME	Signal Converter (08A)
CIRCUIT NAME	Window Comparator
VARIANT DESCRIPTION	Single Supply, THD, Window Threshold Trimmers
BOARD DESIGN	PCB50-A-07
PRODUCT DESCRIPTION	Panel of #08A-005 miniPCBs, v-scored (1 Panel = 4 Pieces)

# Circuit Description

This circuit amplifies a small voltage signal.

### Panel Board

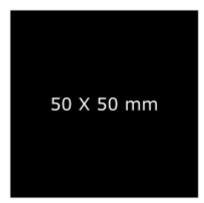


1 of 22 www.miniPCB.com

ECO 1006

Date Printed: 06 September 2022

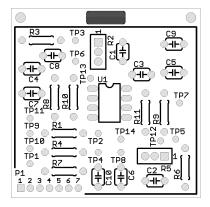
# Single Board



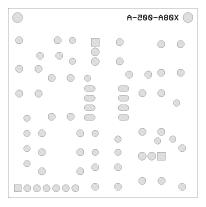
**TOP VIEW** 

# 50 X 50 mm

### Part Locations



**TOP VIEW** 



**BOTTOM VIEW** 

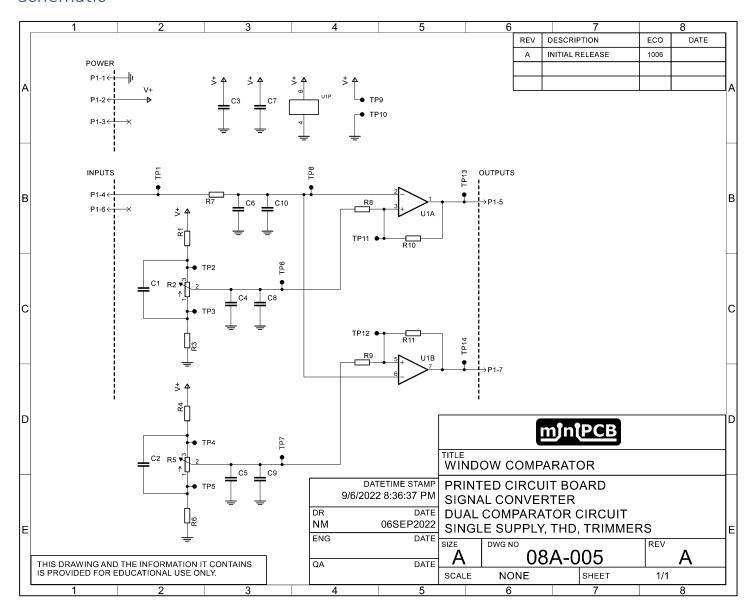
www.miniPCB.com 2 of 22



ECO 1006

Date Printed: 06 September 2022

### Schematic



www.miniPCB.com 3 of 22

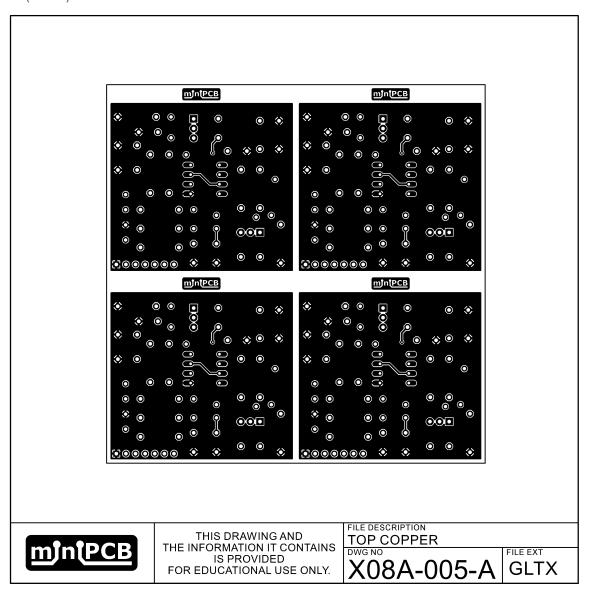
ECO 1006

Date Printed: 06 September 2022

### Gerber Files

This section contains images of the layers included in each Gerber file.

### TOP COPPER (GLTX)



www.miniPCB.com 4 of 22

ECO

Date Printed: 06 September 2022

1006

TOP CREAM (GCTX)

			TFILE DESCRIPTION	
mjniPC	СВ	THIS DRAWING AND THE INFORMATION IT CONTAINS IS PROVIDED FOR EDUCATIONAL USE ONLY.	TOP CREAM  DWG NO  X08A-005-A	FILE EXT

www.miniPCB.com 5 of 22

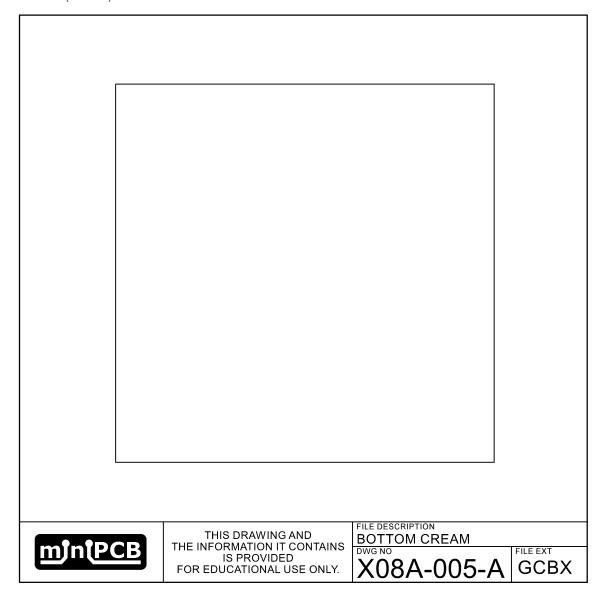
08A-005-A

ECO

Date Printed: 06 September 2022

1006

**BOTTOM CREAM (GCBX)** 



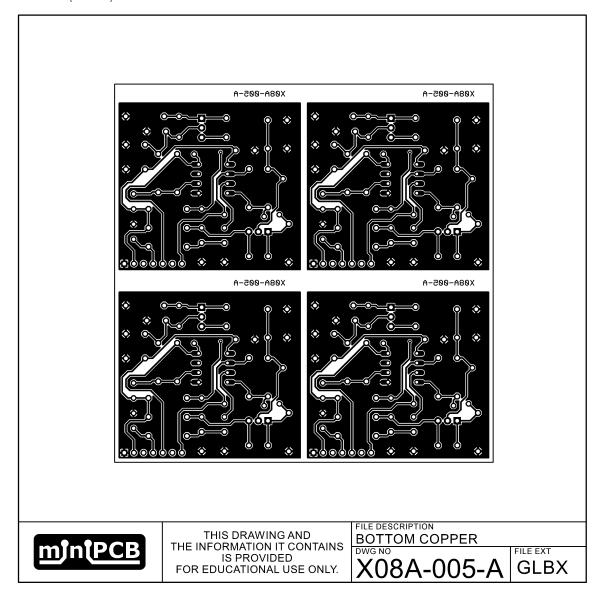
www.miniPCB.com 6 of 22

ECO

Date Printed: 06 September 2022

1006

### **BOTTOM COPPER (GLBX)**

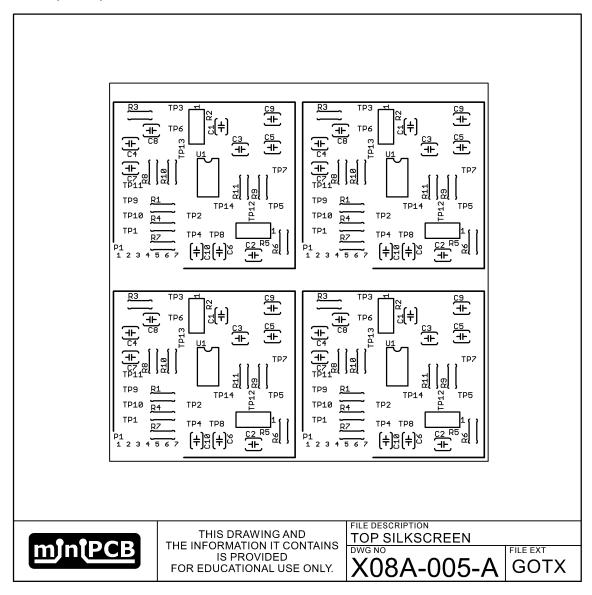


www.miniPCB.com 7 of 22

ECO 1006

Date Printed: 06 September 2022

### TOP SILKSCREEN (GOTX)



www.miniPCB.com 8 of 22

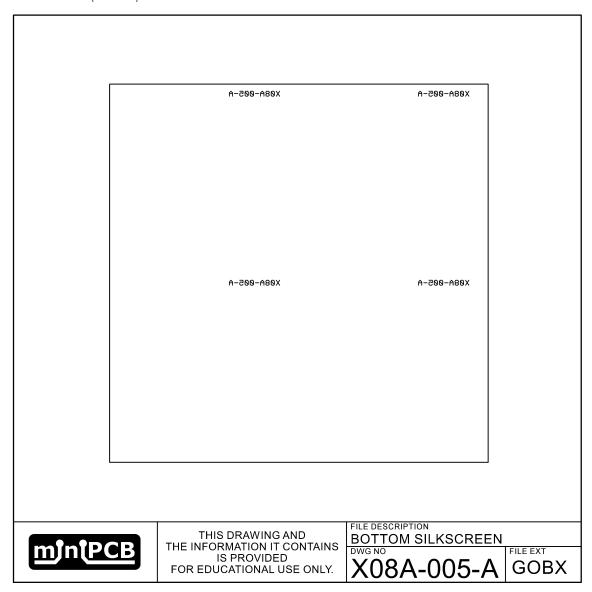
08A-005-A

ECO

Date Printed: 06 September 2022

1006

### **BOTTOM SILKSCREEN (GOBX)**



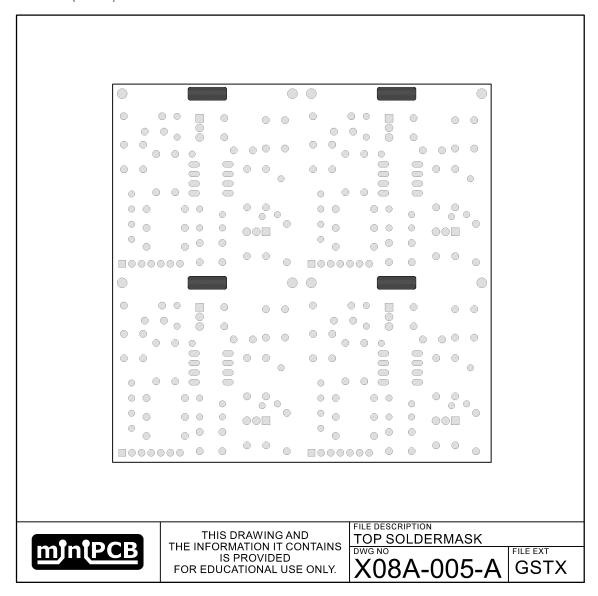
www.miniPCB.com 9 of 22

ECO

Date Printed: 06 September 2022

1006

### TOP SOLDERMASK (GSTX)

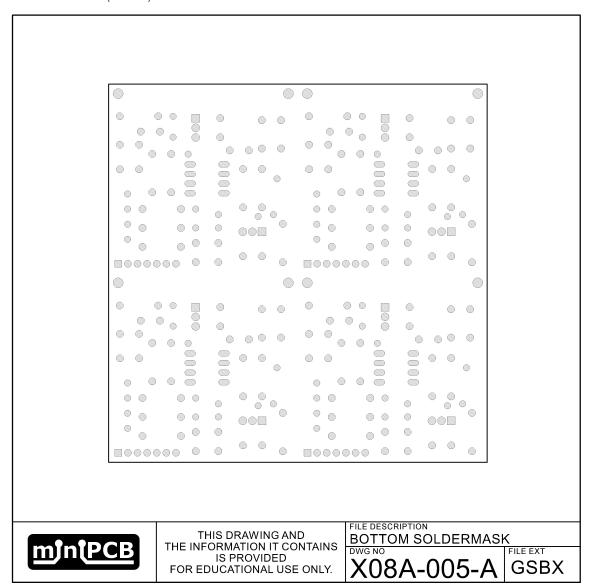


www.miniPCB.com 10 of 22

ECO 1006

Date Printed: 06 September 2022

### **BOTTOM SOLDER MASK (GSBX)**

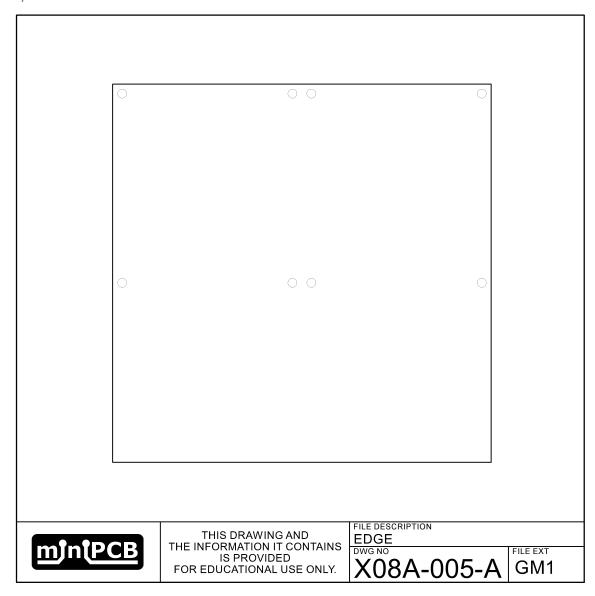


www.miniPCB.com 11 of 22

ECO 1006

Date Printed: 06 September 2022

### EDGE (GM1)



www.miniPCB.com 12 of 22

08A-005-A

ECO

Date Printed: 06 September 2022

1006

VSCORE (GM2)

				FILE DESCRIPTION	
mjntP0	СВ	THIS DRAWING AN THE INFORMATION IT CO IS PROVIDED FOR EDUCATIONAL USE	NTAINS	V-SCORE DWG NO X08A-005-A	FILE EXT

www.miniPCB.com 13 of 22

08A-005-A

ECO

Date Printed: 06 September 2022

1006

MILLING (GM3)

m]n(PCI	7 T	THIS DRAWING HE INFORMATION IT	CONTAINS	FILE DESCRIPTION MILLING DWG NO	FILE EXT
	<b>3</b>	IS PROVIDED FOR EDUCATIONAL U	)	X08A-005-A	<b>A</b> GM3

www.miniPCB.com 14 of 22

08A-005-A

**ECO** 

Date Printed: 06 September 2022

1006

# Theory of Operation

The purpose of this circuit is to...

This circuit is supplied with a positive DC voltage...

The input stimuli is DC coupled...

The output signal is DC coupled...

www.miniPCB.com 15 of 22

08A-005-A

ECO 1006 Date Printed: 06 September 2022

# **Design Inputs**

### Design Requirements Form

### **POWER REQUIREMENTS**

PARAMETER NAME	SYMBOL	UNITS	LOWER LIMIT	TARGET VALUE	UPPER LIMIT
Postive DC Supply	+V	V			
Negative DC Supply	-V	V			

**STIMULI REQUIREMENTS** 

PARAMETER NAME	SYMBOL	UNITS	LOWER LIMIT	TARGET VALUE	UPPER LIMIT
Signal Voltage, Peak to Peak	$V_{s}$	V			
Signal Frequency	$f_s$	Hz			
Common Mode	$V_{cm}$	V			
Source Impedance	$R_{s}$	Ω			

### PERFORMANCE CHARACTERISTICS

PARAMETER NAME	SYMBOL	UNITS	LOWER LIMIT	TARGET VALUE	UPPER LIMIT
Quiescient Current	$I_q$	Α			
Voltage Gain	$A_v$	V/V			
Current Gain	$A_i$	A/A			
Power Gain	$A_p$	P/P			
Input Impedance	$R_i$	Ω			
Output Impedance	$R_i$	Ω			

16 of 22 www.miniPCB.com

08A-005-A

ECO 1006

Date Printed: 06 September 2022

# **Design Outputs**

### Parts List Form

REF DES	PART TYPE	MFG PART NUMBER	PART DESCRIPTION	FIND
				1
				2
				3
				4
				5
				6
				7
				8
				9
				10
				11

www.miniPCB.com 17 of 22

ECO 1006

Date Printed: 06 September 2022

### **Testing Plans**

### **Developmental Testing**

- 1. Plan each calibration and service test.
- 2. Predict expected values for each test measurement.
- 3. Determine if expected values satisfy design requirements.
- 4. Assemble a prototype that is representative of what might be the final design.
- 5. Perform the calibration and service testing plans.
- 6. Determine if the design outputs satisfy design requirements.

### Calibration and Service Testing

- 1. With power off, measure resistances between each pin.
- 2. If measured resistances are not as expected, end testing fail, components need to be replaced.
- 3. With power on, measure voltages at each pin.
- 4. If measured voltages are not as expected, end testing fail, components need to be replaced.
- 5. With power on, adjust potentiometer PX such that the voltage at test point TPX is ##.
- 6. If measured voltages cannot be adjusted to an expected value, end testing fail, components need to be replaced.
- 7. With power on, apply stimuli and measure outputs.
- 8. If measured output signals are not as expected, end testing fail, components need to be replaced.
- If measured output signals are as expected, end testing pass.-

www.miniPCB.com 18 of 22

08A-005-A

ECO 1006

Date Printed: 06 September 2022

# Design Example

### Design Inputs

### **POWER REQUIREMENTS**

PARAMETER NAME	SYMBOL	UNITS	LOWER LIMIT	TARGET VALUE	UPPER LIMIT
Postive DC Supply	+V	V	4.9	5	5.1
Negative DC Supply	-V	V			

**STIMULI REQUIREMENTS** 

PARAMETER NAME	SYMBOL	UNITS	LOWER LIMIT	TARGET VALUE	UPPER LIMIT
Signal Voltage, Peak to Peak	$V_{s}$	V	0.015	0.02	0.025
Signal Frequency	$f_{\scriptscriptstyle S}$	Hz			
Common Mode	$V_{cm}$	V			
Source Impedance	$R_{s}$	Ω			

### PERFORMANCE CHARACTERISTICS

PARAMETER NAME	SYMBOL	UNITS	LOWER LIMIT	TARGET VALUE	UPPER LIMIT
Quiescient Current	$I_q$	Α			
Voltage Gain	$A_v$	V/V			
Current Gain	$A_i$	A/A			
Power Gain	$A_p$	P/P			
Input Impedance	$R_i$	Ω			
Output Impedance	$R_i$	Ω			

www.miniPCB.com 19 of 22

08A-005-A

ECO

Date Printed: 06 September 2022

1006

## Design Outputs

### **PARTS LIST**

QTY REQ	REFERENCE DESIGNATORS	MFG PART NUMBER	PART DESCRIPTION	FIND
3	R1, R2, R5		RESISTOR, 1.5K, 1/4W, 1%	1
2	R3, R4		100	2
1	Q1		2N2222	3
1	C1		10u	4
1	C2		1u	5
1	C3		0.1u	6

www.miniPCB.com 20 of 22

08A-005-A

ECO

Date Printed: 06 September 2022

1006

# Developmental Tests per Example

Test Report per Example

www.miniPCB.com 21 of 22

ECO 1006 Date Printed: 06 September 2022

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# **Revision History**

REV	DESCRIPTION	ECO	DATE
Α	Initial Release		

22 of 22 www.miniPCB.com