

NOTES:  
3. PERFORMANCE FUNCTIONS:  
3.1 AMPLIFIER PERFORMANCE:  
- INPUT SIGNAL IS INVERTED AND AMPLIFIED.

3.2  
- DC BIAS BLOCKED BY INPUT CAPACITOR WHEN JUMPER REMOVED.

4. CALIBRATION ADJUSTMENT PROCEDURE:

4.1 PURPOSE  
- SET OUTPUT DC BIAS TO HALF OF SUPPLY VOLTAGE.

4.2 SCOPE  
- PERFORM DURING CALIBRATION PROCEDURE.

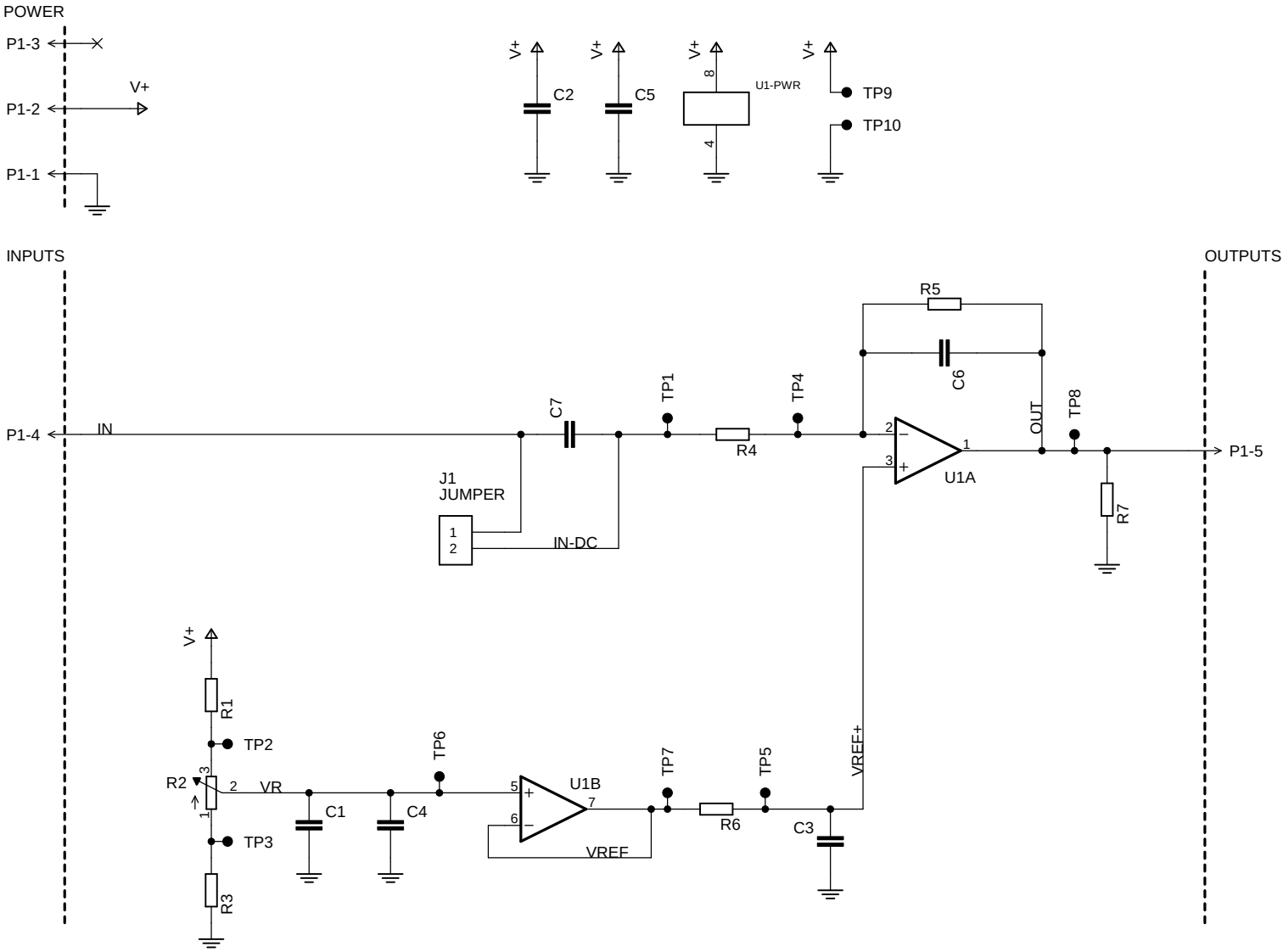
4.3 PROCEDURAL NOTES  
- PERFORM AFTER SAFE TO TURN ON (STTO) TEST PASSES.

4.4 PROCEDURE  
4.4.1 APPLY POWER:  
- SUPPLY VOLTAGE DETERMINES TARGET ADJUSTMENT.

4.4.2 APPLY STIMULI:  
- ALLOW PIN P1-4 TO FLOAT.

4.4.3 MEASURE RESPONSE:  
- MEASURE OUTPUT VOLTAGE DC BIAS.

4.4.4 ADJUST R2 TRIMMER POTENTIOMETER:  
- PASS WHEN MEASUREMENT EQUALS HALF OF SUPPLY VOLTAGE.



THIS DRAWING AND THE INFORMATION IT CONTAINS  
IS PROVIDED FOR EDUCATIONAL USE ONLY.

**m+nPCB**

TITLE  
INVERTING AMPLIFIER CIRCUIT

SIZE  
**B**

DWG NO  
**04A-005**

REV  
**A1**

SCALE  
NONE

SHEET  
2/2