

Date Printed: 5 February 2023

TEST IDENTIFICATION

TEST NAME

Resistance Value, R2

TEST NO*	T5	UUT PN	04A-005
LAST TEST NO	T4	TEST TYPE	STATIC

COMPONENTS UNDER TEST

REF DES	TYPE	PARAMETER	FAILURE MODE	TEST LIMIT
R2	DESISTOR (CRITICAL)	RESISTANCE	Out of Tolerance: HIGH	UPPER
	RESISTOR (CRITICAL)	RESISTANCE	Out of Tolerance: LOW	LOWER
	N/A	N/A	N/A	N/A
	N/A		N/A	N/A
	N1/A	N/A	N/A	N/A
	N/A		N/A	N/A
	N/A	N/A	N/A	N/A
	N/A		N/A	N/A
	N/A	N/A	N/A	N/A
	N/A		N/A	N/A
	N/A	N/A	N/A	N/A
	N/A		N/A	N/A
	N1/A	N/A	N/A	N/A
	N/A		N/A	N/A
	N1/A	N/A	N/A	N/A
	N/A		N/A	N/A
	N1/A	N/A	N/A	N/A
	N/A		N/A	N/A
	N1/A	N/A	N/A	N/A
	N/A		N/A	N/A
	21/2	N/A	N/A	N/A
	N/A		N/A	N/A
	N1/A	N/A	N/A	N/A
	N/A		N/A	N/A
	N1/A	A N/A	N/A	N/A
	N/A		N/A	N/A
	N1/A	NI/A	N/A	N/A
	N/A	N/A	N/A	N/A



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TEST PROCEDURE

PURPOSE		
Verify resistor is within expected resistance toleran	nce.	
COORE		
SCOPE		
Handheld multimeter at test points.		
	N1 / A	
	N/A	



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POWER

None

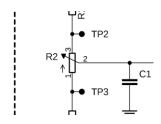
STIMULI

None

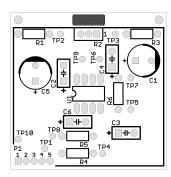
MEASUREMENT

Measure resistance across resistor.

SIGNAL PIN TP2



RETURN PIN TP3





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ACCEPTANCE CRITERIA

PARAMETER	LOWER LIMIT	TARGET VALUE	UPPER LIMIT	UNITS
RESISTANCE	90 ΚΩ	100 ΚΩ	110 ΚΩ	Ω

TEST RESULTS

RESULT	NEXT STEP	ADJUST	REPLACE
IN TOLERANCE	Т6	N/A	N/A
OOT HIGH	END FAIL	N/A	R2
OOT LOW	END FAIL	N/A	R2
OOT HIGH AND UNABLE TO ADJUST	N/A	N/A	N/A
OOT LOW AND UNABLE TO ADJUST	N/A	N/A	N/A



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THIS SPACE INTENTIONALLY BLANK

FORM CONTROLS

Save

Save and Close

Close Without Saving

Delete

Log Entry Key 20230205191102 Computer ID desktop-jti13j1

User ID nolan

Date Time 20230205191102

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