REV	DESCRIPTION	ECO	DATE
A1	INITIAL RELEASE	1023	04NOV2023
A2	ADDED C24	1024	21APR2024

NOTES:
THIS PROJECT WAS CREATED IN SUPPORT OF UTSA ENGINEERING CURRICULUM.

PROJECT REVISION HISTORY:
v1.0, AUG.2014
INITIAL RELEASE

v2.0, JAN.2015
ADDED PROTOTYPING AREA

v3.0, AUG.2015
ADDED HIGH-SIDE TRANSISTOR SWITCH
ADDED ABC CAPACITANCE TOUCH BUTTONS

v4.0, AUG.2018
CONVERTED TO SURFACE MOUNT
REPLACED ABC WITH UTSA FOR CAPACITANCE TOUCH BUTTONS

v5.1, JAN.2022
EXPANDED TO 100 x 100 mm
ADDED ATmega328P-AU
REMOVED HIGH-SIDE TRANSISTOR SWITCH

v5.2, MAY.2022
ADDED ATMEGA BUTTON
CHANGED OLED DISPLAY
ADDED DTR CAPACITOR

v5.3, JUNE.2023
ADDED TO miniPCB PROJECT: PN 11A-001, REV A1
UPDATED ATmega328P-AU PIN BREAKOUTS
UPDATED FOOTPRINTS AND SILKSCREEN FOR HIGHER DENSITY LAYOUT

REV A2, MARCH.2024
ADDED BYPASS CAPACITOR TO AREF SIGNAL ON ATmega328P-AU

DATETIME STAMP 4/21/2024 7:40 PM

DR	N. MANTEUFEL	DATE 06MAR2024
ENG	P. MORTON	DATE 24MAR2024
QA	N. MANTEUFEL	DATE 21APR2024

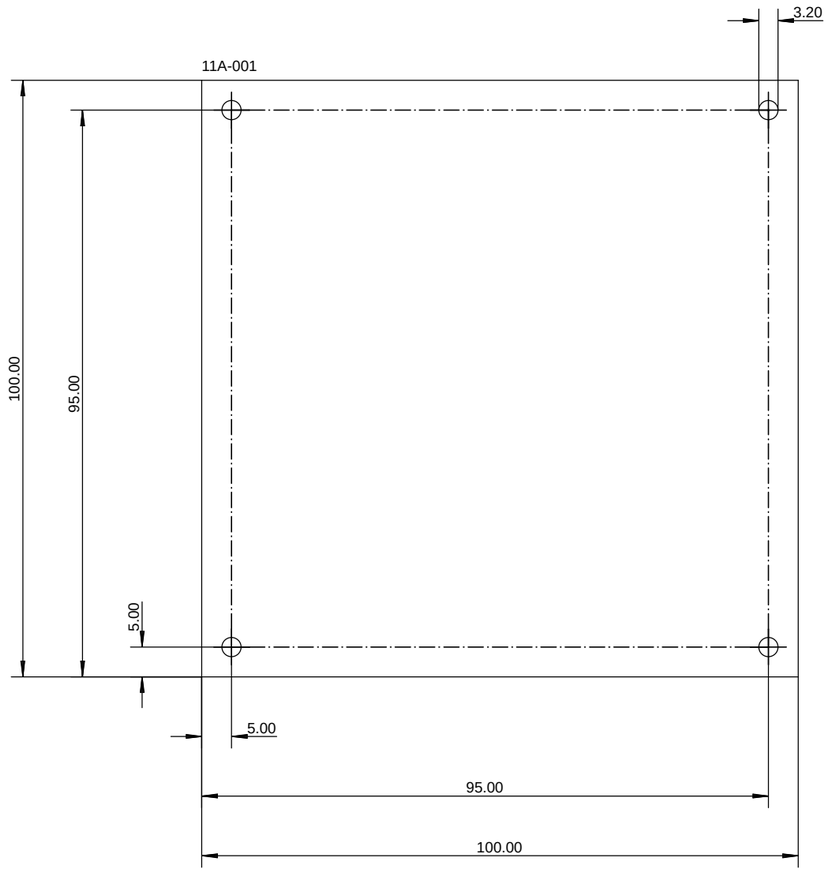
miniPCB

TITLE
VIVA UTSA BOARD

PRINTED CIRCUIT BOARD
COMPUTING
MICROCONTROLLER BOARD
PIC16F1829, ATmega328P-AU

SIZE	DWG NO	REV
B	11A-001	A2

SCALE NONE SHEET 1/5



LOGO

VIVA

FIDUCIALS

A1 A2

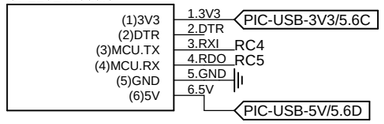
mnPCB

TITLE
MECHANICAL DIMENSIONS

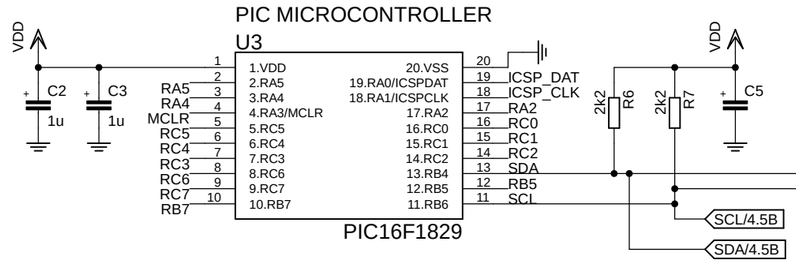
SIZE	DWG NO	REV
B	11A-001	A2

SCALE	NONE	SHEET	2/5

CP2102 Module

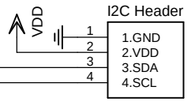


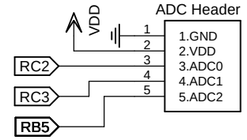
PIC MICROCONTROLLER



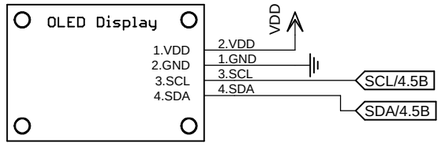
PIC16F1829

VIVA HEADERS

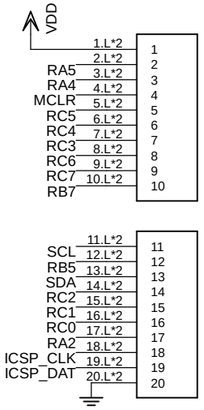




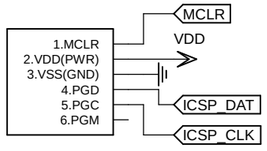
OLED Display



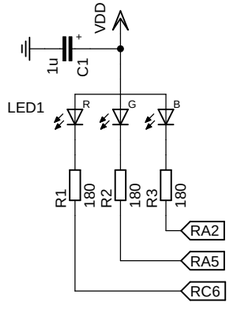
PIC16 HEADERS



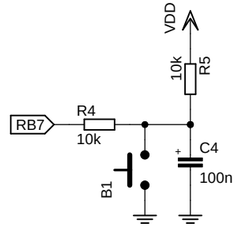
PROGRAMMING HEADER



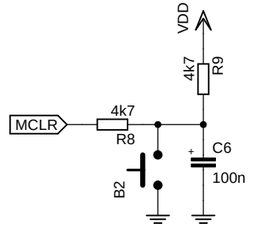
HELLO WORLD! LED



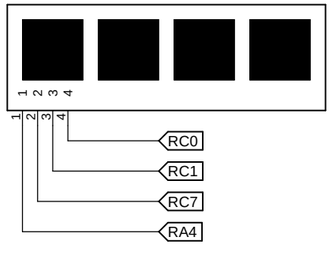
GENERAL PURPOSE BUTTON



RESET BUTTON

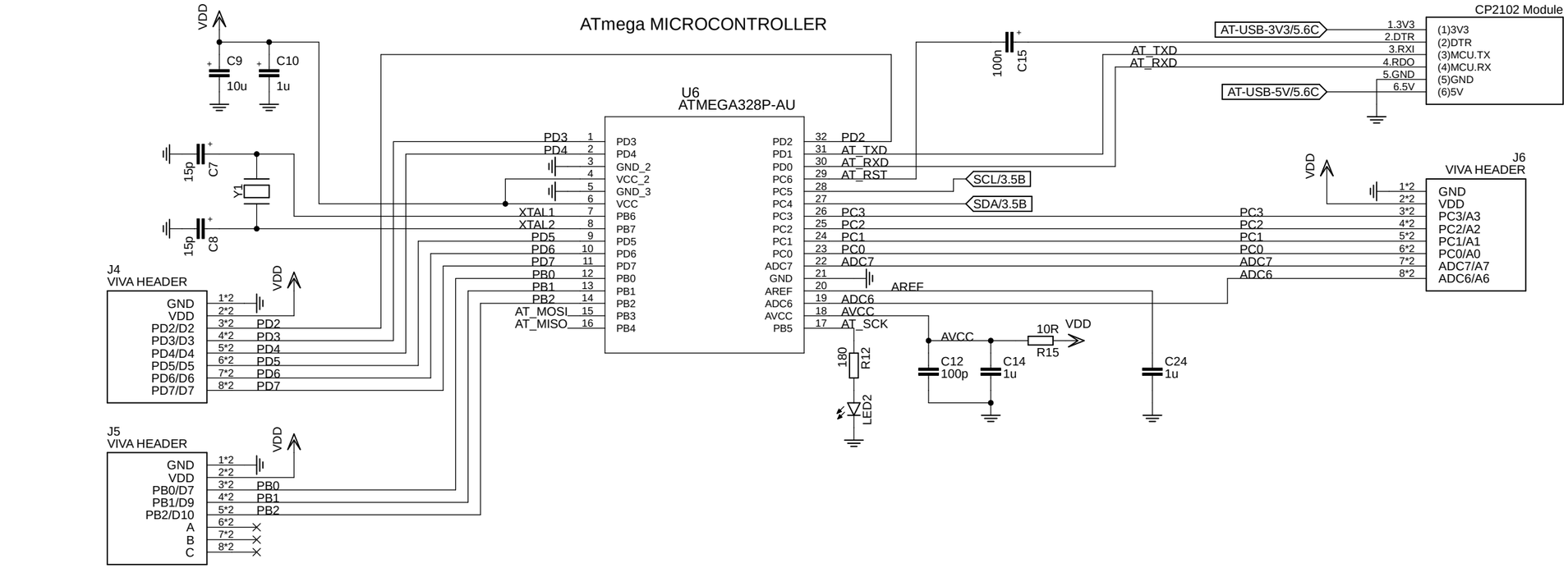


UTSA CAPACITANCE KEYPAD

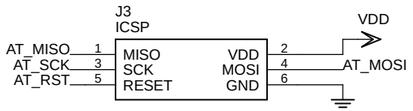


TITLE	PIC MICROCONTROLLER	
SIZE B	DWG NO 11A-001	REV A2
SCALE NONE	SHEET 3/5	

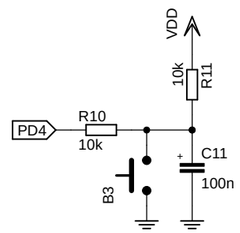
ATmega MICROCONTROLLER



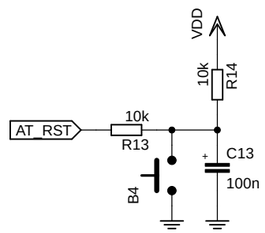
PROGRAMMING HEADER



GENERAL PURPOSE BUTTON

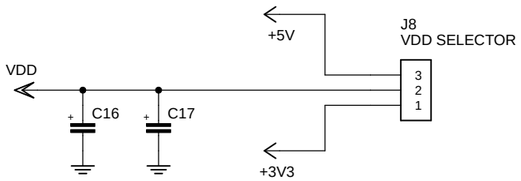


RESET BUTTON

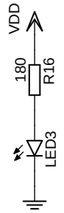


TITLE	ATmega MICROCONTROLLER	
SIZE B	DWG NO 11A-001	REV A2
SCALE NONE	SHEET 4/5	

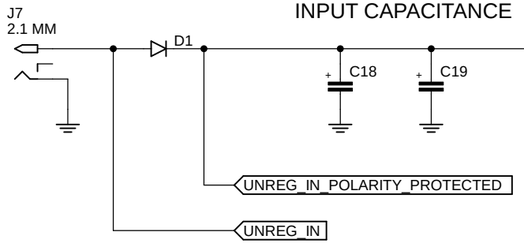
VDD SELECTION JUMPER



POWER INDICATOR LED

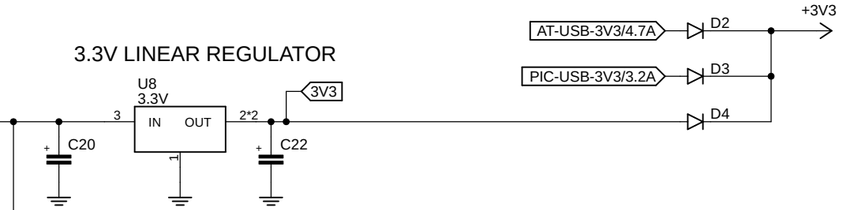


POWER STEERING

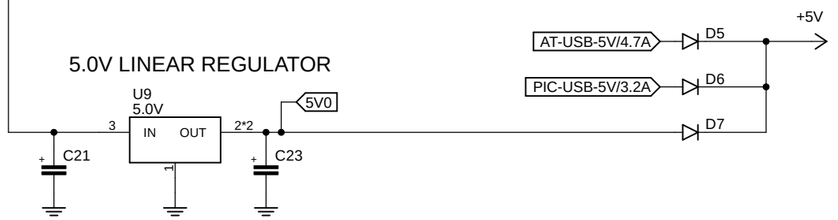


INPUT CAPACITANCE

3.3V LINEAR REGULATOR



5.0V LINEAR REGULATOR



TITLE	POWER REGULATION	
SIZE B	DWG NO 11A-001	REV A2
SCALE NONE	SHEET 5/5	