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NOTES:  
THIS PROJECT WAS CREATED IN SUPPORT OF UTSA ENGINEERING CURRICULUM.

PROJECT REVISION HISTORY:  
v1.0, AUG.2014  
INITIAL RELEASE  
  
v2.0, JAN.2015  
ADDED PROTOTYPING AREA  
  
v3.0, AUG.2015  
ADDED HIGH-SIDE TRANSISTOR SWITCH  
ADDED ABC CAPACITANCE TOUCH BUTTONS  
  
v4.0, AUG.2018  
CONVERTED TO SURFACE MOUNT  
REPLACED ABC WITH UTSA FOR CAPACITANCE TOUCH BUTTONS  
  
v5.1, JAN.2022  
EXPANDED TO 100 x 100 mm  
ADDED ATmega328P-AU  
REMOVED HIGH-SIDE TRANSISTOR SWITCH  
  
v5.2, MAY.2022  
ADDED ATMEGA BUTTON  
CHANGED OLED DISPLAY  
ADDED DTR CAPACITOR  
  
v5.3, JUNE.2023  
ADDED TO miniPCB PROJECT: PN 11A-001, REV A1  
UPDATED ATmega328P-AU PIN BREAKOUTS  
UPDATED FOOTPRINTS AND SILKSCREEN FOR HIGHER DENSITY LAYOUT  
  
REV A2, MARCH.2024  
ADDED BYPASS CAPACITOR TO AREF SIGNAL ON ATmega328P-AU

REV	DESCRIPTION	ECO	DATE
A1	INITIAL RELEASE	1023	04NOV2023
A2	ADDED C24	1024	

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B

C

D

E

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IS PROVIDED FOR EDUCATIONAL USE ONLY.

DATETIME STAMP	
3/7/2024 5:11 PM	
DR	DATE
N. MANTEUFEL	06MAR2024
ENG	DATE
P. MORTON	
QA	DATE

TITLE

VIVA UTSA BOARD

PRINTED CIRCUIT BOARD  
COMPUTING  
MICROCONTROLLER BOARD  
PIC16F1829, ATmega328P-AU

SIZE	DWG NO	REV
B	11A-001	A2

SCALE	SHEET
NONE	1/5

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