

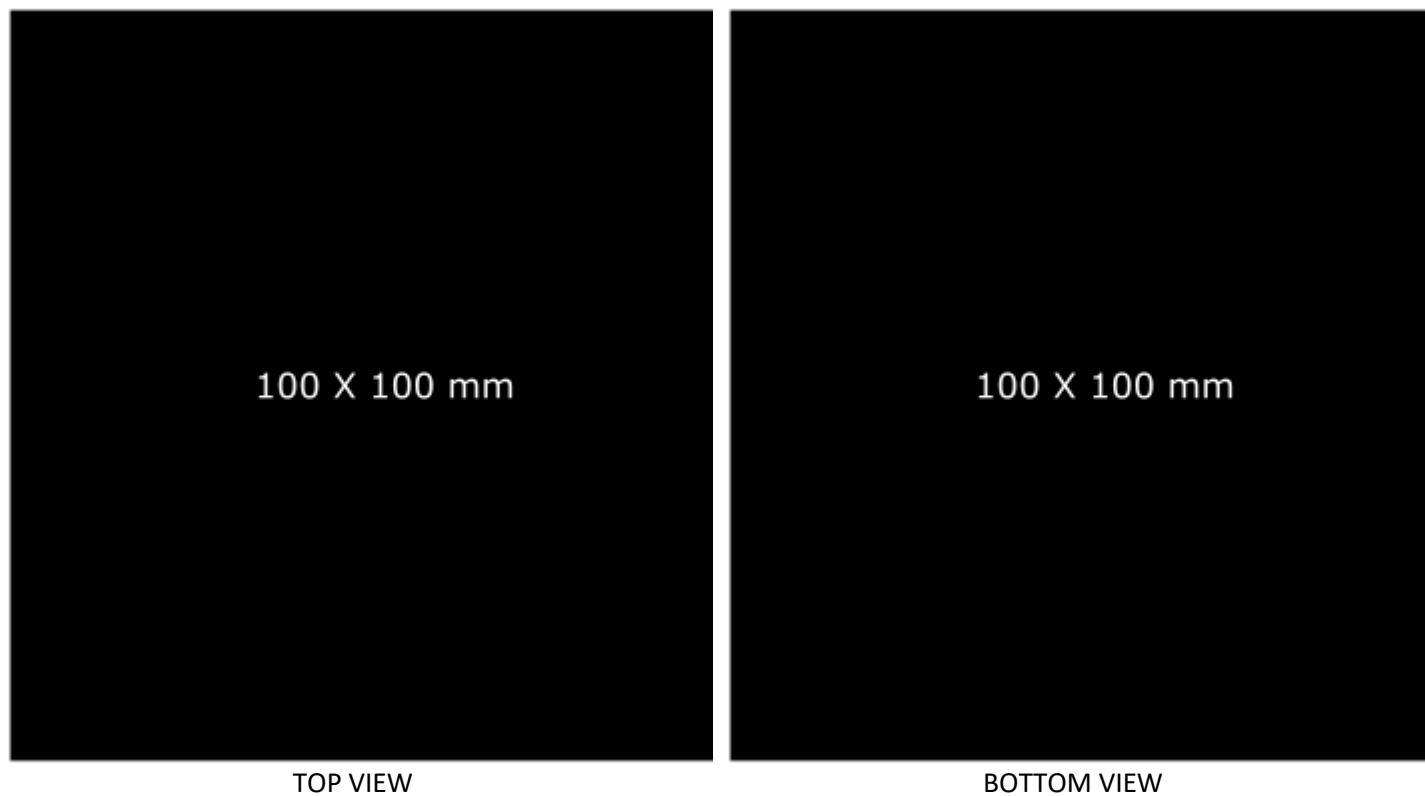
Opamp Non-Inverting Amplifier

PART NUMBER	04A-010
GROUP NAME	Opamp Amplifiers (04A)
CIRCUIT NAME	Opamp Non-Inverting Amplifier
VARIANT DESCRIPTION	Single Supply, THD, DC Bias Trimmer, Testpoints
BOARD DESIGN	PCB50-A-07
PRODUCT DESCRIPTION	Panel of #04A-010 miniPCBs, v-scored (1 Panel = 4 Pieces)

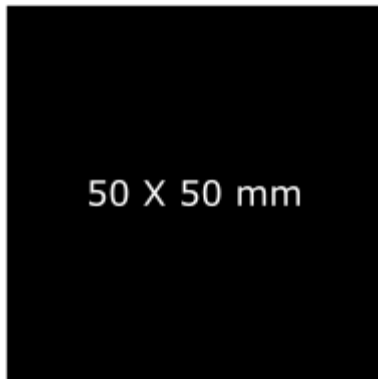
Circuit Description

This circuit amplifies a small voltage signal.

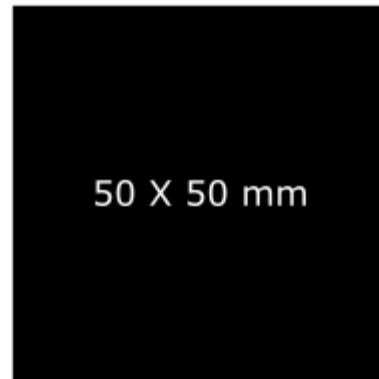
Panel Board



Single Board

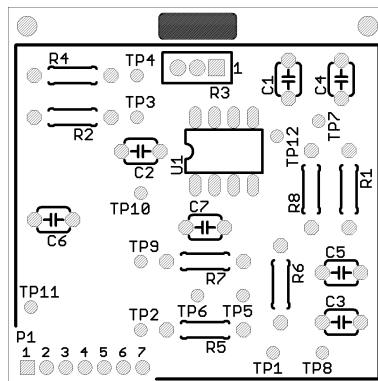


TOP VIEW

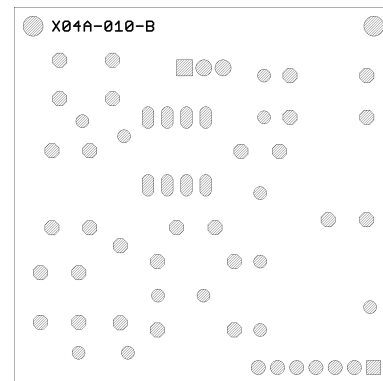


BOTTOM VIEW

Part Locations

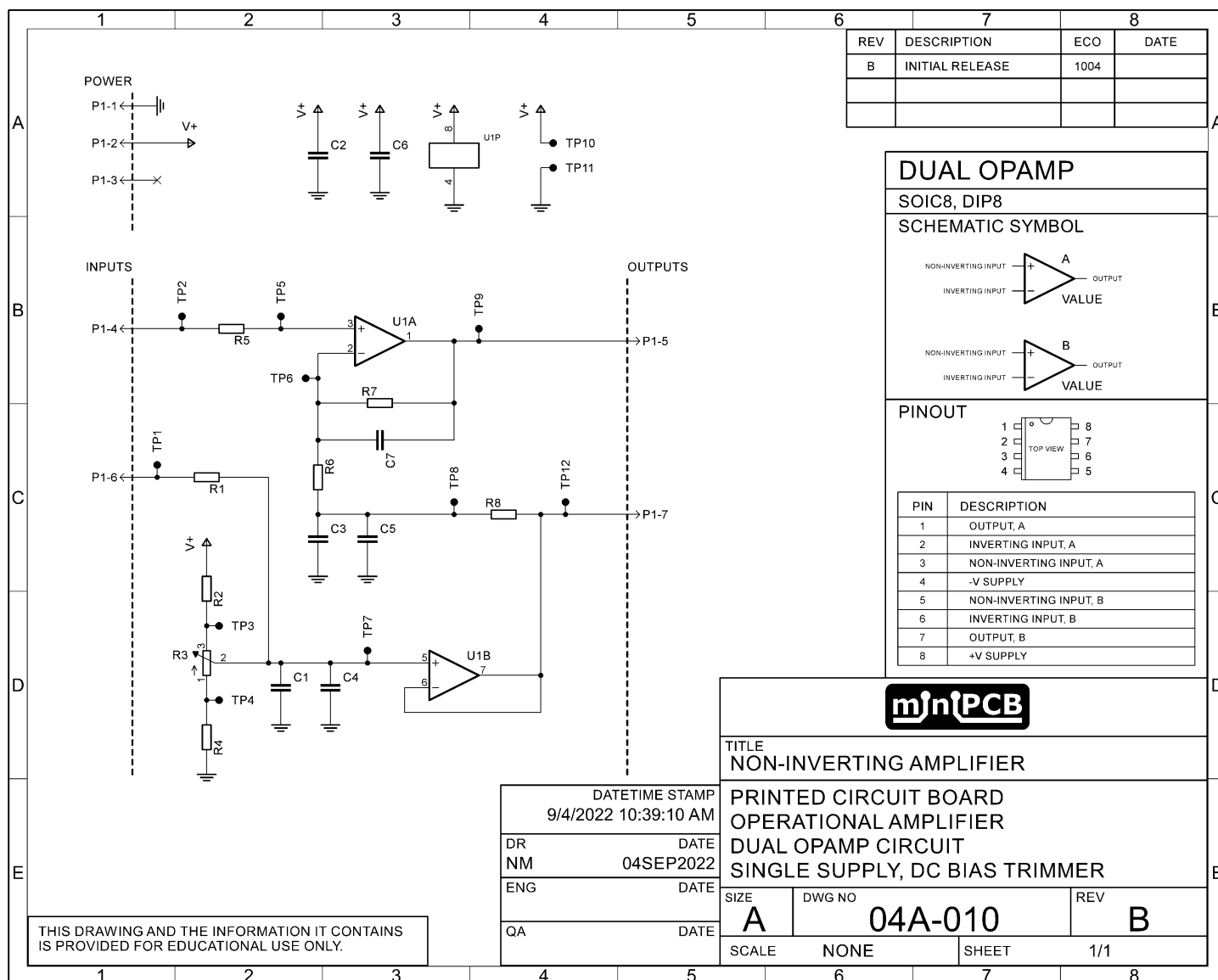


TOP VIEW



BOTTOM VIEW

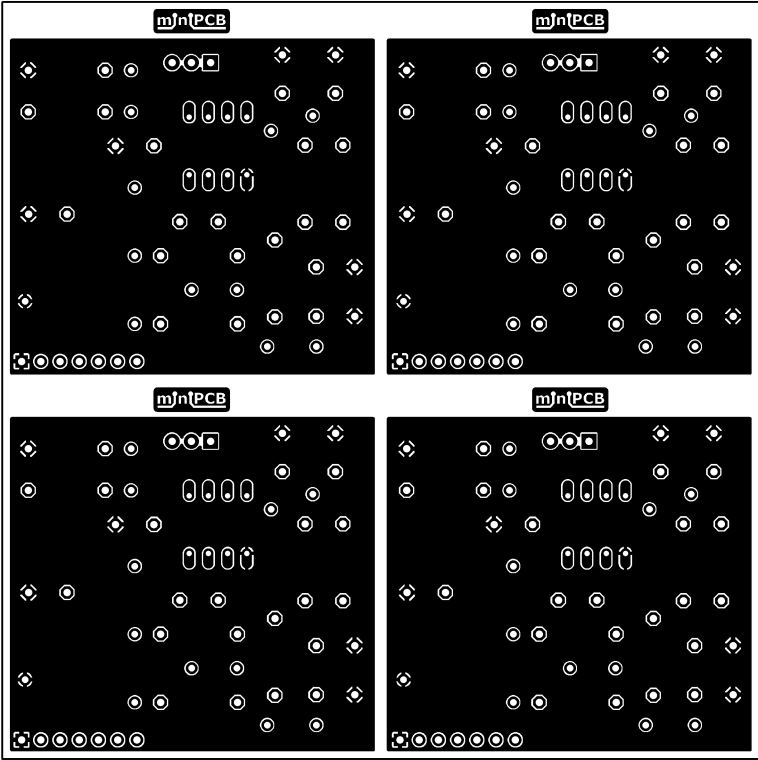
Schematic




Gerber Files

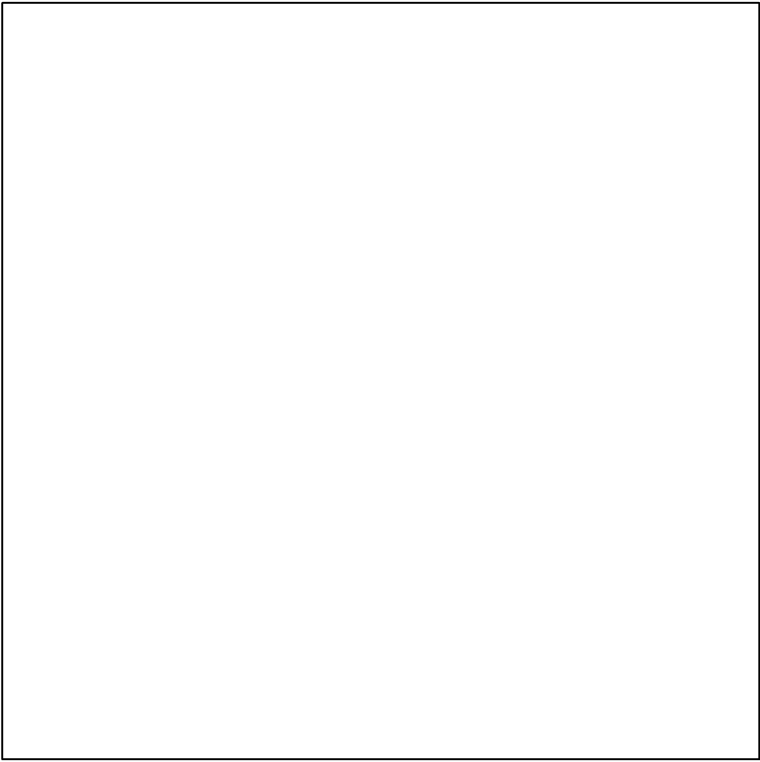

This section contains images of the layers included in each Gerber file.

TOP COPPER (GLTX)




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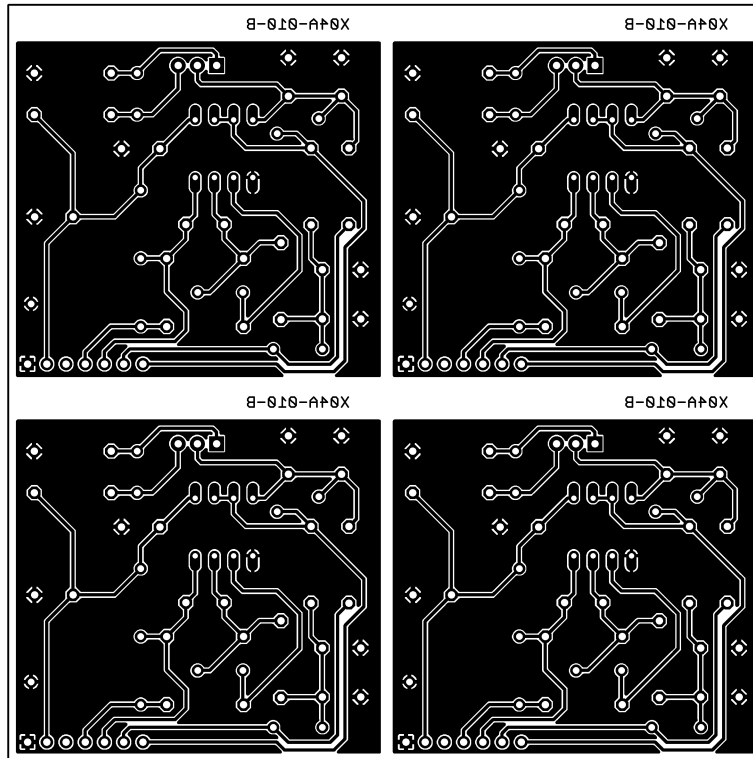
TOP CREAM (GCTX)

			
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BOTTOM CREAM (GCBX)

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					DWG NO X04A-010-B	FILE EXT GCBX

BOTTOM COPPER (GLBX)



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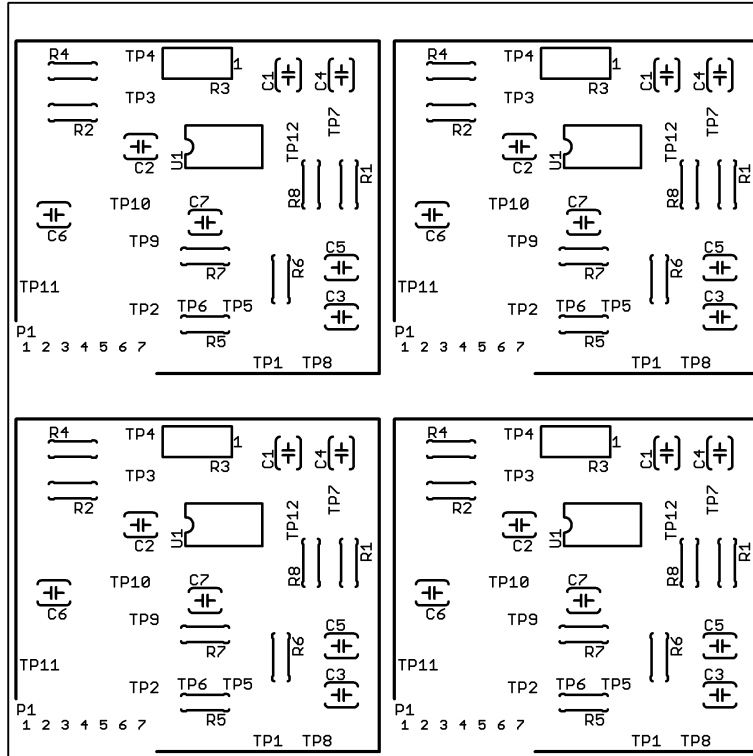
DWG NO

X04A-010-B

FILE EXT

GLBX

TOP SILKSCREEN (GOTX)



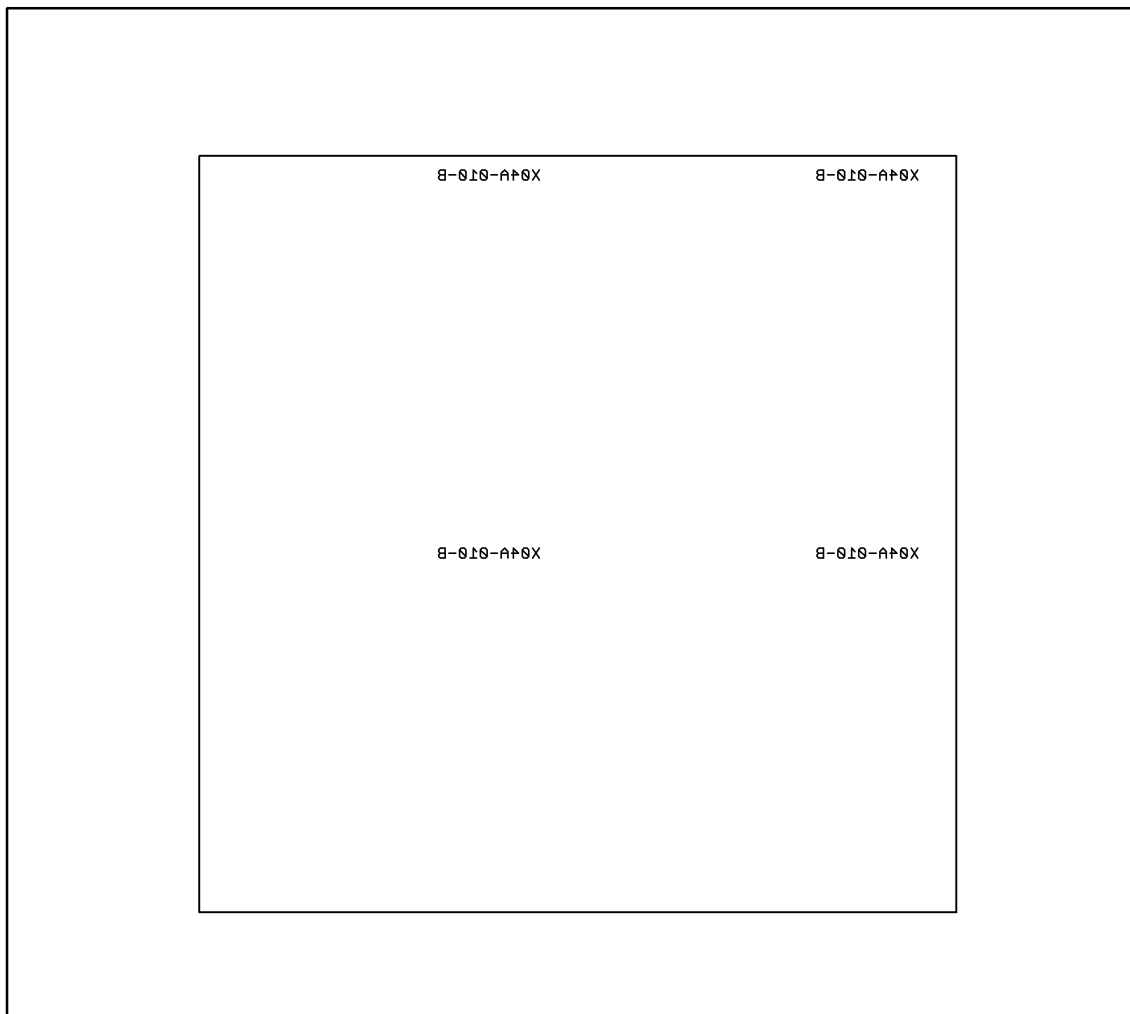

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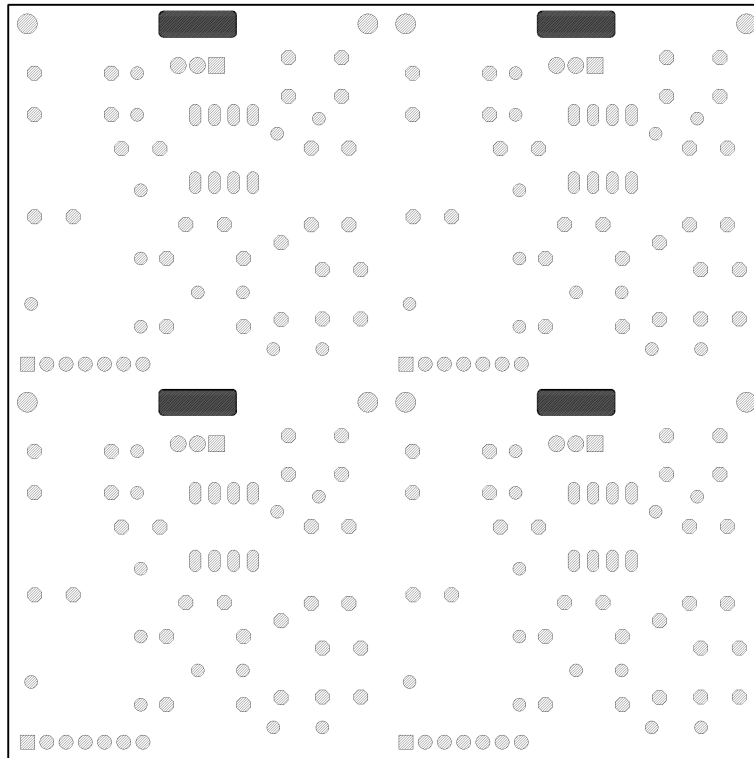
DWG NO
X04A-010-B

FILE EXT
GOTX

BOTTOM SILKSCREEN (GOBX)

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			GOBX	

TOP SOLDERMASK (GSTX)



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FILE DESCRIPTION

TOP SOLDERMASK

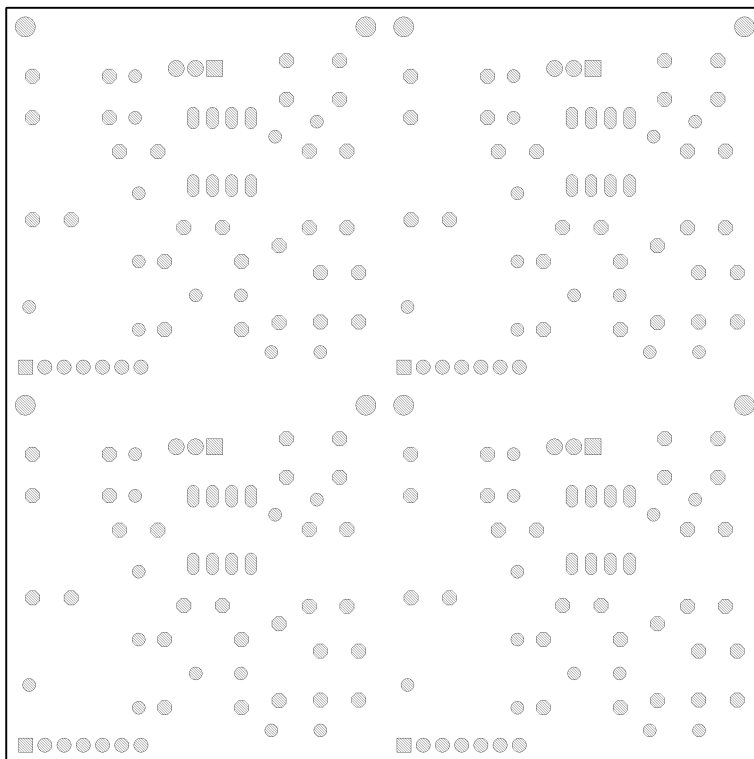
DWG NO

X04A-010-B

FILE EXT

GSTX

BOTTOM SOLDER MASK (GSBX)



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FILE DESCRIPTION

BOTTOM SOLDERMASK

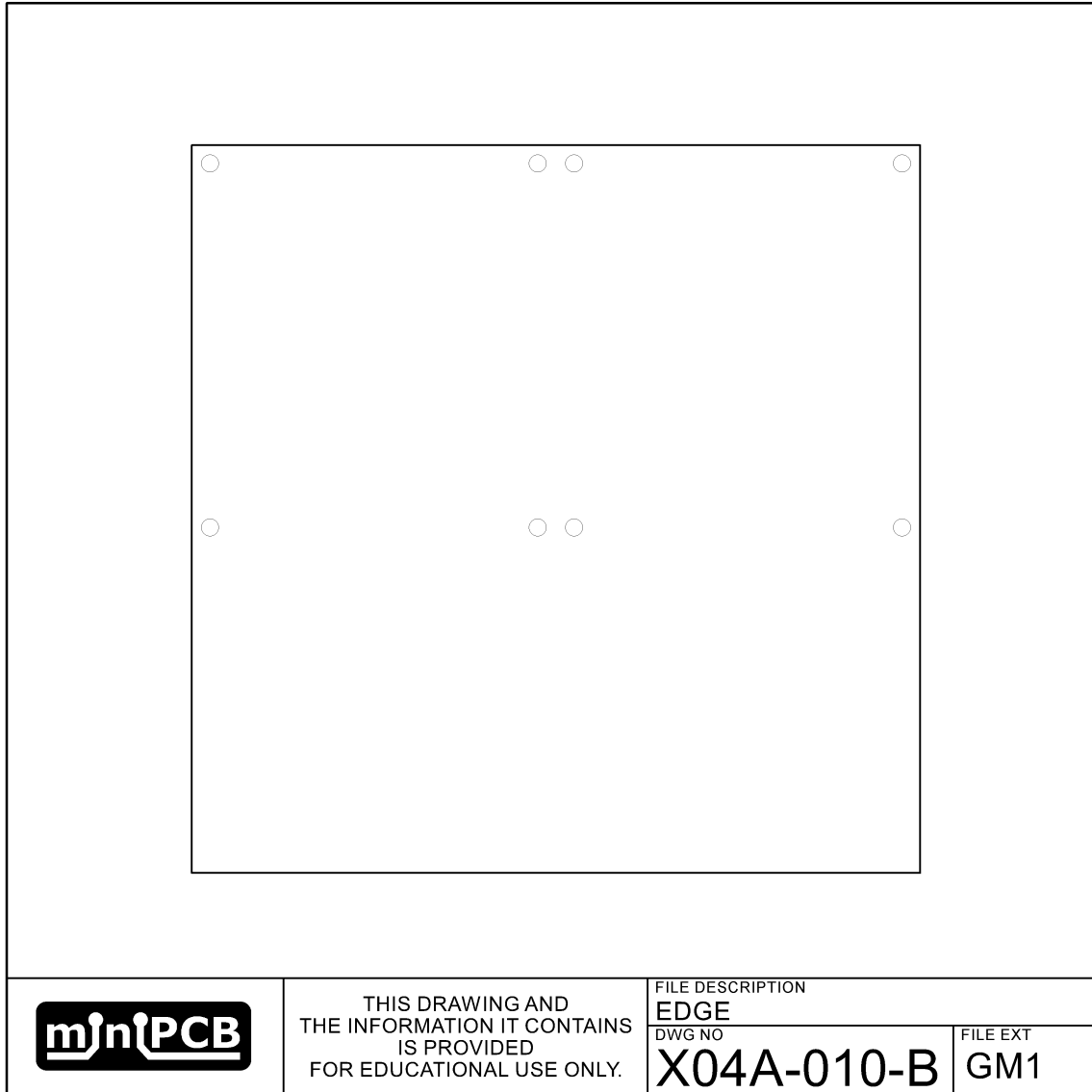
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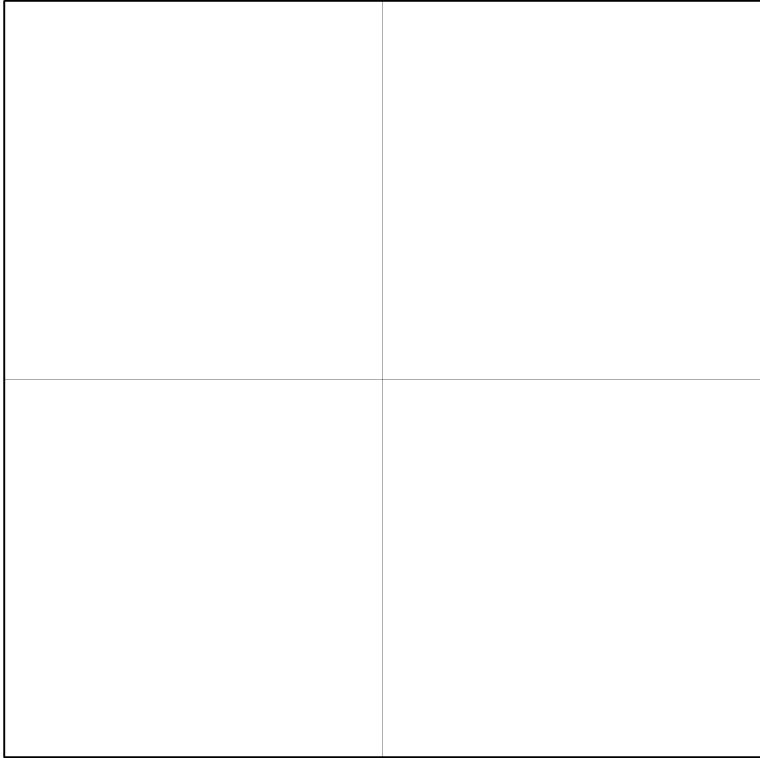

FILE EXT

GSBX

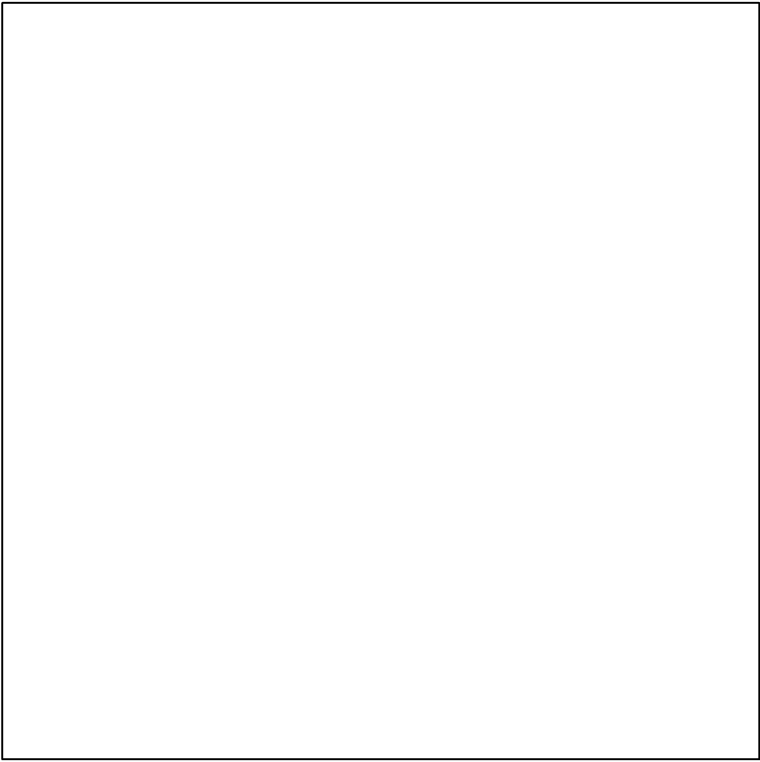

EDGE (GM1)



VSCORE (GM2)

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		V-SCORE	
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		X04A-010-B	GM2

MILLING (GM3)

		
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		<p>DWG NO</p> <p>X04A-010-B</p>

Design Inputs

Design Requirements Form

POWER REQUIREMENTS

PARAMETER NAME	SYMBOL	UNITS	LOWER LIMIT	TARGET VALUE	UPPER LIMIT
Positive DC Supply	+V	V			
Negative DC Supply	-V	V			

STIMULI REQUIREMENTS

PARAMETER NAME	SYMBOL	UNITS	LOWER LIMIT	TARGET VALUE	UPPER LIMIT
Signal Voltage, Peak to Peak	V_s	V			
Signal Frequency	f_s	Hz			
Common Mode	V_{cm}	V			
Source Impedance	R_s	Ω			

PERFORMANCE CHARACTERISTICS

PARAMETER NAME	SYMBOL	UNITS	LOWER LIMIT	TARGET VALUE	UPPER LIMIT
Quiescent Current	I_q	A			
Voltage Gain	A_v	V/V			
Current Gain	A_i	A/A			
Power Gain	A_p	P/P			
Input Impedance	R_i	Ω			
Output Impedance	R_o	Ω			

Design Outputs

Parts List Form

REF DES	PART TYPE	MFG PART NUMBER	PART DESCRIPTION	FIND
				1
				2
				3
				4
				5
				6
				7
				8
				9
				10
				11

Testing Plans

Developmental Testing

1. Plan each calibration and service test.
2. Predict expected values for each test measurement.
3. Determine if expected values satisfy design requirements.
4. Assemble a prototype that is representative of what might be the final design.
5. Perform the calibration and service testing plans.
6. Determine if the design outputs satisfy design requirements.

Calibration and Service Testing

1. With power off, measure resistances between each pin.
2. If measured resistances are not as expected, end testing fail, components need to be replaced.
3. With power on, measure voltages at each pin.
4. If measured voltages are not as expected, end testing fail, components need to be replaced.
5. With power on, adjust potentiometer PX such that the voltage at test point TPX is ##.
6. If measured voltages cannot be adjusted to an expected value, end testing fail, components need to be replaced.
7. With power on, apply stimuli and measure outputs.
8. If measured output signals are not as expected, end testing fail, components need to be replaced.
9. If measured output signals are as expected, end testing pass.-

Design Example

Design Inputs

POWER REQUIREMENTS

PARAMETER NAME	SYMBOL	UNITS	LOWER LIMIT	TARGET VALUE	UPPER LIMIT
Positive DC Supply	+V	V	4.9	5	5.1
Negative DC Supply	-V	V			

STIMULI REQUIREMENTS

PARAMETER NAME	SYMBOL	UNITS	LOWER LIMIT	TARGET VALUE	UPPER LIMIT
Signal Voltage, Peak to Peak	V_s	V	0.015	0.02	0.025
Signal Frequency	f_s	Hz			
Common Mode	V_{cm}	V			
Source Impedance	R_s	Ω			

PERFORMANCE CHARACTERISTICS

PARAMETER NAME	SYMBOL	UNITS	LOWER LIMIT	TARGET VALUE	UPPER LIMIT
Quiescent Current	I_q	A			
Voltage Gain	A_v	V/V			
Current Gain	A_i	A/A			
Power Gain	A_p	P/P			
Input Impedance	R_i	Ω			
Output Impedance	R_o	Ω			

Design Outputs

PARTS LIST

QTY REQ	REFERENCE DESIGNATORS	MFG PART NUMBER	PART DESCRIPTION	FIND
3	R1, R2, R5		RESISTOR, 1.5K, 1/4W, 1%	1
2	R3, R4		100	2
1	Q1		2N2222	3
1	C1		10u	4
1	C2		1u	5
1	C3		0.1u	6

Developmental Tests per Example

Test Report per Example

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Revision History

REV	DESCRIPTION	ECO	DATE
A	Initial Release		