TEST IDENTIFICATION

TEST NAME

Capacitance Value, C1

TEST NO*	T11	**UUT PN**	04A-005
LAST TEST NO	T10	**TEST TYPE**	STATIC

COMPONENTS UNDER TEST

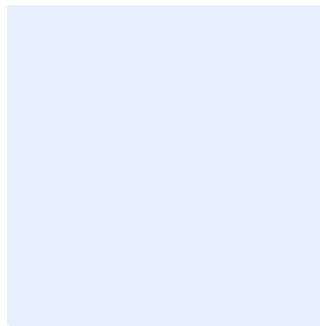| REF DES	TYPE	PARAMETER	FAILURE MODE	TEST LIMIT
C1	CAPACITOR (GENERAL)	CAPACITANCE	Out of Tolerance: HIGH	UPPER
			Out of Tolerance: LOW	LOWER
	N/A	N/A	N/A	N/A
			N/A	N/A
	N/A	N/A	N/A	N/A
			N/A	N/A
	N/A	N/A	N/A	N/A
			N/A	N/A
	N/A	N/A	N/A	N/A
			N/A	N/A
	N/A	N/A	N/A	N/A
			N/A	N/A
	N/A	N/A	N/A	N/A
			N/A	N/A
	N/A	N/A	N/A	N/A
			N/A	N/A
	N/A	N/A	N/A	N/A
			N/A	N/A
	N/A	N/A	N/A	N/A
			N/A	N/A
	N/A	N/A	N/A	N/A
			N/A	N/A
	N/A	N/A	N/A	N/A
			N/A	N/A
	N/A	N/A	N/A	N/A
			N/A	N/A
	N/A	N/A	N/A	N/A
			N/A	N/A

TEST PROCEDURE

PURPOSE

Verify capacitor is within expected resistance tolerance.

SCOPE

Handheld multimeter measurement at test points.
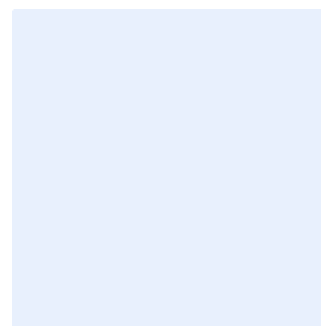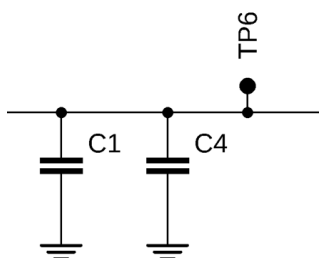
N/A

POWER
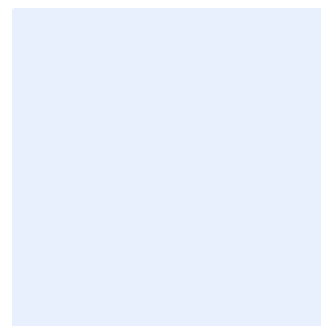
None
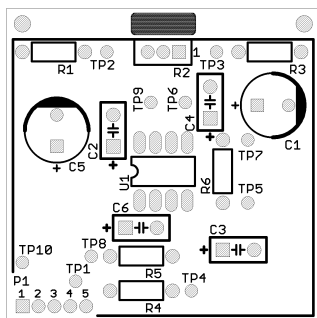
STIMULI

None

MEASUREMENT

Measure capacitance across capacitor.

SIGNAL PIN TP6

RETURN PIN TP10

ACCEPTANCE CRITERIA

PARAMETER	LOWER LIMIT	TARGET VALUE	UPPER LIMIT	UNITS
CAPACITANCE	42.3 uF	47 uF	51.7 uF	FARADS

TEST RESULTS

RESULT	NEXT STEP	ADJUST	REPLACE
IN TOLERANCE	T12	N/A	N/A
OOT HIGH	END FAIL	N/A	C1
OOT LOW	END FAIL	N/A	C1
OOT HIGH AND UNABLE TO ADJUST	N/A	N/A	N/A
OOT LOW AND UNABLE TO ADJUST	N/A	N/A	N/A

THIS SPACE INTENTIONALLY BLANK

FORM CONTROLS

Save	Save and Close	Close Without Saving	Delete

Log Entry Key	20230205194445
Computer ID	desktop-jti13j1
User ID	nolan
Date Time	20230205194445
PDF	C:\Repos\miniPCB\EAGLE\miniPCB\04\A\04A-005\TESTbase v2\Tests\PDFs\T11.pdf