

Common Emitter Amplifier

By Nolan Manteufel with ChatGPT

A common emitter amplifier is a type of bipolar junction transistor (BJT) amplifier configuration that is widely used in electronic circuits to amplify signals. It's called a "common emitter" because the emitter terminal of the transistor is common to both the input and output of the amplifier. This configuration provides voltage amplification, high input impedance, and low output impedance.

Here's the theory of operation for a common emitter amplifier:

Components: The common emitter amplifier consists of an NPN transistor (like the 2N3904 or 2N2222), resistors, and a power supply.

Biasing: Proper biasing is essential to ensure the transistor operates in its active region. This typically involves setting a DC voltage at the base terminal (V_{bb}) and at the collector terminal (V_{cc}) while connecting the emitter to a common ground (0V or GND). Biasing sets the quiescent operating point (Q-point) of the transistor.

Signal Input: The input signal is applied to the base terminal (V_{in}). This AC signal is superimposed on the DC bias voltage. The input resistor (R_b) limits the base current to protect the transistor and helps to match the input impedance of the amplifier.

Transistor Action: The transistor amplifies the input signal. When a positive voltage is applied at the base (V_{in} increases), it causes an increase in the base current. This leads to an increase in collector current due to the transistor's current gain (Beta or h_{fe}). The collector current flows from the collector to the emitter.

Collector Load Resistor: A collector load resistor (R_c) is connected to the collector terminal. It converts the change in collector current into an amplified output voltage. The voltage across R_c is the amplified signal (V_{out}).

Emitter Resistor (Optional): In some cases, an emitter resistor (R_e) is added to stabilize the DC biasing and to provide negative feedback, which helps in improving the linearity and stability of the amplifier.

Output Signal: The amplified signal appears at the collector terminal. The output is typically taken as the voltage across the collector resistor ($V_{out} = I_c * R_c$).

Output Coupling Capacitor (Optional): If a DC component is present at the collector, it is blocked using an output coupling capacitor (C_{out}) to ensure that only the AC component of the signal is passed to the next stage of the circuit.

Gain and Phase Inversion: The common emitter amplifier provides a voltage gain that is approximately equal to the ratio of the collector resistor to the emitter resistor ($A_v \approx -R_c / R_e$). Additionally, the output signal is phase-inverted with respect to the input signal.

Common emitter amplifiers are widely used due to their high voltage gain and can be found in various applications like audio amplifiers and intermediate frequency (IF) amplifiers in radio receivers. The biasing and component values need to be carefully chosen to ensure proper operation and meet specific performance requirements.

Change and Liability Notice

This document is subject to change without notice. While effort has been made to ensure the accuracy of the material contained within this document, Nolan Manteufel shall under no circumstances be liable for incidental or consequential damages or related expenses resulting from the use of this document.

Trademark Notice

miniPCB is a trademark of Nolan Manteufel. This document does not constitute permission to use the miniPCB trademark.

WORDMARK	FIGUREMARK	FIGUREMARK
miniPCB™		

Revision History

REV	DESCRIPTION	ECO	DATE
A	Initial Release	N/A	08NOV2023