Pin	Pin	Pin	5V nin	I		T						1									
N°	Pin Name	Туре	5V pin Tolerant	Functions	AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15	Default Function HydraBus
60 B	00Т0		-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	Default Boot from Flash (pull-down resistor 2.2Kohms)
7 N	RST	VO	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	Reset Button / SWD_DEBUG
																					UBTN (User Button 0)
14 (F	A0-WKUP PA0)	νo	YES	ADC123_IN0 / WKUP(4)	-	TIM2_CH1 TIM2_ETR	TIM 5_CH1	TIM8_ETR		-	-	USART2_CTS	UART4_TX	-	-	ETH_MII_CRS	-	-	-	EVENTOUT	Can be disabled by removing Jumper JMP1 UBTN
15 P	A1	l/O	YES	ADC123 IN1	_	TIM2 CH2	TIM5 CH2	_		_		USART2 RTS	UART4_RX	_		ETH_MII_RX_CLK ETH_RMII_REF_CLK	_			EVENTOUT	ADC123_IN1
16 P	A2	1/0	YES	ADC123_IN2 ADC123_IN3	-	TIM2_CH3 TIM2_CH4	TIM5_CH3 TIM5_CH4	TIM9_CH1	-	-		USART2_TX USART2_RX	-	-	OTG_HS_ULPI_D0	ETH_MDIO ETH_MII COL	-	-	-	EVENTOUT	USART2_TX
17 P	A3	I/O	YES	ADC123_IN3	-	TIM2_CH4	IIM5_CH4	TIM9_CH2	-	-		USAR12_RX	-	-	OTG_HS_ULPI_DU	ETH_MII_COL	-			EVENTOUT	USAR12_RX
				ADC40 INIA/							CDI2 NCC										ULED (User LED 0) Can be disabled by removing Jumper JMP1 ULED
20 P	A4	ľΟ	NO (3.3V)	ADC12_IN4 / DAC1_OUT	-	-	-	-	-	SPI1_NSS	SPI3_NSS I2S3_WS	USART2_CK	-	-	-	-	OTG_HS_SOF	DCMI_HSYNC	-	EVENTOUT	JMP1 ULED
	A5	lο	NO (3.3V)	ADC12_IN5 / DAC2_OUT	_	TIM2_CH1 TIM2_ETR		TIM8 CH1N		SPI1 SCK			_		OTG_HS_ULPI_CK			_		EVENTOUT	DAC2_OUT
22 P	A6	I/O	YES	ADC12_IN6	-	TIM1_BKIN	TIM3_CH1	TIM8_BKIN	-	SPI1_MISO	-	-	-	TIM13_CH1		-	-	DCMI_PIXCK	-	EVENTOUT	ADC12_IN6
23 P	A7	lo	YES	ADC12_IN7	-	TIM1_CH1N	TIM3_CH2	TIM8_CH1N	-	SPI1_MOSI	-	-	-	TIM14_CH1	-	ETH_MII_RX_DV ETH_RMII_CRS_DV	-	-	-	EVENTOUT	ADC12_IN7
41 P 42 P 43 P	A8 A9	I/O	YES	OTG_FS_VBUS	MCO1	TIM1_CH1 TIM1_CH2		-	I2C3_SCL I2C3_SMBA	-		USART1_CK USART1_TX	-	-	OTG_FS_SOF	-	-	DCMI D0	-	EVENTOUT EVENTOUT	USART1_CK USART1_TX
43 P	A10	I/O	YES		-	TIM1_CH3	-	-		-	-	USART1_RX	-	-	OTG_FS_ID	-	-	DCMI_D1	-	EVENTOUT	USART1_RX
44 P	A11	lo	YES		_	TIM1_CH4	-	-		-	-	USART1_CTS	_	CAN1_RX	OTG_FS_DM	-	-	_	-	EVENTOUT	USB D- (USB0 connector)
45 P	A12	l/O	YES			TIM1_ETR						USART1_RTS		CAN1_TX	OTG_FS_DP				L	EVENTOUT	USB D+ (USB0 connector)
P	A13 ITMS-SWDIO)	-										DOJUNI I INIO		Orati_IX	0.00,000						
			YES		JTMS-SWDIO	-	-	-	-	-	-	-	-	-	-	-	-	-	-	EVENTOUT	SWDIO
49 (J	A14 ITCK-SWCLK)	VΟ	YES		TCK-SWCLK	-	-	-	-	-	-	-	-	-	-	-	-	-	-	EVENTOUT	SWCLK
50 (J	A15 ITDI)		YES		JTDI	TIM 2_CH1 TIM 2_ETR TIM1_CH2N	-	-	-	SPI1_NSS	SPI3_NSS/ I2S3S_WS		-	-	ļ.	-		-	-	EVENTOUT	SPI1_NSS
26 P	B0 B1	1/0	YES YES	ADC12_IN8 ADC12_IN9	-	TIM1_CH2N TIM1_CH3N	TIM3_CH3	TIM8_CH2N	-			-	-	-	OTG_HS_ULPI_D1 OTG_HS_ULPI_D2	ETH_MII_RXD2 ETH_MII_RXD3	-		-	EVENTOUT EVENTOUT	ADC12_IN8
P	B2-BOOT1			PO IZ_INS		- mi_Chain	. IIVIO_CFI4	. awo_cnan					<u> </u>		O TO TIO OLPI DZ	E.I.I_WIII_IXAD3			Ĺ		
	PB2)	1/0	YES		-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	EVENTOUT	
	B3 ITDO/ RACESWO)	L	VEC		JTDO/	TIMO C: 10				CDM COV	SPI3_SCK I2S3_CK									D/ENTO::-	enu eck
P	R4		YES		TRACESWO	TIM2_CH2		-	-	SPI1_SCK			-	-	-	-	-		H	EVENTOUT	
56 (1	IJTRST)	ľΟ	YES		NJTRST	-	TIM3_CH1	-	-	SPI1_MISO	SPI3_MISO	I2S3ext_SD	-	-	-	-	-	-	-	EVENTOUT	SPI1_MISO
	B5		YES		_	_	TIM3_CH2	-	I2C1_SMBA	SPI1_MOSI	SPI3_MOSI I2S3_SD	-	_	CAN2_RX	OTG_HS_ULPI_D7	ETH_PPS_OUT	-	DCMI_D10	-	EVENTOUT	SPI1_MOSI
58 P 59 P	B6 B7	I/O I/O	YES		-	-	TIM4_CH1 TIM4_CH2	-	I2C1_SCL I2C1_SDA	-	-	USART1_TX USART1_RX	-	CAN2_TX	-	-	- FSMC_NL	DCMI_D5 DCMI_VSYNC	-	EVENTOUT EVENTOUT EVENTOUT	I2C1_SCL I2C1_SDA
61 P	B8	1/0	YES		-		TIM4_CH3	TIM10_CH1	I2C1_SCL	-		-		CAN1_RX	-	ETH_MII_TXD3	SDIO_D4	DCMI_D6	-	EVENTOUT	CAN1_RX
62 P	В9	νo	YES				TIM4_CH4	TIM11_CH1	I2C1_SDA	SPI2_NSS I2S2_WS	-			CAN1_TX		-	SDIO_D5	DCMI_D7	-	EVENTOUT	CAN1_TX
			YES			TIM2_CH3			I2C2_SCL	SPI2_SCK I2S2_CK		USART3_TX			OTG_HS_ULPI_D3	ETH_MII_RX_ER				EVENTOUT	SPI2_SCK
										1232_CR						ETH_MII_TX_EN ETH_RMII_TX_EN			f		1202_CR
30 P	B11	I/O	YES		-	TIM2_CH4	-	-	I2C2_SDA	CDI2 NCC	-	USART3_RX	-	-	OTG_HS_ULPI_D4	ETH_RMII_TX_EN	-	-	-	EVENTOUT	USB ID
33 P	B12	lο	YES		-	TIM1_BKIN	-	-	I2C2_SMBA	SPI2_NSS I2S2_WS	-	USART3_CK	-	CAN2_RX	OTG_HS_ULPI_D5	ETH_MII_TXD0 ETH_RMII_TXD0	OTG_HS_ID	-	-		(USB1 connector)
34 P	B13	lo	YES	OTG_HS_VBUS	_	TIM1_CH1N				SPI2_SCK I2S2_CK		USART3_CTS		CAN2_TX	OTG_HS_ULPI_D6	ETH_MII_TXD1 ETH_RMII_TXD1	_			EVENTOUT	Supply VUSB (USB1 connector)
			YES			TIM1 CH2N		TIL 10 OLION		SPI2 MISO	I2S2ext_SD			TIM12 CH1			OTG_HS_DM				USB D- (USB1 connector)
	B14	-			-			TIM8_CH2N	-	SPI2_MISO SPI2_MOSI I2S2_SD	I2S2ext_SD	USART3_RTS	-			-					USB D+
36 P	B15 C0 C1	VO	YES YES	ADC123_IN10	RTC_50Hz	TIM1_CH3N	-	TIM8_CH3N	-	12S2_SD	-	-	-	TIM12_CH2	OTG HS ULPI STP	-	OTG_HS_DP	-	-	EVENTOUT	(USB1 connector) ADC123_IN10 SPI2 CS (by SW)
9 P	C1	VO VO	YES YES	ADC123_IN11 ADC123_IN12	-	-			-	SPI2 MISO	- I2S2ext SD	ŧ .	-	-		ETH_MDC ETH _MII_TXD2	-	-	E	EVENTOUT EVENTOUT	SPI2 CS (by SW)
		-			-	-		-	-	SPI2_MISO SPI2_MOSI I2S2_SD	IZSZEXT_SD		-	-			-		H		
11 P	C3	I/O	YES	ADC123_IN13	-	-	-	-	-	12S2_SD		-	-	-	OTG_HS_ULPI_NXT	ETH_MII_TX_CLK	-	-	-	EVENTOUT	SPI2_MOSI
24 P	C4	lo	YES	ADC12_IN14	-	-	-	-	-	-	-	-	-	-	-	ETH_MII_RXD0 ETH_RMII_RXD0	-	-	-	EVENTOUT	ADC12_IN14
25 P	C5	νo	YES	ADC12_IN15							-					ETH_MII_RXD1 ETH_RMII_RXD1			-	EVENTOUT	ADC12_IN15
25 P 37 P 38 P	C6	I/O	YES YES		-		TIM3_CH1 TIM3_CH2	TIM8_CH1 TIM8_CH2	-	I2S2_MCK	- 12S3_MCK	-	USART6_TX USART6_RX	-			SDIO_D6 SDIO_D7	DCMI_D0 DCMI_D1	-	EVENTOUT	USART6_TX USART6_RX
																					MicroSD Card SD 4bits
39 P	C8		YES				TIM3_CH3	TIM8_CH3					USART6_CK				SDIO_D0	DCMI_D2		EVENTOUT	MicroSD Card SD 4bits
40 P	C9	ΙΟ	YES		MCO2	-	TIM3_CH4	TIM8_CH4	I2C3_SDA	I2S_CKIN							SDIO_D1	DCMI_D3		EVENTOUT	MicroSD Card SD 4bits SDIO_D1
51 P	C10	l/O	YES		-					-	SPI3_SCK/ I2S3S_CK	USART3_TX/	UART4_TX				SDIO_D2	DCMI_D8	-	EVENTOUT	MicroSD Card SD 4bits SDIO_D2
	C11		YES							I2S3ext SD	SPI3 MISO/	USART3_RX	UART4_RX				SDIO D3	DCMI_D4		EVENTOUT	MicroSD Card SD 4bits SDIO_D3
										-COUCKLOD	SPI3_MISO/ SPI3_MOSI I2S3_SD										MicroSD Card SD 4bits
53 P		I/O	YES	EVENTOUT /	-		-	-	-	-	12S3_SD	USART3_CK	UART5_TX	-	-	-	SDIO_CK	DCMI_D9	-	EVENTOUT	SDIO_CK
2 P	C13	ΙΟ	YES	RTC_AF1	-	-	-	-	-	-	-	-	-	-	-	-	-	-	Ŀ	-	
3 P	C14-OSC32_IN PC14)	lνο	YES	EVENTOUT / OSC32 IN(4)		L		-	1	-	-			-	ļ.	_	_		-	-	Optional RTC 32,768KHz
	C15- SC32_OUT																				
4 (F	SC32_OUT PC15)	νo	YES	EVENTOUT / OSC32_OUT(4)	-	-			-	-		-	<u> </u>			-		-		-	Optional RTC 32,768KHz
54 0	D2	vo	YES				TIM3_ETR						UART5_RX				SDIO_CMD	DCMI_D11		EVENTOUT	MicroSD Card SD 4bits SDIO_CMD
Р	H0-OSC IN		. 23	EVENTOUT /			THO_CIR						G-AKTO_KK				OD/O_OMD	DOMI_DIT		LVEIVIOUI	
5 (F	PH0) H1-OSC_OUT	VO	YES	OSC_IN(4)				-		-				-			-				Quartz 8MHz
6 (F	H1-OSC_OUT PH1)	1/0	YES	EVENTOUT / OSC_OUT(4)	-	-		-	-	-		-	-	-		-			-		Quartz 8MHz
1 V	BAT CAP_1		-					-			-						-			-	+3V3
31 V 47 V 19 V	CAP_2		-	-	-		l I		-	-	l	ŧ .									
32 V 48 V 64 V 13 V	DD	-	-	-	-	-		-	-	-	-	-	-	-	-	-	-				-
48 V	DD	-	-		-	-		-	-	-	-	-					-	-	-	-	
13 V	DDA SS	-						-		-									-		
63 V 12 V	SS		-		-	-		-	-	-	-	-	-	-		-	-	-	E	-	-
12 V	ooA				-	-			ř	-		1					-		1		

Pin 3.3V Only !! Not 5V tolerant HydraBus pins not reusable HydraBus pins can be reused, available on J1/J2/J3 or SWD_DEBUG Default Peripheral for HydraBus HydraFW

			STM32F405RGT_pinNo																			
March Marc	Pin N°	Pin Name	Pin Type	5V pin Tolerant	Functions	AFO	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15	Default Function HydraBus
Part	1	VBAT	-	-	-	-	Ar I	- Al 2	Ar 3	A/4	-	- Al-0	-	-	- Al-3	-	AFTI	AF 12	AF13	- AF 14	- AF 10	+3V3
Property column	_	PO42		VEC	EVENTOUT /																	
March Marc	- 2		100	155			-	-	-		-	-		-	-	-	-	-		<u> </u>	-	Ontional
Part	3	(PC14)	1/0	YES	OSC32_IN(4)	-	-	-		-	-	-		-	-	-	-	-	-	-	-	RTC 32,768KHz
Part		PC15- OSC32 OUT			EVENTOUT /																	Ontional
March Marc	4		1/0	YES	OSC32_OUT(4)					-						-	-	-		-		RTC 32,768KHz
The column The	5	PH0-OSC_IN (PH0)	I/O	YES	EVENTOUT / OSC IN(4)						_	_			_	_	2			1		Quartz 8MHz
		PH1-OSC_OUT			EVENTOUT /																	
1	6	(PH1) NPST		YES	OSC_OUT(4)	-	-	-	-	-	-	-	-	-	-	-	-	-	-		-	Quartz 8MHz
1				YES	ADC123_IN10	-	-		-	-		-	-	-	-	OTG_HS_ULPI_STP	-	-		-	EVENTOUT	TRESCE BUILDING TO WE BELOW
1						-	-	-	-	-	-	-	-	-	-	-		-	-	-		
1	10	PC2	1/0	YES	ADC123_IN12	-	-	-	-	-		I2S2ext_SD	-	-	-	OTG_HS_ULPI_DIR	ETH_MII_TXD2	-	-	+ -	EVENTOUT	_
1			1/0	YES	ADC123_IN13	-	-	-		-	I2S2_SD	-		-	-	OTG_HS_ULPI_NXT	ETH_MII_TX_CLK	-	-	-	EVENTOUT	
No. Control			-	-	-	-	-	-	-	-	-	-		-	-	-	-	-	-	-	-	1-
No.	13	VDDA	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-	-		-	-	· ·
No. 1																						(User Button 0)
No. 1	14	PA0-WKUP (PA0)	1/0	YES	ADC123 IN0 / WKUP(4		TIM2_CH1 TIM2_ETR	TIM 5 CH1	TIM8 ETR				USART2 CTS	UART4 TX		_	ETH MII CRS				EVENTOUT	Can be disabled by removing Jumper JMP1 UBTN
Column													_	_			ETH MIL DY CLK					
Column	15	PA1	1/0	YES	ADC123 IN1	_	TIM2 CH2	TIM5 CH2		_		_	USART2 RTS	UART4 RX		-	ETH_RMIIREF_ CLK				EVENTOUT	
1	16		1/0	YES	ADC123_IN2	-	TIM2_CH3	TIM5_CH3	TIM9_CH1	-	-	-	USART2_TX		-	-	ETH_MDIO	-		-	EVENTOUT	
1			I/O	YES	ADC123_IN3	-	TIM2_CH4	TIM5_CH4	TIM9_CH2	-	-	-	USART2_RX	-	-	OTG_HS_ULPI_D0	ETH_MII_COL	-	-	-	EVENTOUT	
Part			<u> </u>	-	-	-	-	-		-	-	-	-		-	-	-	-	-	+ :	-	1
Part																						ULED
					ADC10 INV							epia Noc										(User LED 0)
2	20	PA4	1/0	NO (3.3V)	DAC1_OUT	-	-	-	-	-	SPI1_NSS	12S3_WS	USART2_CK	-	-	-	_	OTG_HS_SOF	DCMI_HSYNC	-	EVENTOUT	JMP1 ULED
2	24	DAE	1/0	NO (2.2) 0	ADC12_IN5 /		TIM2_CH1		TIME CHAN		enii ecii					OTO HE UI DI OV					EVENTOUT	<u> </u>
Part	22							TIM3 CH1							TIM13 CH1	OTG_HS_ULPI_CK	-		DCMI PIXCK			
1																	ETH MII RX DV		non			
10	23	PA7	1/0	YES	ADC12_IN7	-	TIM1_CH1N	TIM3_CH2	TIM8_CH1N	-	SPI1_MOSI	-	-	-	TIM14_CH1	-	ETH_RMII_CRS_DV	-	-	+-	EVENTOUT	-
Part	24	PC4	1/0	YES	ADC12_IN14	-	-			-	-	-			-	-	ETH_RMII_RXD0	-		1.	EVENTOUT	
1																	ETH _MII_RXD1					
Part						-	TIM1 CH2N	TIM3 CH3	TIMS CH2N	-	-	-	-		-	OTG HS UI PL D1		-		+ :		
2	27					-				-	-	-	-	-	-			-		-		
Part		PB2-BOOT1		1000																		
10	28	(PB2)	1/0	YES		-	-	-	-	-		-	-	-	-	-	-	-	-	+ -	EVENTOUT	
1	29	PB10	1/0	YES		-	TIM2_CH3	-	-	I2C2_SCL	12S2_CK	-	USART3_TX	-	-	OTG_HS_ULPI_D3		-	-	-	EVENTOUT	
1	20	DD44		VEC			TIMO CILIA			1000 004			LICADTA DV			OTO HE HIDI DA	ETH_MILTX_EN				D/DITOUT	
Second Color			-	-	-	-	- TIW2_C114	-	-	IZCZ_SDA	-	-	- USAK15_KX	-	-	-		-	-	-		-
Part			-	-		-	-			-	-	-		-	-	-	-	-	-	-	-	-
Part	22	DD12	1/0	VEC			TIM4 DVIN			ISCS CMBA	SPI2_NSS		HEADTS OF		CAND DV	OTO HE HIDI DE	ETH_MIL_TXD0	OTO HE ID			EVENTOUT	USB ID
Part	33	FBIZ	100	123			TIMIT_DIGIN	-	-	IZCZ_SWIDA			USAK15_CK	-	CANZ_KX	OTG_TIS_OEFT_DS		OTG_NG_ID		+		
18	34	PB13	1/0	YES	OTG_HS_VBUS	-	TIM1_CH1N	-	-	-	12S2_CK	-	USART3_CTS	-	CAN2_TX	OTG_HS_ULPI_D6	ETH_RMII_TXD1	-	-	-	EVENTOUT	
Second Column	35	PB14	1/0	YES			TIM1 CH2N		TIM8 CH2N	_	SPI2 MISO	I2S2ext SD	USART3 RTS		TIM12 CH1	_	_	OTG HS DM			EVENTOUT	
17 17 17 17 17 17 17 17																						USB D+
PC	36					RTC_50Hz	TIM1_CH3N	TIMO CHI		-	I2S2_SD	-	-	LICADTE TV	TIM12_CH2	-	-	OTG_HS_DP	DOME DO	-		(USB1 connector)
30 PGS 10 VSS			1/0	YES		-	-	TIM3_CH2	TIM8_CH2		LOL_MOR	I2S3_MCK		USART6_RX				SDIO_D7	DCMI_D1		EVENTOUT	
## PAS 10 VIS MACCO TIME CHI TIME CHI																						MicroSD Card SD 4bits
## PATS NO YES MCCS MC	39	PC8	1/0	YES		-	-	TIM3_CH3	TIM8_CH3					USAR16_CK				SDIO_D0	DCMI_D2		EVENTOUT	
E						MCO2		TIM3_CH4	TIM8_CH4	I2C3_SDA	I2S_CKIN							SDIO_D1	DCMI_D3			SDIO_D1
A					OTO ES VIDIO	MCO1		-	-	I2C3_SCL	-	-		-	-	OTG_FS_SOF	-	-	DOME DO	-		
44 PA11 10 VES	43				OTG_FS_VBUS					.203_SMBA						OTG_FS_ID			DCMI_D1	±÷		
## PAIS 10 YES		DA44	1/0	VEC											CAN'S DY						EVENTOUT	USB D-
A	44	PATT	1/0	TES				-					USAR IT_CIS		CAN1_RX	UIG_FS_DM	-			1	EVENTOUT	
ACCUPATION ACC	45		1/0	YES		-	TIM1_ETR	-	-	-	-	-	USART1_RTS	-	CAN1_TX	OTG_FS_DP	-	-	-	-	EVENTOUT	(USB0 connector)
ACCUPATION ACC	46	PA13 (JTMS-SWDIO)	I/O	YES		JTMS-SWDIO											_				EVENTOUT	SWDIO
AB	47		-	-	-	-	-	-		-	-	-		-	-	-	-	-	-	-	-	-
April Commonwealth Commonwealt		VDD	-	-	-	-	-	-			-	-		-	-	-	-			-	-	-
PA15 FOLD	49	PA14 (JTCK-SWCLK)	I/O	YES		TCK-SWCLK										_					EVENTOUT	SWCLK
SPI PC10 IO YES		PA15					TIM 2_CH1					SPI3_NSS/										
Second S	50	(JTDI)	1/0	YES		JTDI	TIM 2_ETR	-	-	-	SPI1_NSS	I2S3S_WS	-	-	-	-	-	-	-	-	EVENTOUT	Missage Cond CD C
Second S	51	PC10	I/O	YES		-						SPI3_SCK/ I2S3S_CK	USART3_TX/	UART4_TX	-	-	-	SDIO_D2	DCMI_D8	-	EVENTOUT	SDIO_D2
S																						MicroSD Card SD 4bits
S	52	PC11	1/0	YES							i2S3ext_SD		USART3_RX	UART4_RX		-	-	SDIO_D3	DCMI_D4	1	EVENTOUT	
Second Pock	53	PC12	1/0	YES		-	-	-	-	-	-	I2S3_SD	USART3_CK	UART5_TX	-	-	-	SDIO_CK	DCMI_D9	-	EVENTOUT	SDIO_CK
P83 C		DDA	1/0	VEC				TIMO ETC						HADTS DV				epic cur	DCM D4		EVENTOL	MicroSD Card SD 4bits
ST TRACESWO 10 VES TRACESWO TMQ CH2	54		I/O	168				TIMO_ETR		-	-	-		UAR15_KX		-	-	SDIO_CMD	DCMI_D11	-	EVENTOUT	ODIO_CMD
Fig.	1	(JTDO/		1000	1	JTDO/		1				SPI3_SCK			1						m.m.m.o:	
F			1/0	YES	 	TRACESWO	TIM2_CH2	<u> </u>	-	-	SPI1_SCK	12S3_CK	-	-	-	-	-	-	-	+-	EVENTOUT	
Fig.	56	(NJTRST)	1/0	YES		NJTRST	-	TIM3_CH1	-	-	SPI1_MISO		I2S3ext_SD	-	-	-	-	-	-	-	EVENTOUT	
Se PB6 IO YES	57	PR5	1 1/0	VES	_	1 .	Ι	TIM3 CH2	l . T	I2C1 SMPA	SPI1 MOSI	SPI3_MOSI	_	l . –	CANS BY	OTG HS UI PL D7	ETH PPS OUT	I	DCMI D10	1.	EVENTOUT	
SP	58	PB6	I/O	YES				TIM4_CH1		I2C1_SCL						-			DCMI_D5	±Ξ	EVENTOUT	
BOOTD		PB7	I/O	YES		-	-	TIM4_CH2	-	I2C1_SDA			USART1_RX	-		-	-	FSMC_NL	DCMI_VSYNC	-	EVENTOUT	
61 PB8 10 YES - 1TAM-CH3 TM10_CH1 ECT_SCL CANT_RX - ETH_MII_TXXX SDIO_CH DCM_DB EVENTOUT 62 PB9 10 YES - TAM-CH4 TM11_CH1 ECT_SCA EXS_VS - CANT_TX - SDIO_DB DCM_DT EVENTOUT 63 VSS	60	воото	1														_					Default Boot from Flash (pull-down resistor 2.2Kohms)
62 PB9 I/O YES TIMM CH4 TIM11 CH1 IZC1 SDA IZSZ WS CAN1 TX SDIO D5 DCMI D7 - EVENTOUT			1/0	YES		-	-	TIM4_CH3	TIM10_CH1	I2C1_SCL	-	-	-	-	CAN1_RX	-	ETH_MII_TXD3	SDIO_D4	DCMI_D6	-	EVENTOUT	
83 VSS	62	ppo	100	VEC				TIMA CHA	TIM11 CH	12C1 9D4	SPI2_NSS		l . –	l	CAN1 TV			SDIO DE	DCMI D7		EVENTOUT	
64 VDD			-	- 163	-	-	-	- CH4		IZUI_SUA	1232_WS	-		-		-	-	- 3010_03	- DOMI_D/	-	-	
Bin 2 2 V Code II Not GV Adversed			-	-		-	-	-		-	-	-		-	-	-	-		-	-	-	-
	Pin 3	3V Only II Not 5V	tolerant						1													