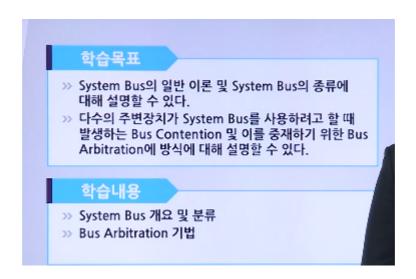
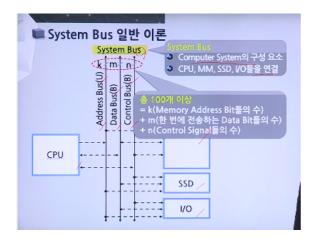
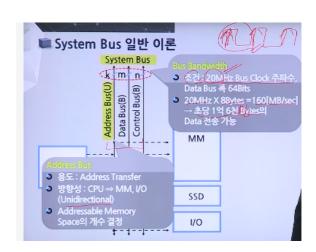
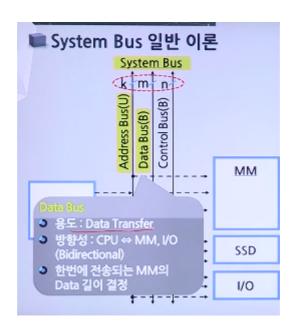
## 31강\_System Bus 개요 및 분류

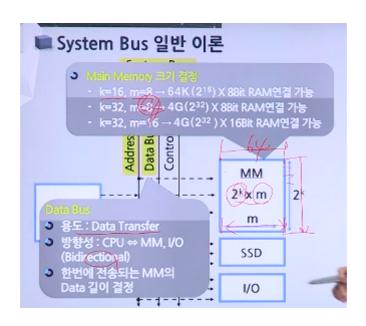
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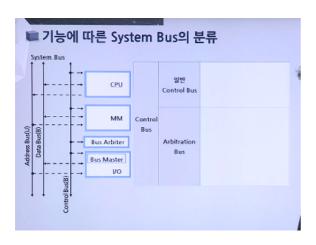


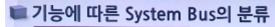












## Read Operation 순서

- Bus를 사용하고자 하는 구성 요소(CPU, MM, I/O)의 Bus Master가 Bus Arbiter로부터 Bus Priority 획득 (Bus Request → Bus Grant)
- Address와 Read 신호 전송
- Data가 전송되어 올 때까지 대기

## Write Operation 순서

- Bus를 사용하고자 하는 구성 요소(CPU, MM, I/O)의 Bus Master가 Bus Arbiter로부터 Bus Priority 획득 (Bus Request → Bus Grant)
- Bus를 통하여 Address와 Data 및 Write 신호 전송

