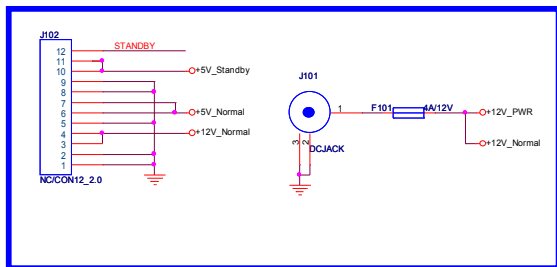
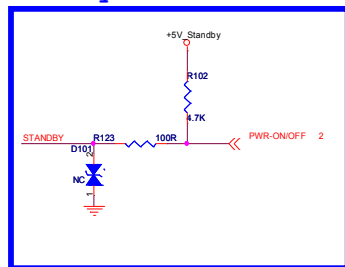


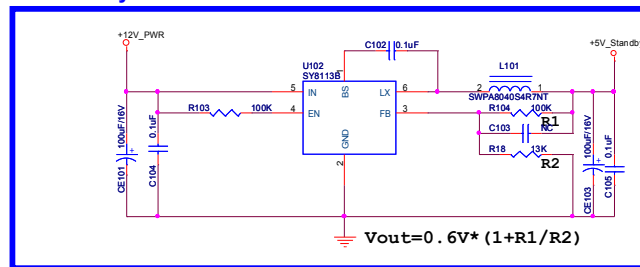
Connetor



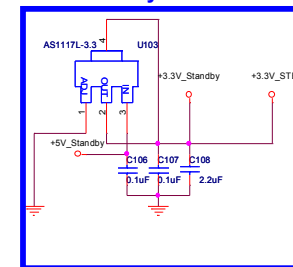
Standby control



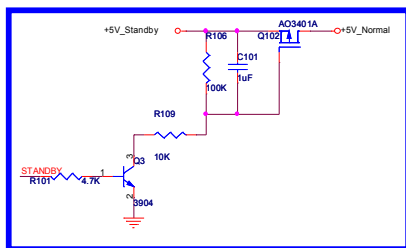
5V Standby Power



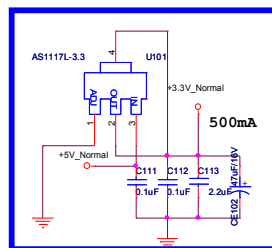
3.3V Standby Power LDO



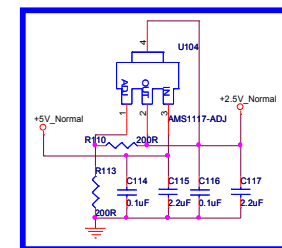
5V Normal Power



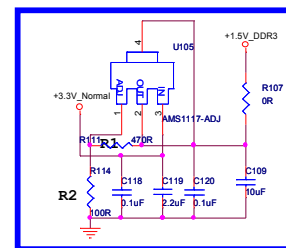
3.3V Normal Power LDO



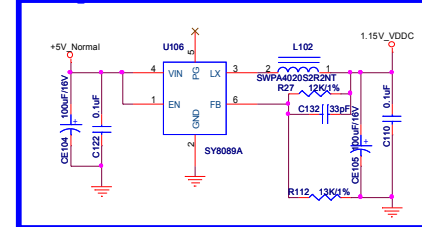
2.5V Normal Power for ADC



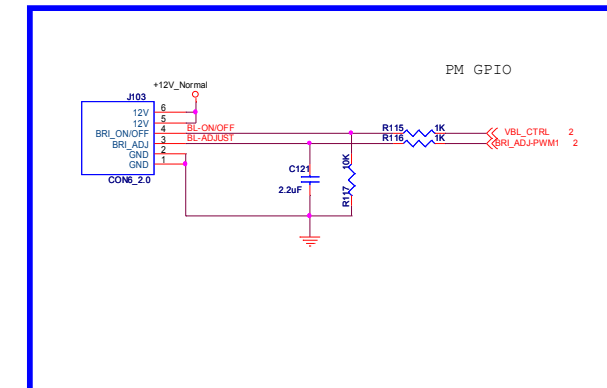
1.5V Normal Power for DDR3



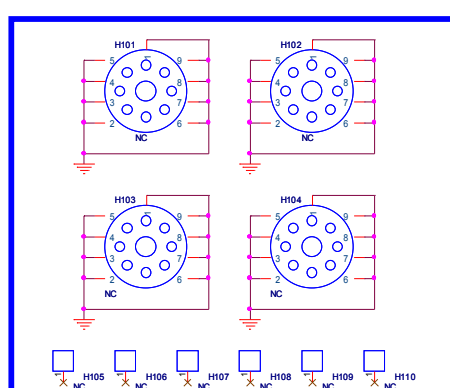
Corepower



Inverter controller



Test Point & MARK



The diagram illustrates a 12-channel signal conditioning module. It features two 12-pin headers (P1 and P2) on the left, each with a 12V power supply and ground pins. The module contains two identical signal conditioning channels. Each channel consists of an input buffer (U1A, U1B), a precision rectifier (U2A, U2B), and a precision full-wave rectifier (U3A, U3B). The output of each channel is connected to a 12V supply and ground. The module is powered by a 12V supply and ground. A note indicates that the output is 'Close to IC'.

[illegible][illegible]

The circuit diagram shows a voltage divider biasing network for a MOSFET amplifier. It consists of two resistors, R_{B1} and R_{B2}, connected in series between XTAL1 and XTAL0. The gate of the MOSFET (M1) is connected to the junction of R_{B1} and R_{B2}. A capacitor C_{B1} is connected from the gate to ground. The source of the MOSFET is connected to ground through a resistor R_{S1} and a capacitor C_{S1}. The drain of the MOSFET is connected to VDD1 through a resistor R_{D1}. The text "R_{B2} Close to MGT IC" indicates that resistor R_{B2} should be placed close to the microcontroller.

5V standby

5V

R002 100k

D001 1N4148

Q001 2N3904

R004 100k

Z001 5V

C002 2.2uF

C001 10uF

5V standby

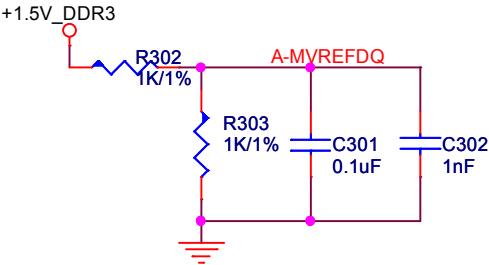
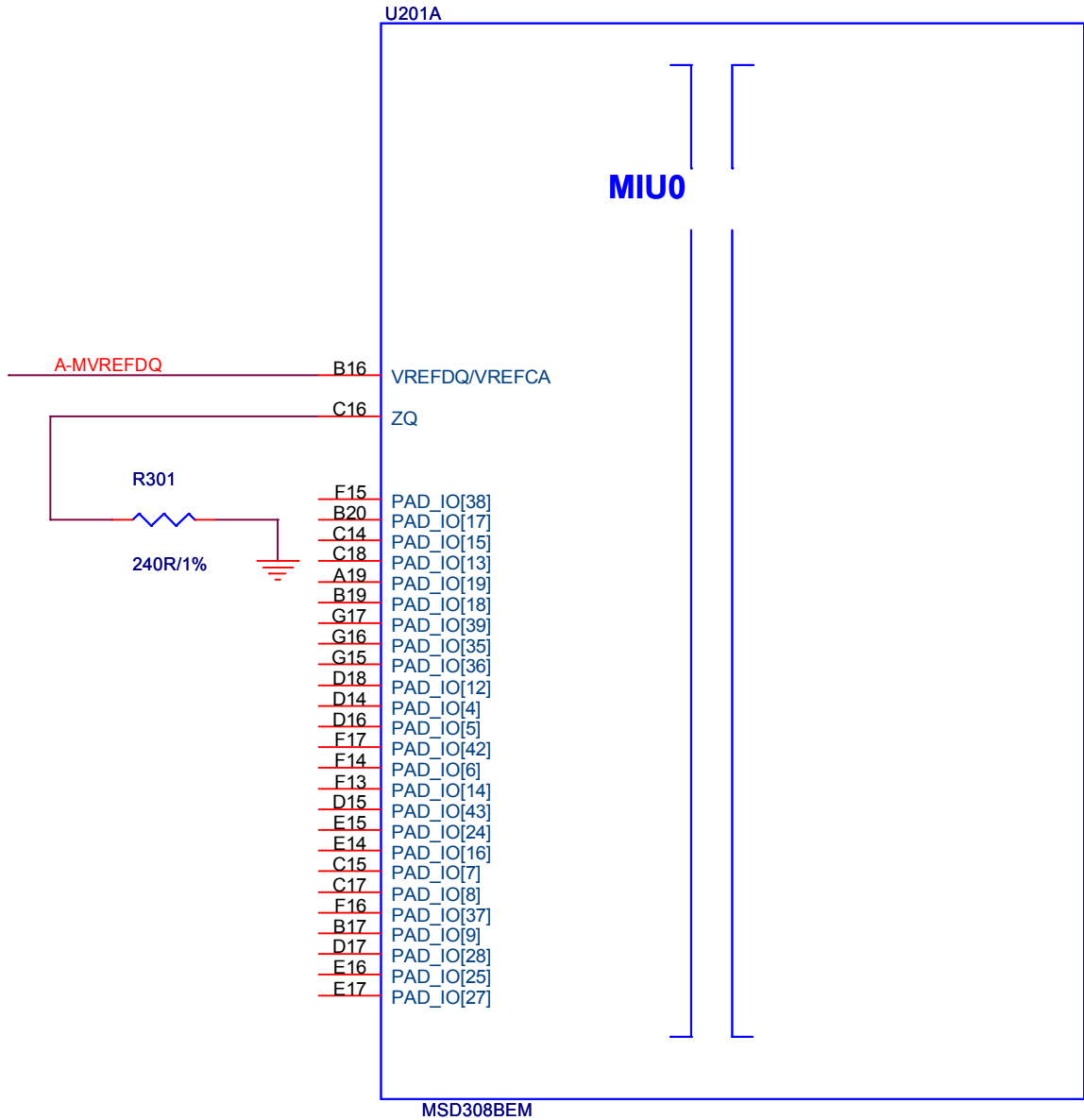
Diagram 1: 220V 500W-2.5A power supply circuit. The transformer has primary taps at 220V, 230V, 240V, and 250V, and secondary taps at 0V, 1.5A, 2A, and 2.5A. The circuit includes a 1000µF 25V electrolytic capacitor and a 1000Ω 0.5W resistor.

Diagram 2: 220V 500W-2.5A power supply circuit. The transformer has primary taps at 220V, 230V, 240V, and 250V, and secondary taps at 0V, 1.5A, 2A, and 2.5A. The circuit includes a 1000µF 25V electrolytic capacitor and a 1000Ω 0.5W resistor.

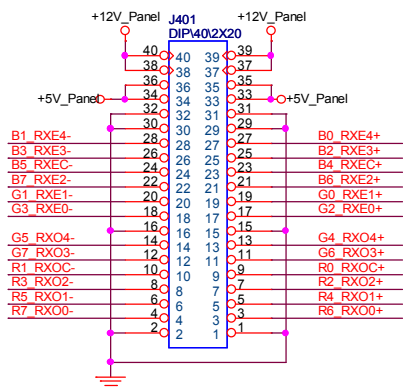
系统调用与内核函数映射表

| 系统调用 | 内核函数 |
|------------|------------|
| fork | do_fork |
| wait | do_wait |
| execve | do_execve |
| brk | do_brk |
| shmget | shmget |
| shmat | shmat |
| shmctl | shmctl |
| semget | semget |
| semop | semop |
| semctl | semctl |
| msgget | msgget |
| msgrcv | msgrcv |
| msgsnd | msgsnd |
| fcntl | fcntl |
| open | open |
| read | read |
| write | write |
| creat | creat |
| close | close |
| dup | dup |
| dup2 | dup2 |
| pipe | pipe |
| poll | poll |
| select | select |
| socket | socket |
| connect | connect |
| listen | listen |
| accept | accept |
| send | send |
| recv | recv |
| sendto | sendto |
| recvfrom | recvfrom |
| setsockopt | setsockopt |
| getsockopt | getsockopt |
| shutdown | shutdown |
| bind | bind |
| listen | listen |
| accept | accept |
| connect | connect |
| send | send |
| recv | recv |
| sendto | sendto |
| recvfrom | recvfrom |
| setsockopt | setsockopt |
| getsockopt | getsockopt |
| shutdown | shutdown |
| bind | bind |
| listen | listen |
| accept | accept |
| connect | connect |
| send | send |
| recv | recv |
| sendto | sendto |
| recvfrom | recvfrom |
| setsockopt | setsockopt |
| getsockopt | getsockopt |
| shutdown | shutdown |
| bind | bind |
| listen | listen |
| accept | accept |
| connect | connect |
| send | send |
| recv | recv |
| sendto | sendto |
| recvfrom | recvfrom |
| setsockopt | setsockopt |
| getsockopt | getsockopt |
| shutdown | shutdown |
| bind | bind |
| listen | listen |
| accept | accept |
| connect | connect |
| send | send |
| recv | recv |
| sendto | sendto |
| recvfrom | recvfrom |
| setsockopt | setsockopt |
| getsockopt | getsockopt |
| shutdown | shutdown |
| bind | bind |
| listen | listen |
| accept | accept |
| connect | connect |
| send | send |
| recv | recv |
| sendto | sendto |
| recvfrom | recvfrom |
| setsockopt | setsockopt |
| getsockopt | getsockopt |
| shutdown | shutdown |
| bind | bind |
| listen | listen |
| accept | accept |
| connect | connect |
| send | send |
| recv | recv |
| sendto | sendto |
| recvfrom | recvfrom |
| setsockopt | setsockopt |
| getsockopt | getsockopt |
| shutdown | shutdown |
| bind | bind |
| listen | listen |
| accept | accept |
| connect | connect |
| send | send |
| recv | recv |
| sendto | sendto |
| recvfrom | recvfrom |
| setsockopt | setsockopt |
| getsockopt | getsockopt |
| shutdown | shutdown |
| bind | bind |
| listen | listen |
| accept | accept |
| connect | connect |
| send | send |
| recv | recv |
| sendto | sendto |
| recvfrom | recvfrom |
| setsockopt | setsockopt |
| getsockopt | getsockopt |
| shutdown | shutdown |
| bind | bind |
| listen | listen |
| accept | accept |
| connect | connect |
| send | send |
| recv | recv |
| sendto | sendto |
| recvfrom | recvfrom |
| setsockopt | setsockopt |
| getsockopt | getsockopt |
| shutdown | shutdown |
| bind | bind |
| listen | listen |
| accept | accept |
| connect | connect |
| send | send |
| recv | recv |
| sendto | sendto |
| recvfrom | recvfrom |
| setsockopt | setsockopt |
| getsockopt | getsockopt |
| shutdown | shutdown |
| bind | bind |
| listen | listen |
| accept | accept |
| connect | connect |
| send | send |
| recv | recv |
| sendto | sendto |
| recvfrom | recvfrom |
| setsockopt | setsockopt |
| getsockopt | getsockopt |
| shutdown | shutdown |
| bind | bind |
| listen | listen |
| accept | accept |
| connect | connect |
| send | send |
| recv | recv |
| sendto | sendto |
| recvfrom | recvfrom |
| setsockopt | setsockopt |
| getsockopt | getsockopt |
| shutdown | shutdown |
| bind | bind |
| listen | listen |
| accept | accept |
| connect | connect |
| send | send |
| recv | recv |
| sendto | sendto |
| recvfrom | recvfrom |
| setsockopt | setsockopt |
| getsockopt | getsockopt |
| shutdown | shutdown |
| bind | bind |
| listen | listen |
| accept | accept |
| connect | connect |

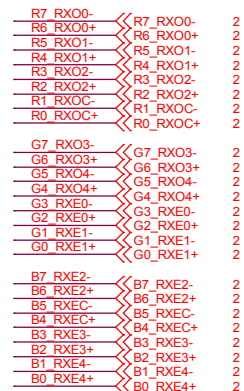
[illegible]



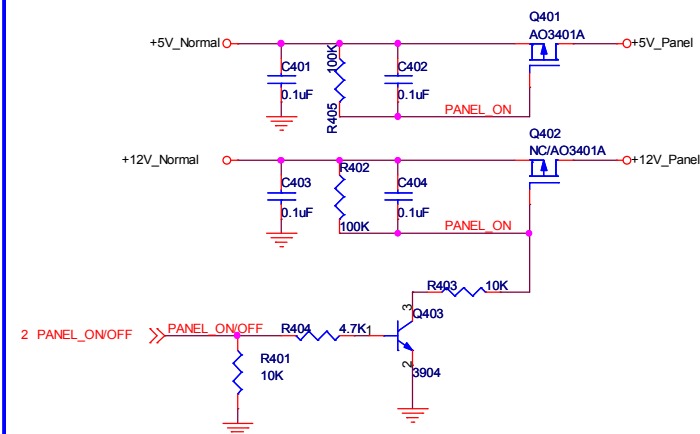
Connector



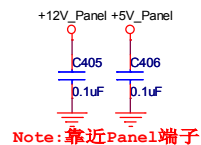
Net to other page



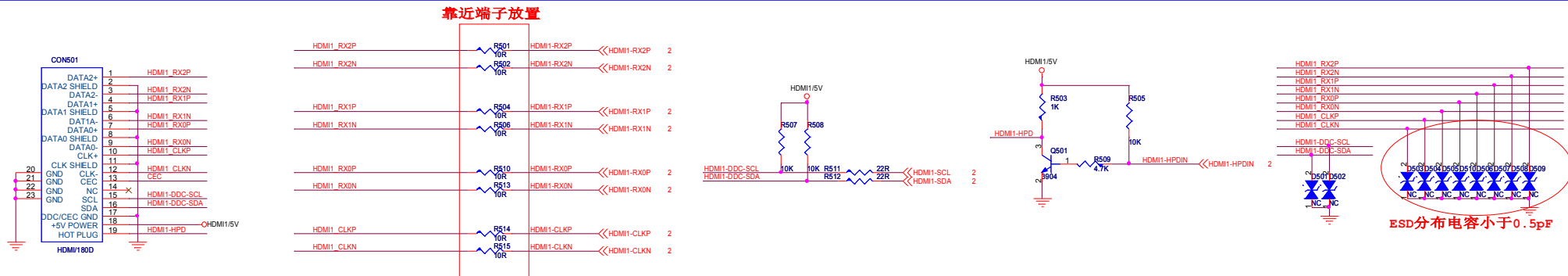
Power for panel



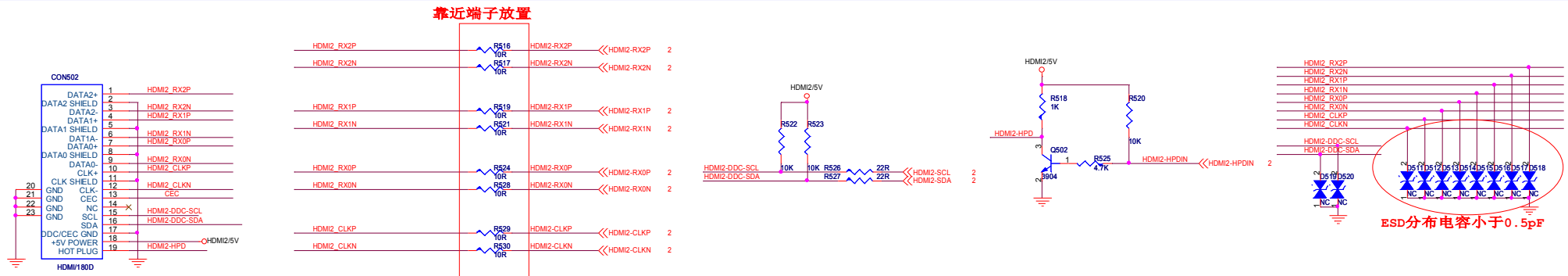
Power decouple



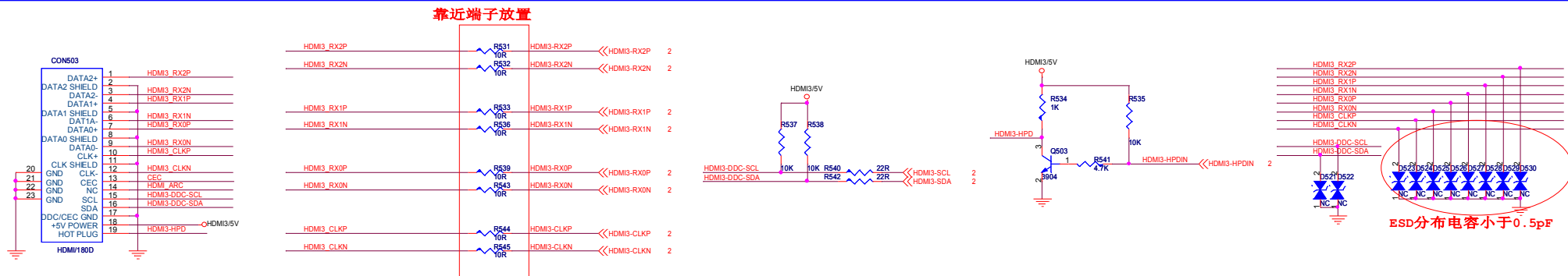
HDMI 1



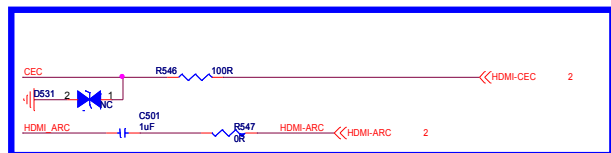
HDMI 2



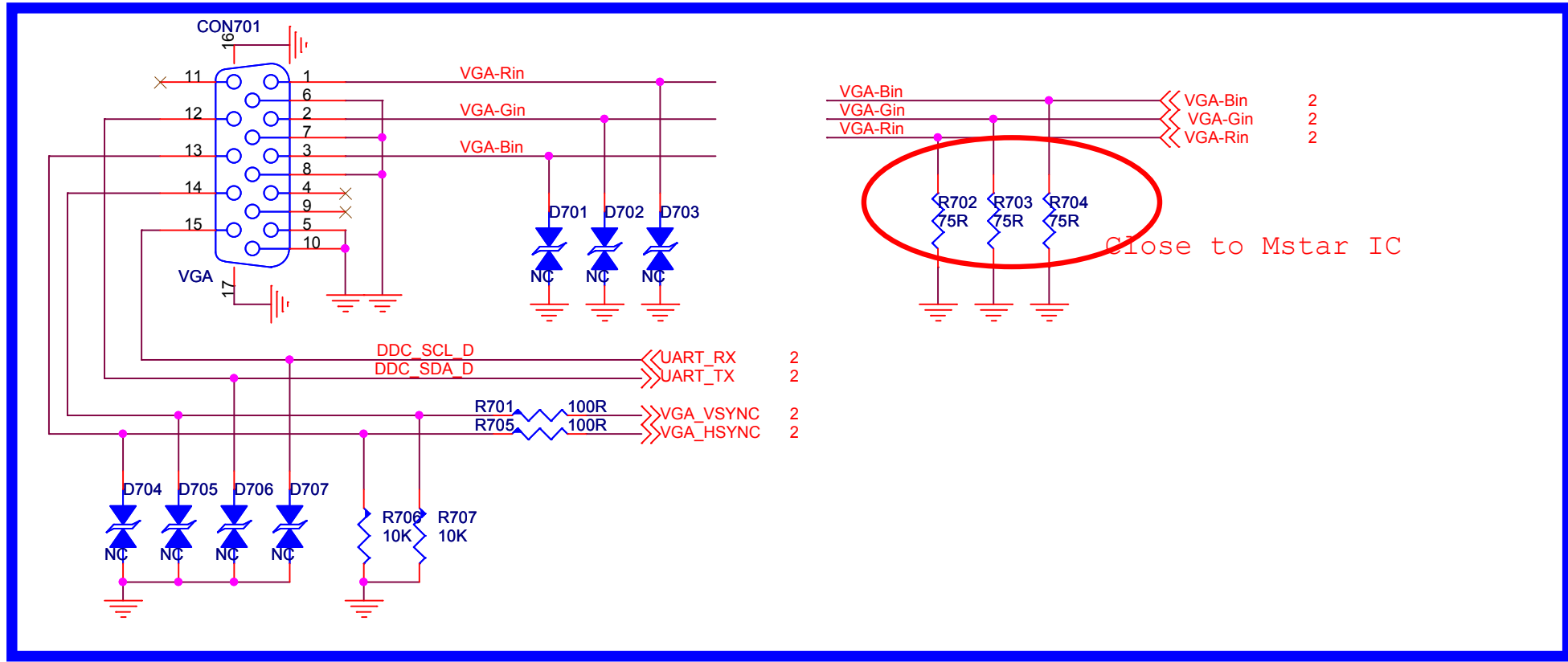
HDMI 3



CEC & ARC



Video



Mstar
semiconductor

MSD306BTM

Size
Custom

Document Number

VGA

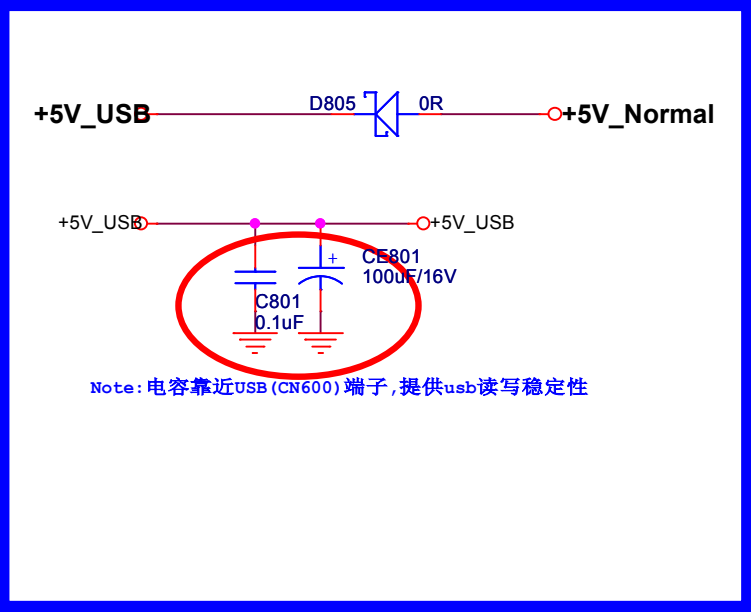
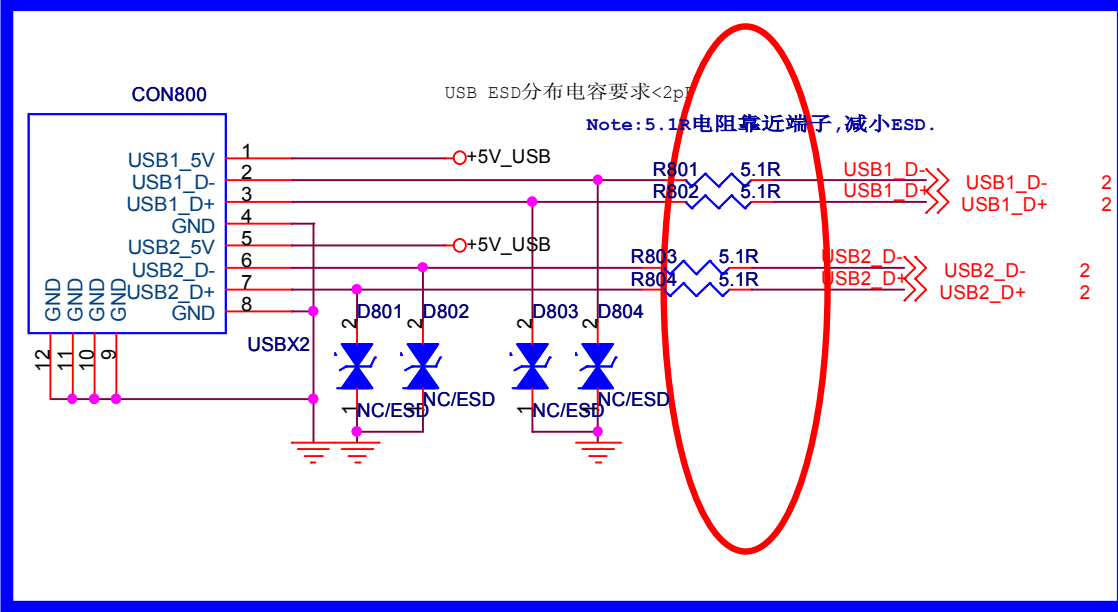
Rev
1.0

Date: Monday, March 10, 2014

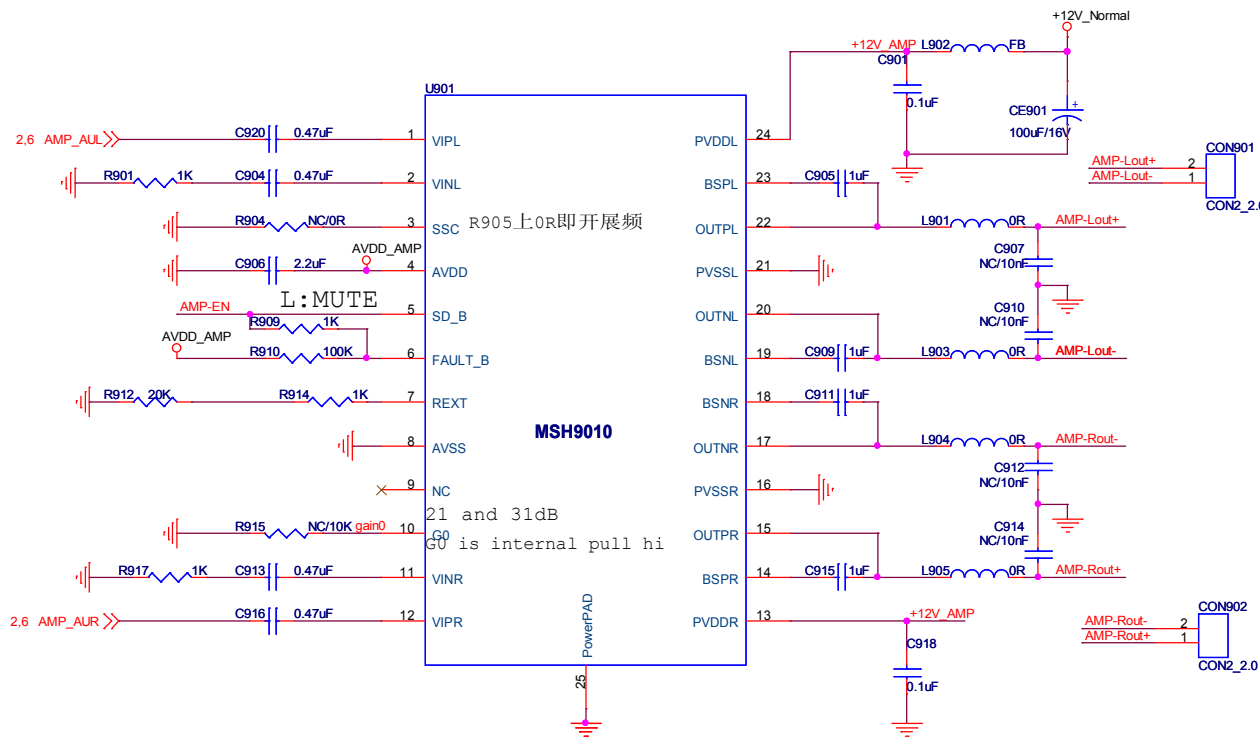
Sheet 7 of 14

USB INTERFACE

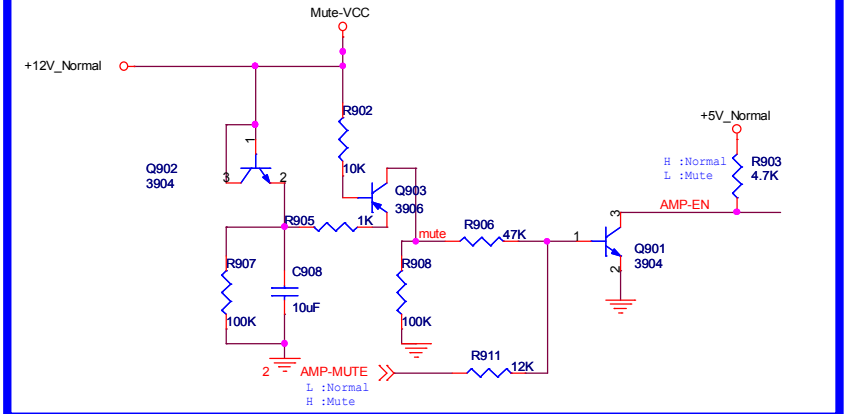
USB POWER



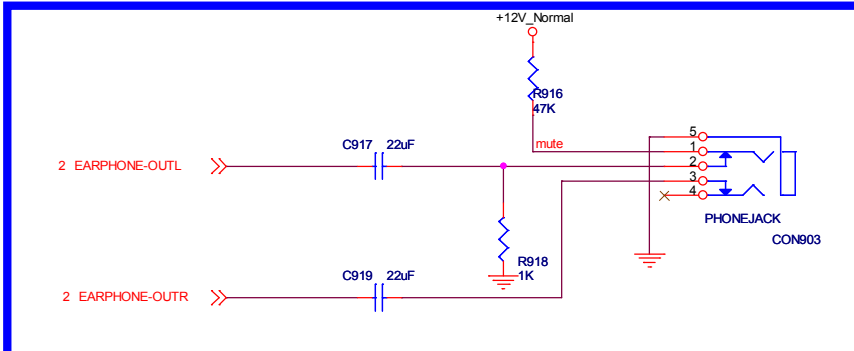
AUDIO AMP



MUTE CONTROL



EARPHONE OUT

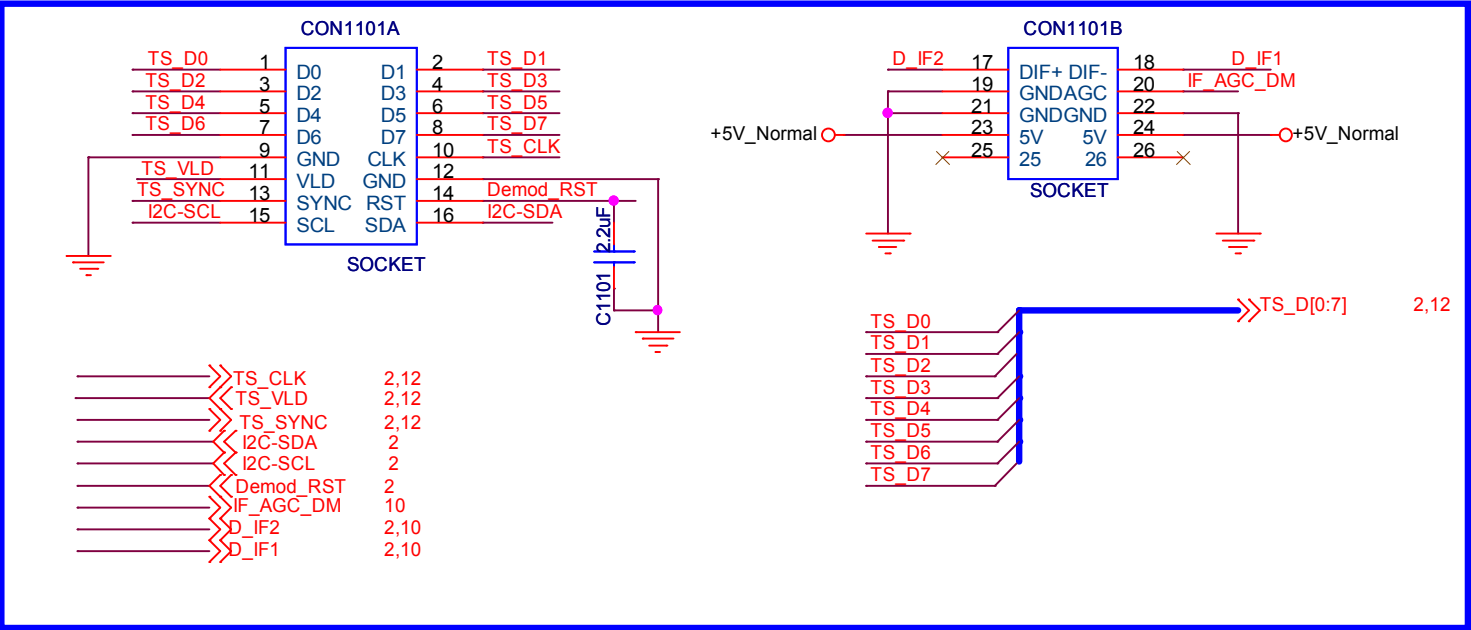


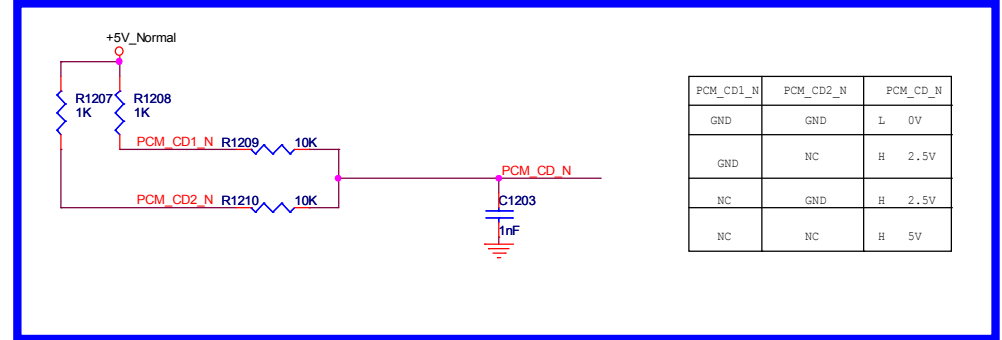
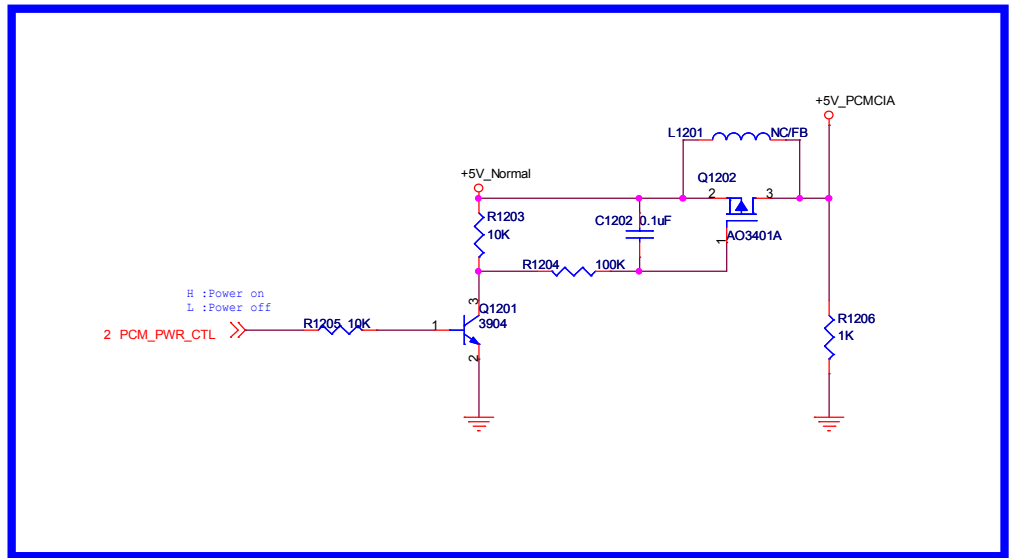
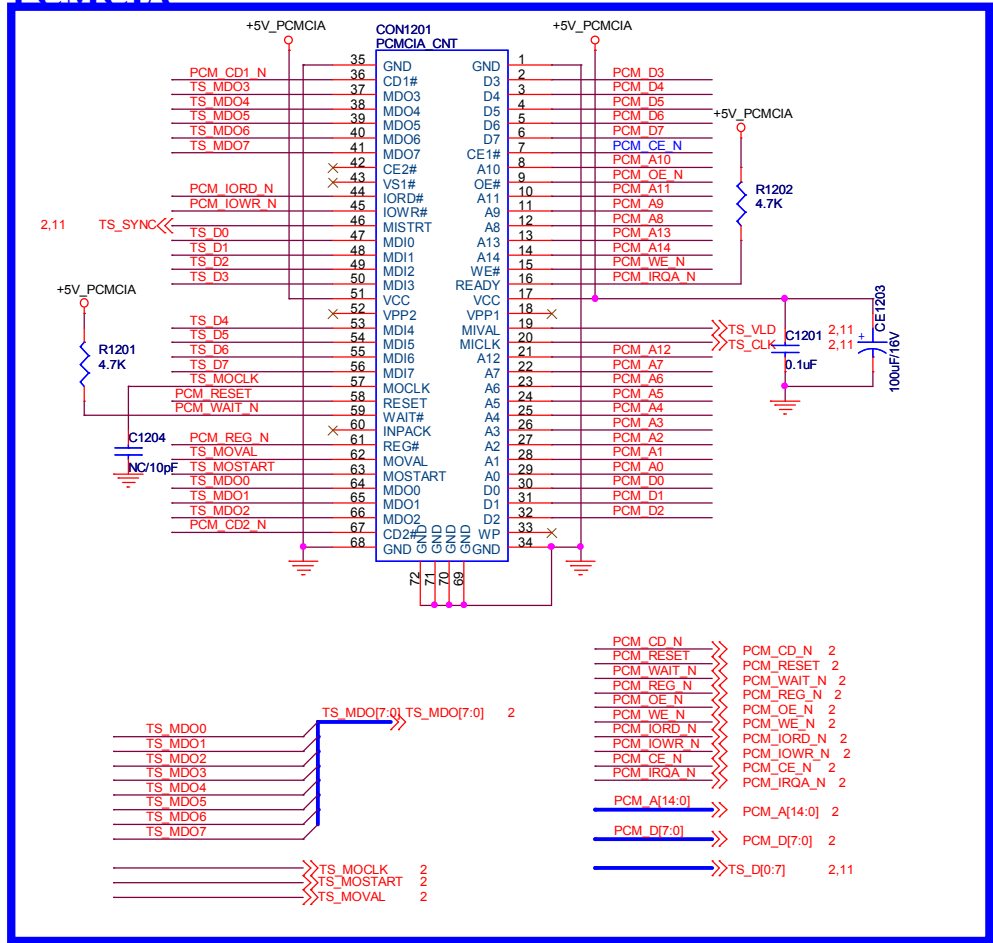
Mstar
semiconductor

| | | | |
|--------|------------------------|----------------------|-----------|
| Title | | | MSD306BTM |
| Size B | Document Number | Audio Amp. Interface | |
| Date: | Friday, April 04, 2014 | Sheet | 9 of 14 |

Rev 1.0

Option Daughter Board Interface





The diagram shows a component labeled P1001 with four pins. Pin 2 is connected to an RF input. Pin 3 is connected to an RF input. Pin 4 is connected to an RF input. Pin 5 is connected to an RF output. The component is represented by a blue rectangle with the text 'P1001' inside.

The schematic diagram illustrates the R840 module, centered around the U1001 IC. The IC is connected to a +3.3V_TU power supply and ground. Key connections include:

- Power and Ground:** AVDD (pin 24) is connected to +3.3V_TU through a 0.1uF capacitor (C100). VBG (pin 20) is connected to ground through a 22nF capacitor (C1003). DET2 (pin 19) is connected to ground through a 0.1uF capacitor (C1002).
- RF Front-End:** RFIN (pin 1) is connected to ground through a 68nH inductor (L1001). RFIN2 (pin 2) is connected to ground through a 270nH inductor (L1002). TF1 (pin 3) is connected to ground through a 180nH inductor (L1008). TF2 (pin 4) is connected to ground through a 100nF capacitor (C1014).
- Control and Data:** SCL (pin 6) is connected to ground through a 100nF capacitor (C1014). SDA (pin 7) is connected to ground through a 330pF capacitor (C1021) and a 1K resistor (R812). CP (pin 9) is connected to ground through a 6n8F capacitor (C1022). DVSS (pin 10) is connected to ground through a 0.1uF capacitor (C1023).
- Crystal and Timing:** XTAL_O (pin 13) and XTAL_I (pin 12) are connected to a 16MHz crystal (Y1001) and ground through 33pF capacitors (C1015 and C1016).
- Other Connections:** T-IF-IN+ (pin 17) and T-IF-IN- (pin 16) are connected to ground through 0.1uF capacitors (C1017 and C1018). TUAGC (pin 15) is connected to ground through a 0.1uF capacitor (C1019).

IF-IN- C101 0.1uF >>>D_IF1 2,11

IF-IN+ C101 0.1uF >>>D_IF2 2,11

T-IF-IN- R1002 200R IF-IN- C101 0.1uF >>>VIFM 2,11

R1003 470R

T-IF-IN+ R1007 200R IF-IN+ C102 0.1uF >>>VIFP 2,11

C1027 33pF

C1029 33pF

change list

V100版 2013年3月7日
V200版 2013年4月4日
1、C121和R117 对应位置更换。
2、耳机静音电路修改。
V300版 2013年5月8日
1、Tuner I2C上增加FB和对地电容。解决斜纹干扰问题。
2、SPI Flash部分上拉可以删去。R266、R268、R270、R272、R274删除。

Mstar

semiconductor

MSD306BTM

Size
A

Document Number

Rev
1.0

Date: Thursday, May 08, 2014

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Change list