版本 Versio	on: V2.1
日期 Date:	2011. 3. 14

名称: DVB-T 电子调谐器

Name: DVB-T ELECTRONIC TUNER

型号: CDT-9FT338-80

Model:

软件:

Software:

客户	客户承认	日期
CUSTOMER	APPROVE (请盖印章)	DATE

深圳市中龙通电子科技有限公司 CHINA DRAGON TECHNOLOGY LIMIED

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DESIGN:	CHECK:	APPROVAL:



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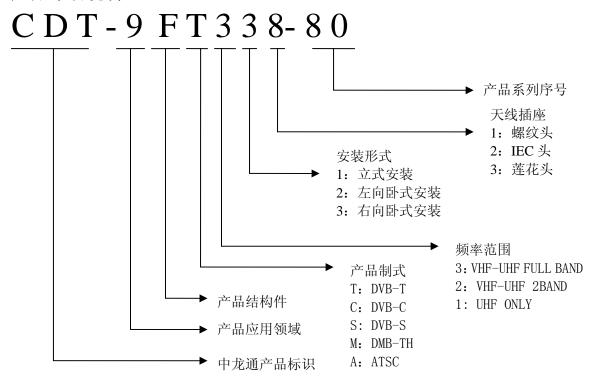
更改记录:

Reversion History:

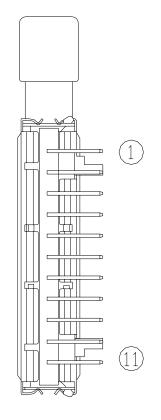
版本 Version	日期 Date	更改内容 Modification
2.1	2011-3-14	新版发行

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产品命名说明:



产品引脚定义:



端子序号	端子名称	端子电压
Terminal	Terminal name	Terminal
No		Voltage
1	ANT +	
2	RF AGC	0~4V
3	AS	
4	SCL	
5	SDA	
6	4MHz OUT	
7	VCC	5V +/-3%
8	IF OUT	
9	IF AGC	0-3.3V
10	DIF OUT	
11	DIF OUT	

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1. 接收制式:

Receiving System: DVB-T , DVB-C

2. 输入阻抗:

Input impedance: 75Ω Unbalance

3. 中频频率:

Intermediate Frequency: 36.125MHz

4. 频率覆盖范围:

Frequency Coverage

BAND	FREQUENCY (MHz)	
Low band	46 MHz ~ 148 MHz	
Mid band	$148~\mathrm{MHz}$ $^{\sim}$ $444~\mathrm{MHz}$	
High band	444 MHz $^{\sim}$ 858 MHz	

5. 使用和测试条件:

Use and Test Conditions:

	存储条件 Storage Conditions	使用条件 Use Conditions	测试条件 Test Conditions
温度 Temperature	-20~+80°C	0~+60°C	25±5℃
相对湿度 Relative Humidity	45%~65%	≤95%	60±15%
气压 Atmosphere	86~106kPa	86~106kPa	86~106kPa

6. 各端子电压电流:

Voltage & current at each terminal:

端子名称	端子电压	端子电流
Terminal Name	Terminal Voltage	Current
RF AGC	0V∼4V	
VCC	4.85V∼5.15V	
IF AGC	0V∼3.3V	

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7. 电气指标 Electrical Data

S/N	参数 (PARAMETER)	MIN.	TYP.	MAX.	UNIT
7. 1	功率增益				
	Power Gain				
	— VHF Low	40	50		dB
	- VHF High	40	50	_	
	— UHF	40	50		
7. 2	增益差				
	Gain Taper				
	- VHF Low			8.0	dB
	— VHF High	_	_	8.0	
	— UHF	_		8.0	
7.3	AGC 控制范围				
	RF AGC Control Range	40	60		dB
	IF AGC Control Range	50	50		
7.4	噪声系数		- 50		
	Noise Figure				
	- VHF Low		5. 5	8.0	dB
	- VHF High	_	5. 5	8.0	GD.
	- UHF		6. 5	9. 0	
7.5	天线输入端电压驻波比		0.0	0.0	
1.0	Antenna Input VSWR				
	- VHF Low	_	2.5	7.0	
	- VHF High	_	2. 5	7. 0	
	- UHF	_	2. 5	7. 0	
7.6	镜象抑制比		2.0	1.0	
	Image Rejection				
	- VHF Low (under 300MHz)	52	55		dB
	- VHF High (under 300MHz)	52	55	_	ab.
	- UHF	50	55		
7. 7	本振停振电压	- 00	00		
'''	Local Frequency Stop Voltage BM			4.2	V
7.8	中频抑制比				
1.0	IF Rejection	60			dB
	相位噪声				
	Phase Noise				
7. 9	1kHz	-55			dBc
1	10kHz	-80			abc.
	100kHz	-95			
	最大输入信号电平				
7. 10	The Max Input Signal Level	100			dBμV
	输入端本振干扰电压				
	例のSC. Voltage at input				
7. 11	f≤1000MHz	_		46	dB μ V
	f>1000MHz	_		52	
[I / IUUU WIIIZ			IJΔ	

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8. I²C 控制部分(I²C Control)

8.1 I²C 控制部分1(I²C Control Part NO.1)

8.1.1 逻辑图表(写模式, R/W=0)

Logic Diagram (Write Mode, R/W=0)

	MSB							LSB	
Address byte (ADB)	1	1	0	0	0	MA1	MAO	R/W = 0	A
Divider byte 1 (DB1)	0	N14	N13	N12	N11	N10	N9	N8	A
Divider byte 2 (DB2)	N7	N6	N5	N4	N3	N2	N1	NO	A
Control byte 1 (CB1)	1	0	ATP2	ATP1	ATP0	RS2	RS1	RS0	A
Band switch byte (BB)	CP1	CP0	0	P5	BS4	BS3	BS2	BS1	A
Control byte 2 (CB2)	1	1	ATC	MODE	T3/DISGCA	T2/IFDA	T1/CP2	TO/XLO	A

[&]quot;A"为应答信号 (Acknowledge)

8.1.2地址选择 (Address Selection)

AS 端子供给电压(Voltage applied on AS)	MA1	MAO
0v∼0.1Vcc	0	0
OPEN OR 0.2Vcc∼0.3Vcc	0	1
0. 4Vcc∼0. 6Vcc	1	0
0. 9Vcc∼Vcc	1	1

8.1.3本振频率设置 (Programmable divider settings (Divider Byte 1, Divider Byte 2))

$$N = 2^{14} \times N14 + 2^{13} \times N13 + ... + 2^{3} \times N3 + 2^{2} \times N2 + 2^{1} \times N1 + N0$$

 $F_{\text{OSC}} = F_{\text{RF}} + F_{\text{IF}} = N * F_{\text{REF}}$

8.1.4 控制信息 (Control info byte)

RF AGC设置 RF AGC take-over point

ATP2	ATP1	ATP0	IFOUT LEVEL (dB \(\psi \)
0	0	0	114
0	0	1	112
0	1	0	110
0	1	1	108
1	0	0	106
1	0	1	104
1	1	0	102
1	1	1	Disabled

参考分频比 (Reference divider ratio)

RS2	RS1	RS0	REFERENCE DIVIDER RATIO	F _{ref}
0	0	0	24	166.67kHz
0	0	1	28	142.86kHz
0	1	0	32	125kHz
0	1	1	64	* 62.5kHz
1	0	0	128	31.25kHz
1	X	1	80	50kHz

^{*} DVB-C时一般用该分频步进值。



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电荷泵电流设置(Charge Pump current setting):

MODE	CP2	CP1	CP0	CHARGE PUMP CURRENT (µA)
X	0	0	0	70
X	0	0	1	140
X	0	1	0	350
X	0	1	1	600
1	1	0	0	900

测试模式(Test mode):

MODE	T3/DISGCA	T2/IFDA	T1/CP2	TO/XLO	DEVICE OPERATION	XTALOUT 4-MHz OUTPUT
0	0	0	0	0	Normal operation	Enabled
0	0	0	0	1	Normal operation	Disabled
1	X	X	X	0	Normal operation	Enabled
1	X	X	X	1	Normal operation	Disabled
0	X	1	X	X	Test mode	Not available
0	1	X	X	X	Test mode	Not available

波段选择(Bandswitching Select)

波段开关(Band s	witching)	BS4	BS3	BS2	BS1
VHF Lo	W	0	0	0	1
VHF Hig	sh	0	0	1	0
UHF		1	0	0	0

MODE:

Mode = 0: IFGCA enabled, DIFOUT1, 2 selected

T3/DISGCA, T2/IFDA, T1/CP2, T0/XLO are Test bits and XTALOUT control bit

Mode = 1: T3/DISGCA = 0 : IF GCA enabled

T3/DISGCA = 1 : IF GCA disabled T2/IFDA = 0 : DIFOUT1, 2 selected T2/IFDA = 1 : AIFOUT selected

T1/CP2 : lcp control bit, See Charge Pump current setting

TO/XLO = 0 : XTALOUT enabled TO/XLO = 1 : XTALOUT disabled

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8.1.5 读模式(R/W=1)

Read Mode

	MSB							LSB	
Address byte (ADB)	1	1	0	0	0	MA1	MA0	R/W = 1	Α
Status byte (SB)	POR	FL	1	1	Х	A2	A1	A0	_

POR 电源标志(POWER ON, POR=1)

FL 锁定标志(锁定时, FL=1)(LOCK FLAG (LOCKED, FL=1))

A/D 转换器电平(A2, A1, A0 DATA, Bit P5 must be set to 0)

A2	A1	AO	A/D转换器输入电压(A/D INPUT)
1	0	0	(0.60~1.00) Vcc
0	1	1	(0.45∼0.60) Vcc
0	1	0	(0.30∼0.45) Vcc
0	0	1	(0.15∼0.30) Vcc
0	0	0	(0∼0.15) Vcc

9. Terminal for External Connection & Outline Drawing

