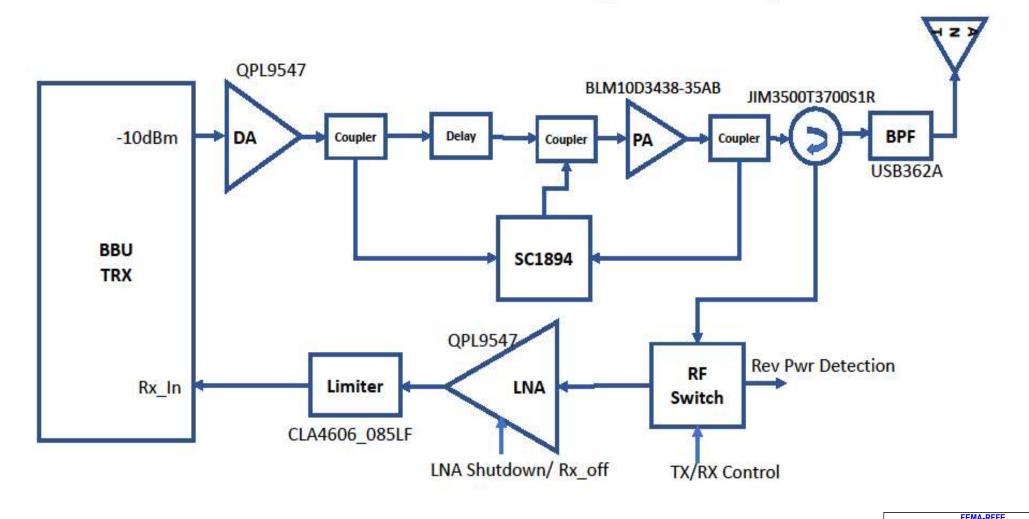
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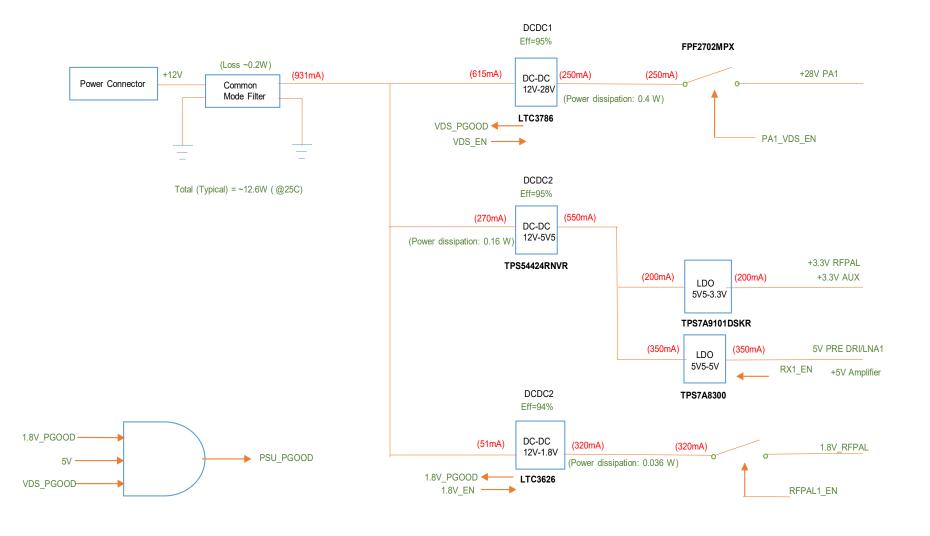
## BLOCK DIAGRAMNEED TO UPDATE

# CBRS B48 TDD FE Block Diagram 1W/30dBm

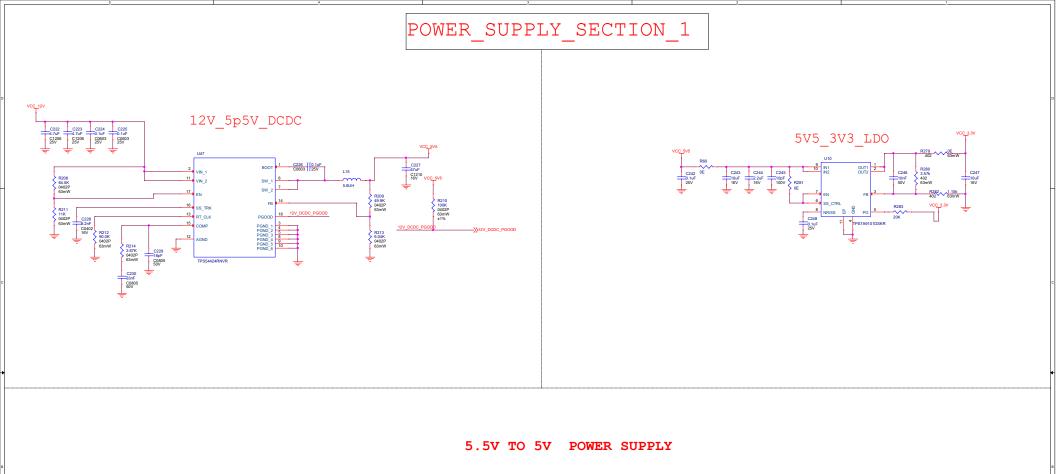


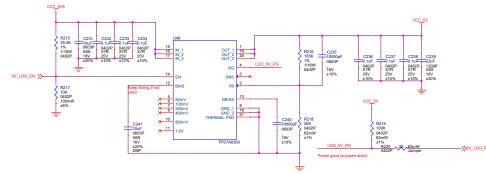
BLOCK DIAGRAM

### POWER TREE

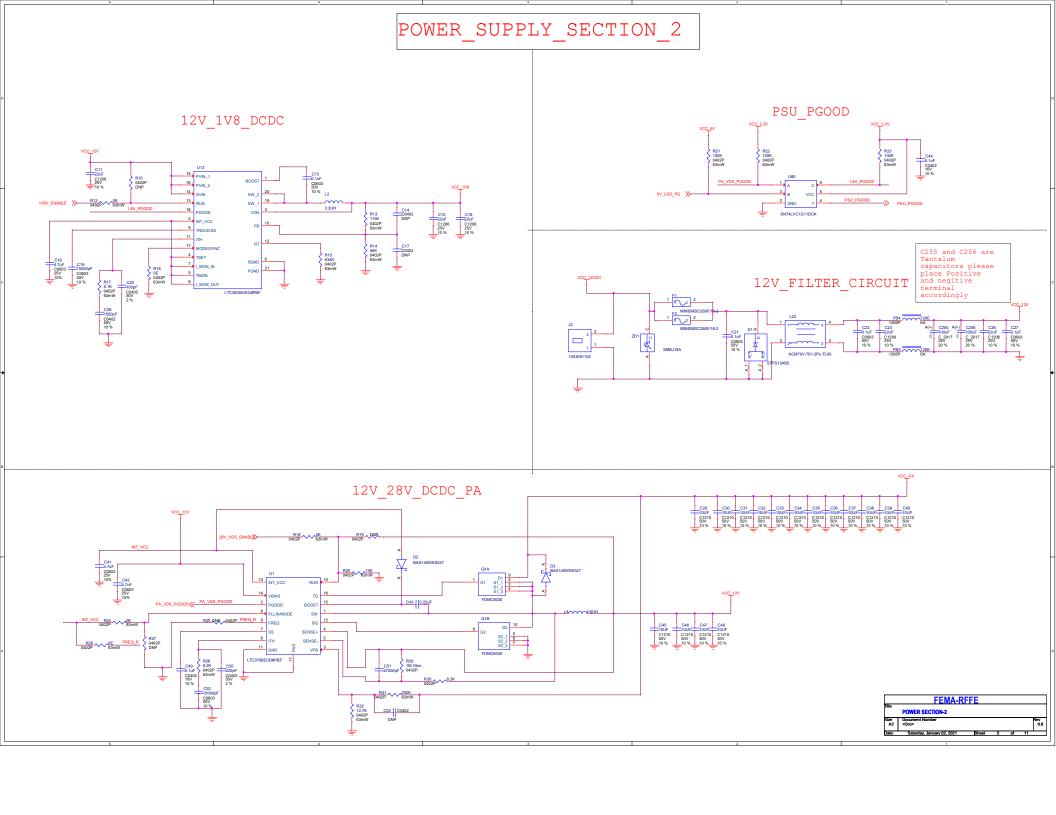






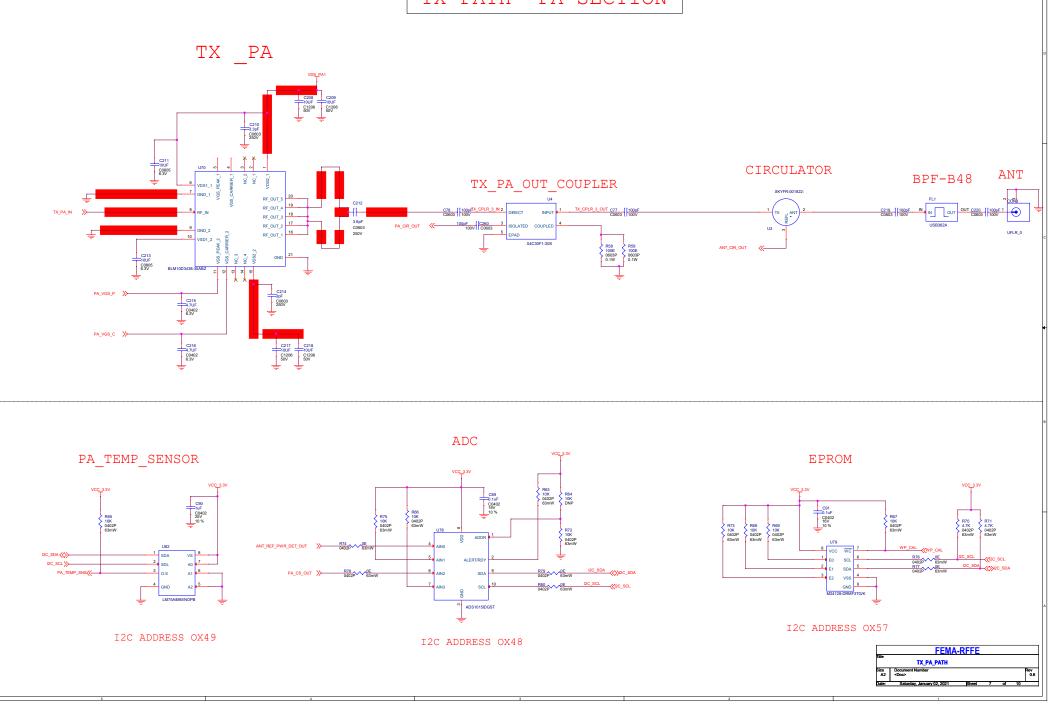


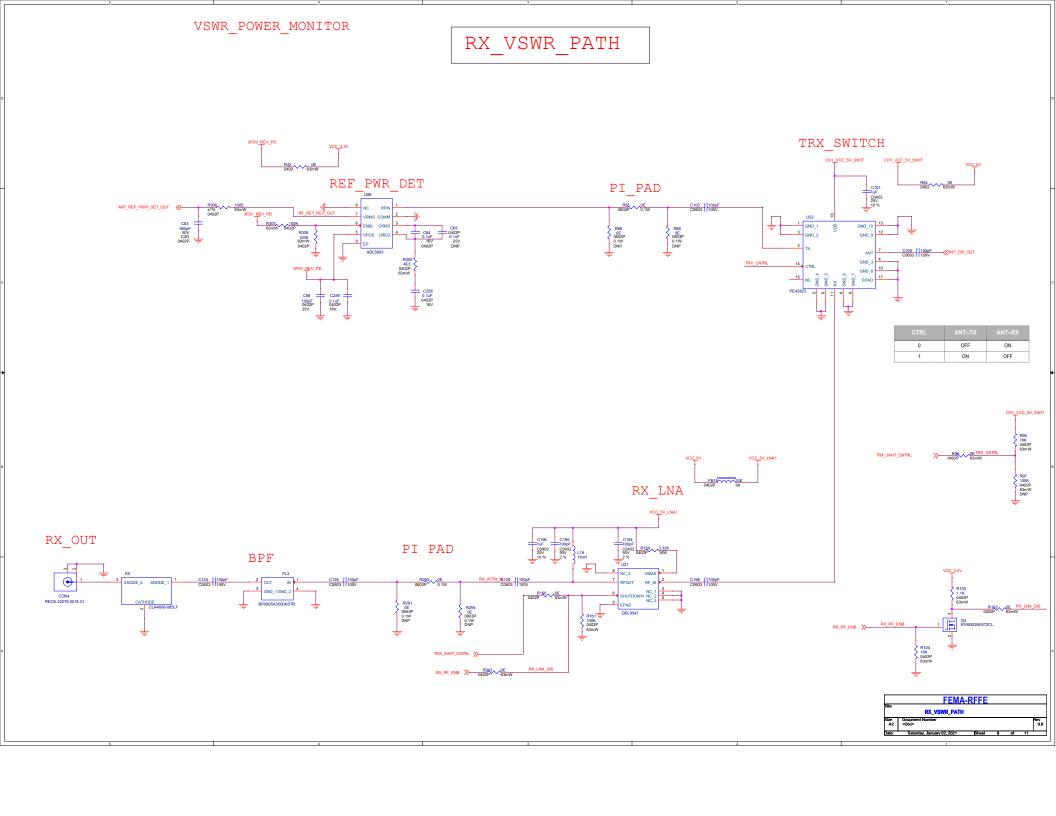




# TX PATH- DRIVER\_ATTN TX\_PRE\_DRIVER TX INPUT TX\_COUPLER\_1 PI PAD TX\_PREDRV\_EN PA\_COUP\_IN C68 100pF C0603 100V TX\_DRIVER TX DELAY LINE TX COUPLER 2 PI PAD PI PAD - 10dB TX\_PA\_IN < TX\_PA\_IN C207 100pF C0603 100V FEMA-RFFE TX\_DRIVER\_PATH

#### TX PATH- PA SECTION





### TX PATH-PA\_LIN\_CIRCUIT

#### PI PAD-18dB TX RFPAL RF CNTRL R106 68E 0603P 0.1W R107 68E 0603P 0.1W R109 12.4K 0402P 63mW C131 1uF C0402 25V 10 % C139 15pF C0402 50V 2 % MGPOUT0 MGPOUT1 C137 0.3pF C0402 50V 0.1 % MGPOUT3 C0402 50V 0.1 % RFOUTN LIN2\_STATO\_R LIN2\_WDTEN\_R WDTENB LIN2\_TESTSEL\_R LOADENB LIN2\_TXENB\_R C251 0.9pF C0402 50V SPI DEBUGER LIN2\_WDTEN\_R X 62 TESTSEL1 X TESTSEL2 LIN2\_TESTSEL\_R R282\_0E R257 OE R259 OE R117 0E 0603P 0.1W DNP R118 0E 0603P 0.1W DNP ALL SERIES RESISTORS IN J3 CONNECTOR NEED TO CONNTECT AS TRI PAD IN LAYOUT VCC\_RFPAL\_1V8 TX RFPAL POWER VDD\_LIN\_3.3V SI2333DS-T1-E3 111 C150 1000pF C0402 50V = 10 % VDD\_LIN\_3.3V C152 C153 C154 1000pF 1000pF 1000pF C0402 C0402 C0402 50V 50V 50V 50V TX\_RFPAL\_ENB >> TX\_RFPAL\_ENB R121 06 83mW C155 C158 4..7uF 1000pF C0803 C0402 25V 50V 10% 10 % C157 C158 4..7uF 1000pF C0603 C0402 25V 50V 10% 10 % FEMA-RFFE C159 1000pF C0402 50V 10 % PA\_LIN\_CIRCUT

