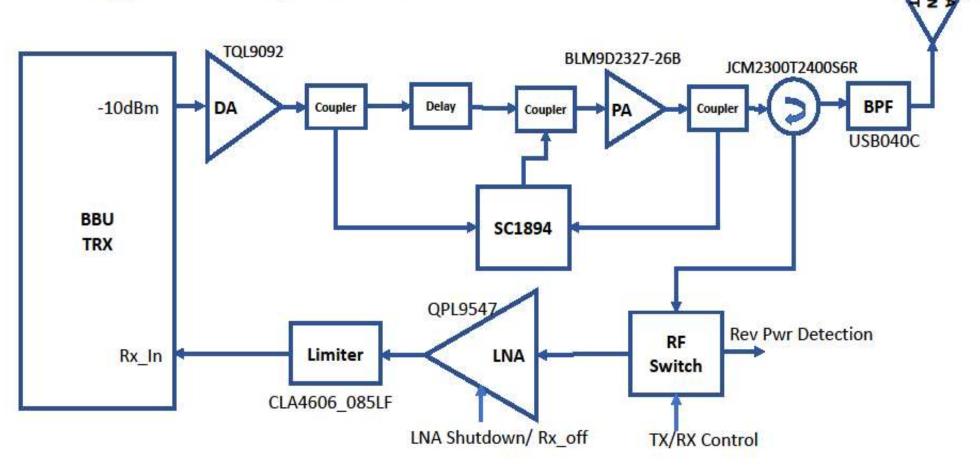
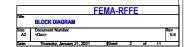
	TABLE C	F CONTENTS				
SHEET NUMBER	SHEET NAME					
1 2 3 4 5 5 6 6 7 7 8 8 9 9 110	NDEX PAGE BLOCK DAGRAM POWER TREE POWER, SECTION TX PATH-DRIVER, ATTN TX PATH-DRIVER, ATTN TX PATH-PA TX, USWIR, PATH PA LNL, CROLLIT PA LONGROUT BOARD_BOARD_CONNECTOR		DATE	VERSION 0.1	REVISION HIS	REMARKS REV A RELEASE

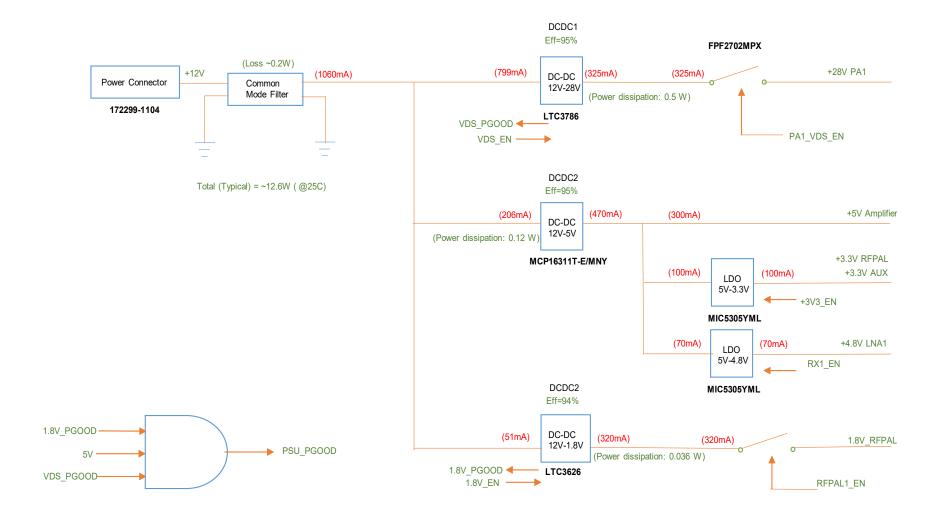


CBRS 2.3G to 2.4GHz (B40) TDD FE Block Diagram 1W/ 30dBm

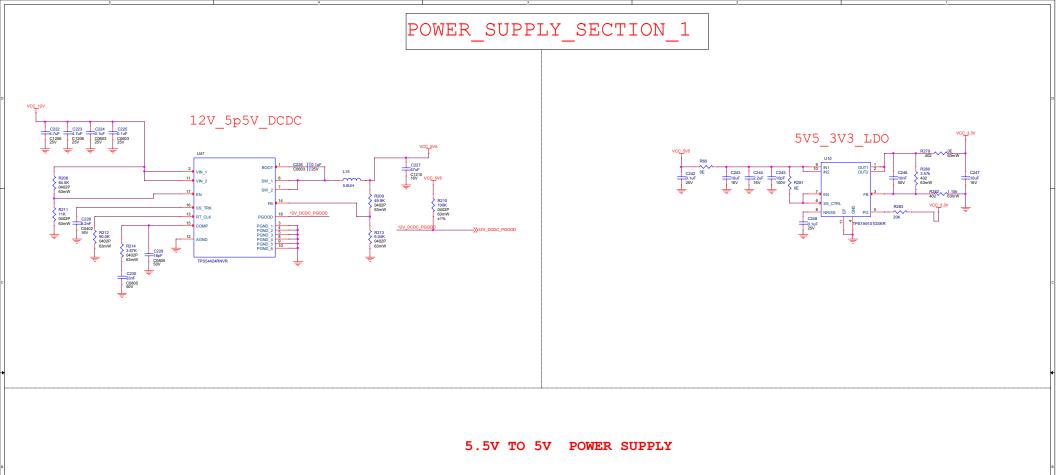


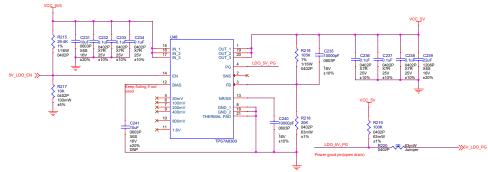


## LTE\_GEN2\_BAND40\_RF\_FRONT\_END\_POWER\_TREE

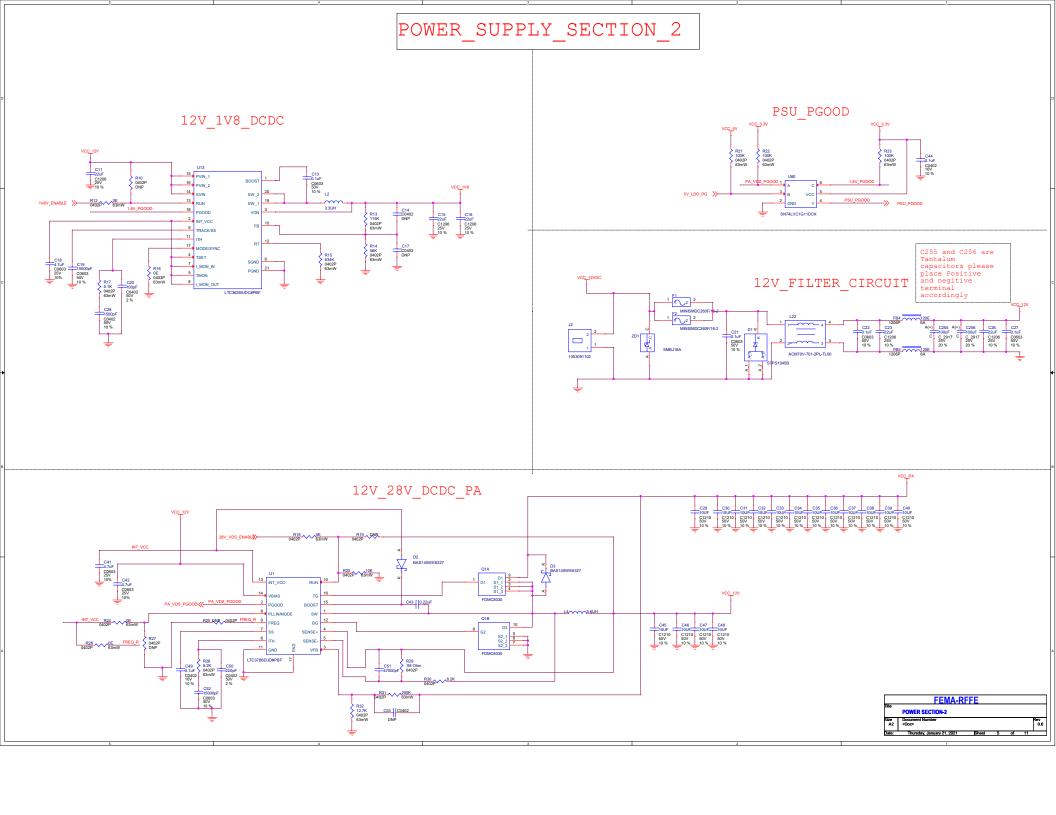




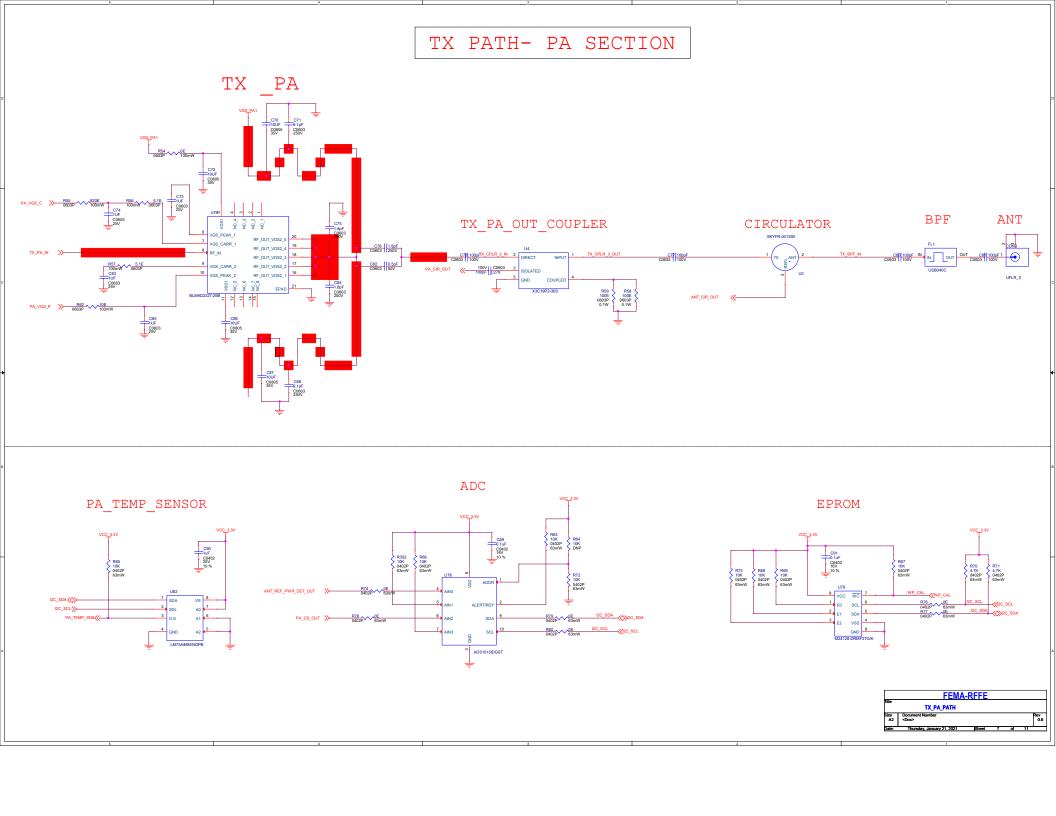




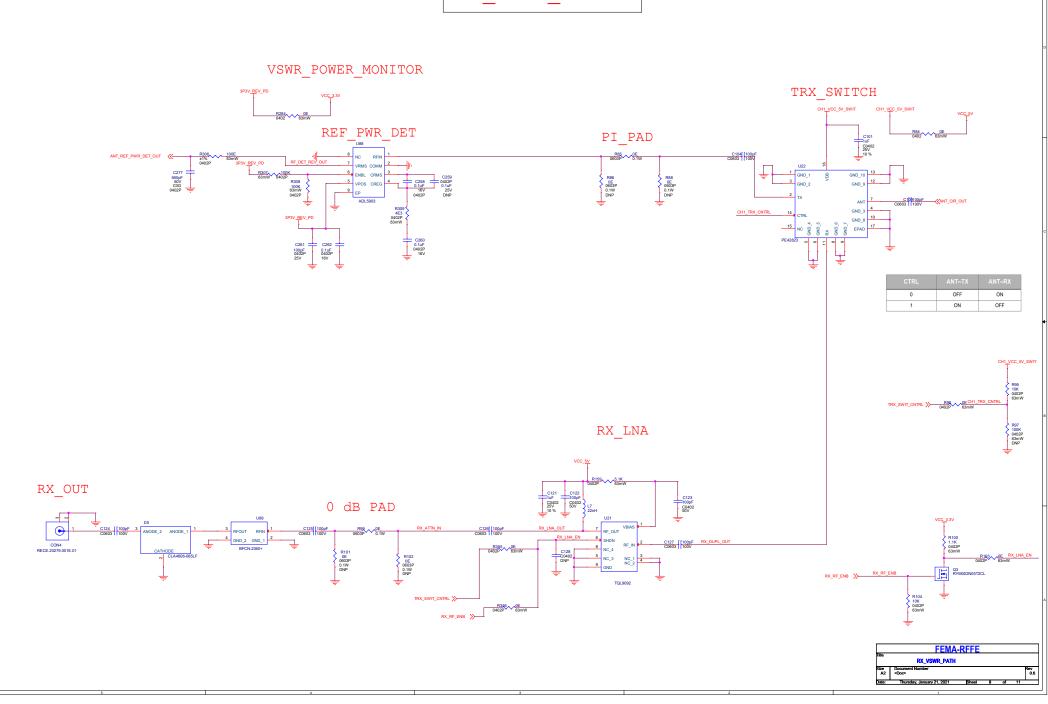




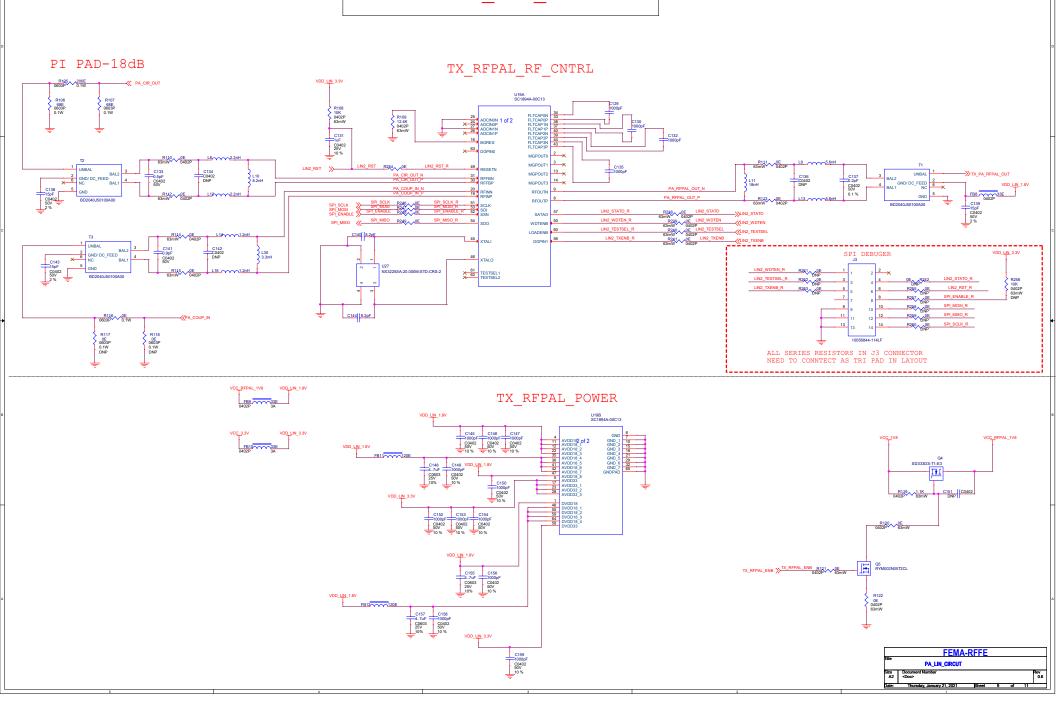
## TX PATH- DRIVER\_ATTN TX\_PRE\_DRIVER TX\_COUPLER\_1 TX INPUT PI PAD R36 0E 0603P 0.1W DNP TX\_DRIVER TX DELAY LINE C82 C83 1uF 100pF C0402 C0402 TX COUPLER 2 PI PAD PI PAD-14dB FEMA-RFFE TX\_DRIVER\_PATH Document Number

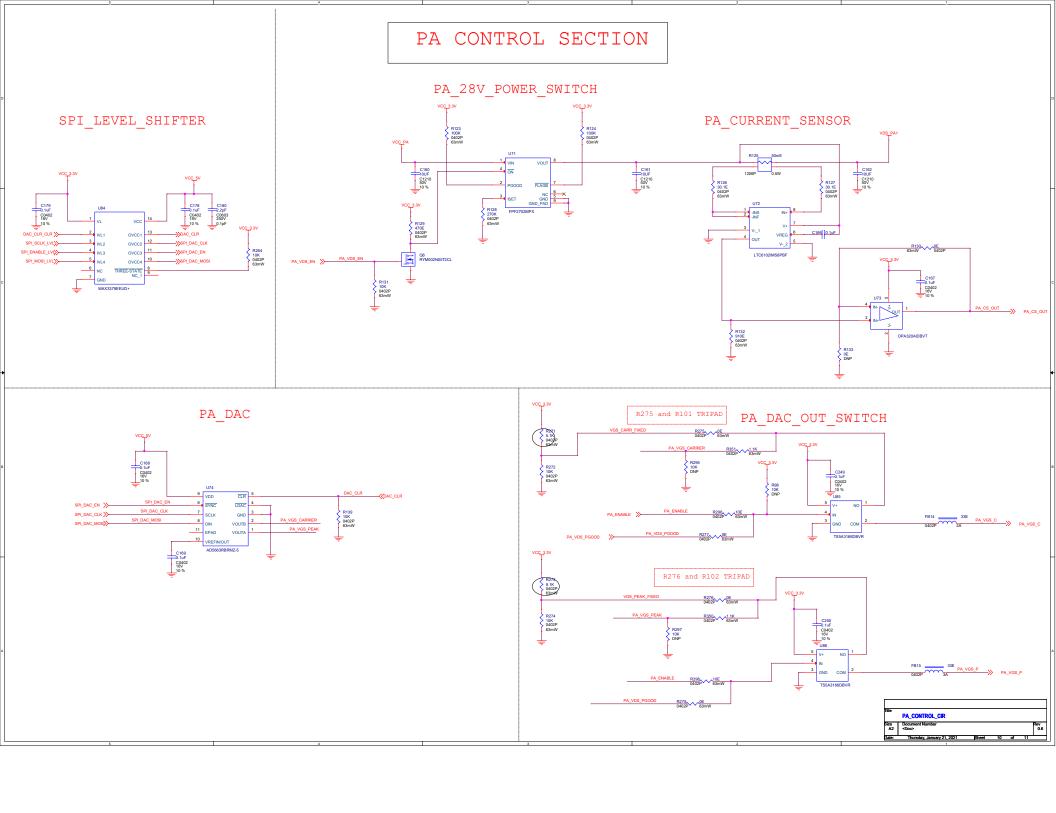


RX\_VSWR\_PATH



## TX PATH-PA\_LIN\_CIRCUIT





## FEMA\_BOARD\_BOARD\_CONNECTOR

