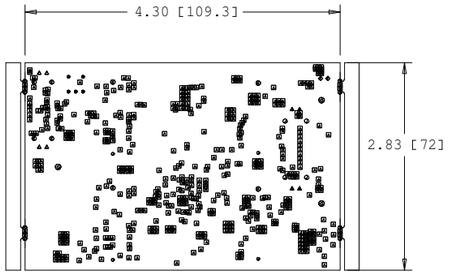
NOTES: UNLESS OTHERWISE SPECIFIED.

1. THIS FILE IS TO BE USED IN CONJUNCTION WITH THE GERBER FILE AND DRILL DATA WHERE APPLICABLE.
2. ALL ARTWORK PATTERNS ARE VIEWED FROM TOP.
3. UNLESS OTHERWISE MENTIONED FABRICATE AS PER IPC-6012D CLASS 2.
4. MATERIAL:
   DIELECTRIC: FR4 AS PER IPC-4101/21.
   COPPER: AS PER STACKUP.
5. SURFACE FINISH:IMMERSION GOLD
6. SOLDER MASK MATERIAL SHALL MEET ALL THE REQUIREMENTS OF IPC-SM-840C AND SHALL BE BLACK IN COLOR AND APPLIED OVER BARE COPPER.
7. SILK SCREEN LEGEND TO BE APPLIED AS PER LAYER STACKUP USING YELLOW NON-CONDUCTIVE EPOXY INK.
   THERE SHALL BE NO SILKSCREEN ON ANY SOLDERABLE AREA.
8. DONT KEEP PCB MANUFACTURER LOGO IN PCB.
9. MINIMUM CONDUCTOR WIDTH:5.00 MILS.
   MINIMUM SPACING:6 MILS.
   MINIMUM DRILL/PAD SIZE:10/20 MILS
10. FOR ALL NON SPECIFIED TOLERANCE FOR FINISH HOLES SIZE IS +/- 3 MILS
11. GERBER SHOULD BE VERIFIED FOR 100% CONTINUITY AND SHORTS TESTING STRICTLY USING IPC-D-356A
12. BOW AND TWIST SHALL NOT EXCEED 0.75% OF LONGEST SIDE.
13. VENDOR TO IDENTIFY TEST PASSED IN SECONDARY SIDE.
    THE BBT REPORT SHOULD BE DELIVERED ALONG WITH PCBS.
14. ALL DIMENSIONS ARE IN:MILS UNLESS OTHERWISE SPECIFIED
15. PCB MANUFACTURER TO MODIFY AND ADD BATCH CODE AT TIME OF FABRICATION.
    DO NOT CHANGE THE FONT SIZE.
16. ALL VIAS SHOULD BE FILLED WITH FLAT SURFACE FINISH



DATE:31-07-2020	PART NUMBER:xxxxx
DESIGNED AT:	RELY SYS
BOARD NAME:	mask-C2-RevB
FILM LAYER:	FABRICATION DRAWING

CAUTION

SENSITIVE ELECTRONIC DEVICES-CLASS 1

LAYER STACK-UP

UNITS -MILS

62.696 +/-10% MM)

- PRIMARY SIDE, SILK SCREEN
- PRIMARY SIDE, SOLDER MASK --- 1 MILS
- PRIMARY SIDE, TOP - 1.770 MILS
- DIELECTRIC ---------- 3.878 MILS
- GROUND LAYER, LAYER 2 - 1.2 MILS
- DIELECTRIC ---------- 47 MILS
- POWER LAYER, LAYER 3 - 1.2 MILS
- DIELECTRIC ---------- 3.878 MILS
- SECONDARY SIDE, BOTTOM LAYER 4 - 1.770 MILS
- SECONDARY SIDE, SOLDER MASK ---1 MILS
- SECONDARY SIDE, SILK SCREEN

SINGLE ENDED (55 OHM) IMPEDANCE CHART			
LAYER(S)	TRACE WIDTH	REF LAYER	TOL
TOP	5 MILS	L2	+/- 10%
BOTTOM	5 MILS	L3	+/- 10%

DIFFERENTIAL SIGNAL (90 OHM) IMPEDANCE CHART				
LAYER(S)	TRACE WIDTH	SEPERATION	REF LAYER	TOL
TOP	6.00 MILS	7.00 MILS	L2	+/- 10%
BOTTOM	6.00 MILS	7.00 MILS	L3	+/- 10%

DRILL CHART: TOP to BOTTOM				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
⊠	10.0	+3.0/-3.0	PLATED	551
◆	39.37	+3.0/-3.0	PLATED	2
▲	39.4	+3.0/-3.0	PLATED	4
•	42.91	+0.0/-0.0	PLATED	5
⊙	47.2	+3.0/-3.0	PLATED	4
▲	55.12	+3.0/-3.0	PLATED	4
⊙	62.99	+3.0/-3.0	PLATED	4
◎	145.7	+3.0/-3.0	PLATED	3
⊞	20.0	+3.0/-3.0	NON-PLATED	8
⊙	43.0	+3.0/-3.0	NON-PLATED	8
•	44.0	+3.15/-3.15	NON-PLATED	4
⊙	64.0	+1.97/-1.97	NON-PLATED	4

UNLESS OTHERWISE SPECIFIED ALL DIMENSIONS ARE IN		mask-C2-RevB	
[X] INCHES	[X] MILS	TITLE	FABRICATION DRAWING
	INITAL	SIZE D	PART NUMBER
PREP. BY:		DATE 31-07-2020	DATE SCALE 1/1 / SHEET 1 OF 1 / REV: B
CHK. BY:	xxxxx		
APP. BY:	xxxxx		

CAD DATA DO NOT MANUALLY UPDATE