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QN3109M6N

N-Channel 30V Fast Switching MOSFET

General Description

The QN3109M6N is a high performance trench N-channel MOSFET which utilizes extremely high cell density to provide low $R_{DS(on)}$ and gate charge characteristics. It is ideally suited to support synchronous buck converter applications.

The QN3109M6N meets RoHS and Green Product requirements while supporting full function reliability.

Features

- ✓ Advanced high cell density Trench technology
- ✓ Super Low Gate Charge
- ✓ Green Device Available

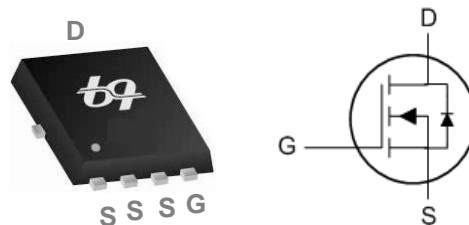
Product Summary

V_{DS}	$R_{DS(ON)} \text{ max}$ ($V_{GS}=10V$)	I_D ($T_C=25^\circ C$)
30V	1.62m Ω	145A

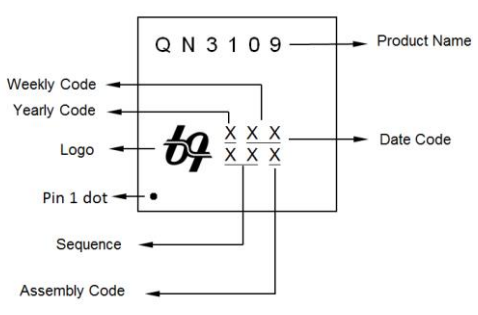
Applications

- ✓ High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- ✓ Networking DC-DC Power System
- ✓ Load Switch

Pin Configuration



Ordering Information

Order Number	Package Type	Top Marking
QN3109M6N	PRPAK5X6	

QN3109M6N-DS-F0000, Jul. 2019

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Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^{\circ}\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}^{1.7}$	145	A
$I_D@T_C=100^{\circ}\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}^{1.7}$	91	A
$I_D@T_A=25^{\circ}\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}^1$	28	A
$I_D@T_A=70^{\circ}\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}^1$	22	A
I_{DM}	Pulsed Drain Current ²	290	A
EAS	Single Pulse Avalanche Energy ³	270.1	mJ
I_{AS}	Avalanche Current	73.5	A
$P_D@T_C=25^{\circ}\text{C}$	Total Power Dissipation ⁴	54	W
$P_D@T_A=25^{\circ}\text{C}$	Total Power Dissipation ⁴	2	W
T_{STG}	Storage Temperature Range	-55 to 150	$^{\circ}\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^{\circ}\text{C}$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	45	62	$^{\circ}\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	1.8	2.3	$^{\circ}\text{C/W}$

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N-Channel Electrical Characteristics

N-Channel Electrical Characteristics: ($T_J=25^{\circ}\text{C}$, unless otherwise noted)						
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	30	--	--	V
$\Delta BV_{DSS} / \Delta T_J$	BVDSS Temperature Coefficient	Reference to 25°C , $I_D=1mA$	--	0.008	--	V/ $^{\circ}\text{C}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=10V, I_D=30A$	--	1.35	1.62	m Ω
		$V_{GS}=4.5V, I_D=15A$	--	1.9	2.5	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.2	--	2.5	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		--	-5.3	--	mV/ $^{\circ}\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=24V, V_{GS}=0V, T_J=25^{\circ}\text{C}$	--	--	1	μA
		$V_{DS}=24V, V_{GS}=0V, T_J=55^{\circ}\text{C}$	--	--	5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	--	--	± 100	nA
gfs	Forward Transconductance	$V_{DS}=5V, I_D=15A$	--	62	--	S
R_g	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1MHz$	--	1.0	--	Ω
Q_g	Total Gate Charge (10V)	$V_{DS}=15V, V_{GS}=10V, I_D=15A$	--	46.3	--	nC
Q_g	Total Gate Charge (4.5V)	$V_{DS}=15V, V_{GS}=4.5V, I_D=15A$	--	21.8	--	
Q_{gs}	Gate-Source Charge		--	8.1	--	
Q_{gd}	Gate-Drain Charge		--	6.8	--	
$t_{d(on)}$	Turn-On Delay Time	$V_{DS}=15V, V_{GS}=10V, R_G=3.3\Omega, I_D=15A$	--	12.7	--	ns
t_r	Rise Time		--	44.4	--	
$t_{d(off)}$	Turn-Off Delay Time		--	34.8	--	
t_f	Fall Time		--	7.7	--	
C_{iss}	Input Capacitance	$V_{DS}=15V, V_{GS}=0V, f=1MHz$	--	3100	--	pF
C_{oss}	Output Capacitance		--	1482	--	
C_{rss}	Reverse Transfer Capacitance		--	44	--	

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Guaranteed Avalanche Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
EAS	Single Pulse Avalanche Energy ⁵	$V_{DD}=25V$, $L=0.1mH$, $I_{AS}=42.1A$	88.62	--	--	mJ

Diode Characteristics

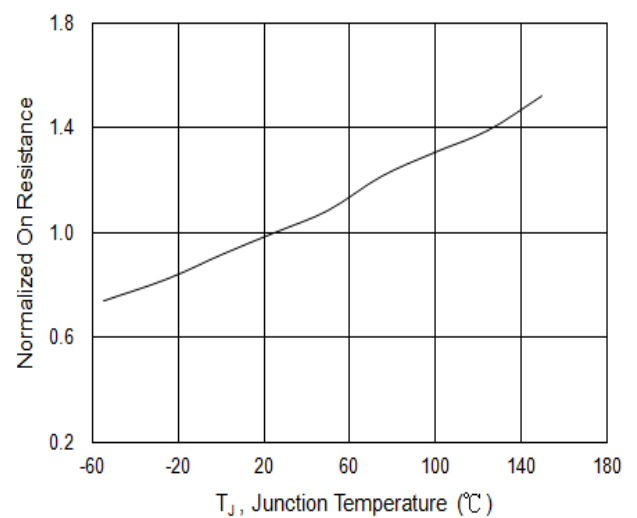
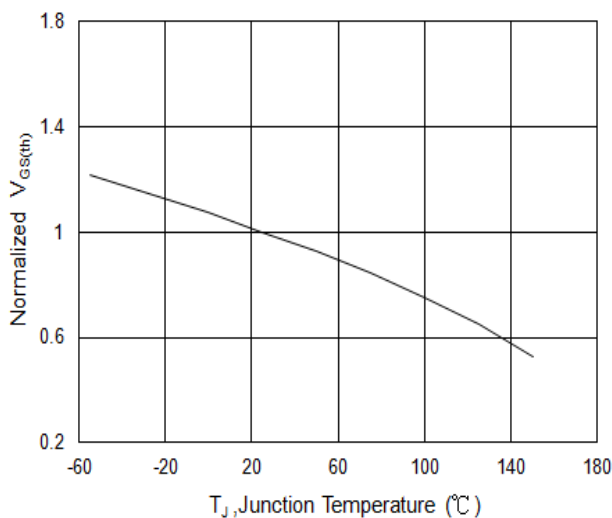
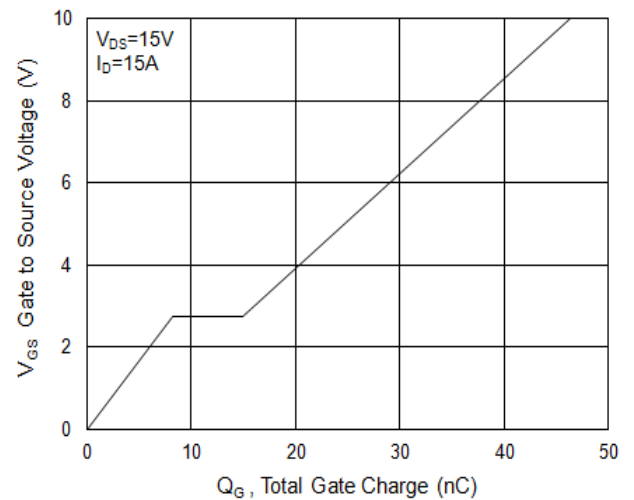
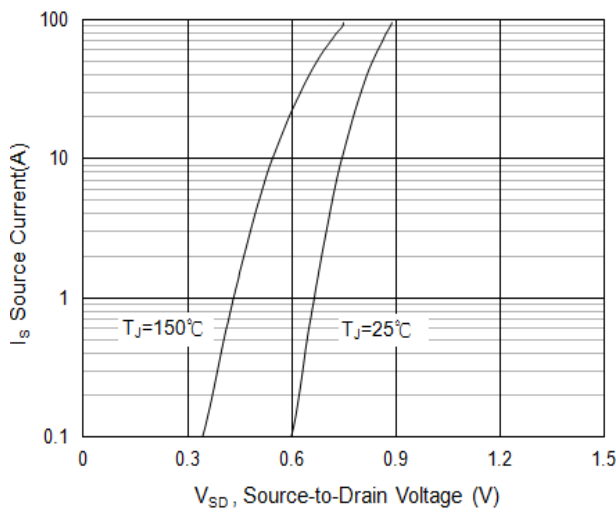
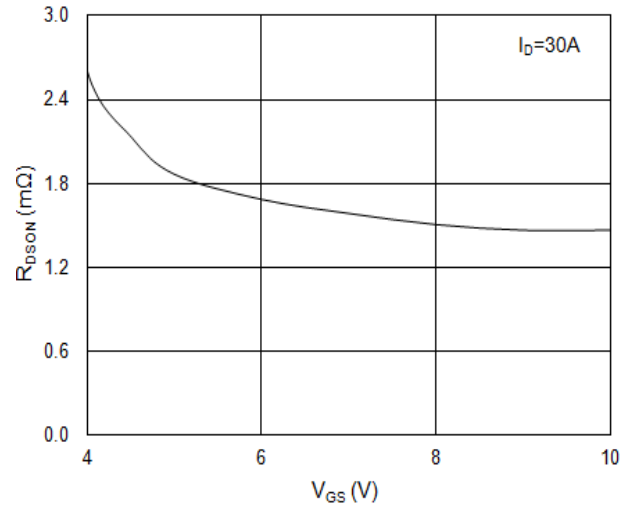
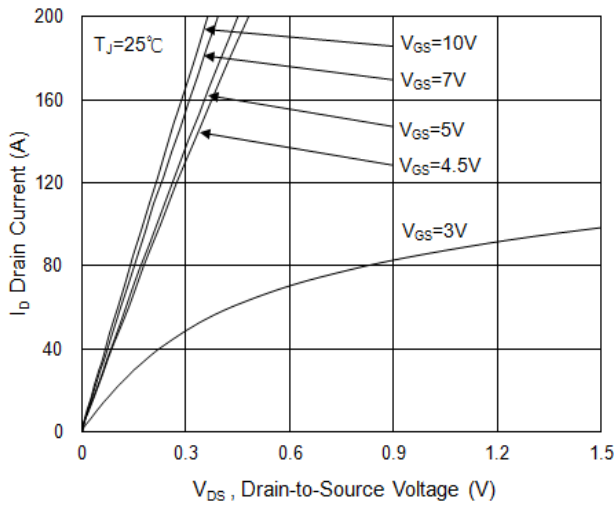
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current ^{1,6}	$V_G=V_D=0V$, Force Current	--	--	145	A
I_{SM}	Pulsed Source Current ^{2,6}		--	--	290	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0V$, $I_S=1A$, $T_J=25^\circ C$	--	--	1.2	V
t_{rr}	Reverse Recovery Time	$I_F=15A$, $di/dt=100A/\mu s$, $T_J=25^\circ C$	--	54.8	--	nS
Q_{rr}	Reverse Recovery Charge		--	67.1	--	nC

Note:

1. Test data conducted with surface mount attachment to 1 inch², FR-4 board utilizing 2oz copper
2. Pulse Test. Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
3. EAS data is a maximum rating. The test condition is $V_{DD}=25V$, $V_{GS}=10V$, $L=0.1mH$
4. The power dissipation is limited by a 150°C maximum junction temperature
5. The Min. value is 100% EAS tested guarantee
6. The data is theoretically the same as I_D and I_{DM} . In real applications, it will be limited by total power
7. The maximum current rating is package limited.

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Typical Characteristics



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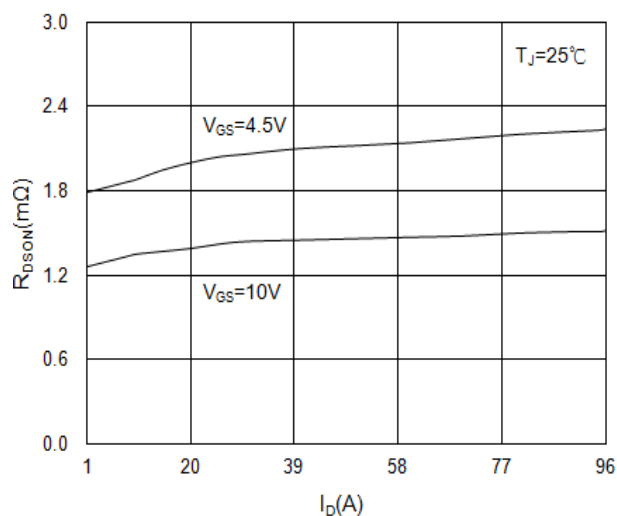


Fig.7: Drain-Source On-State Resistance

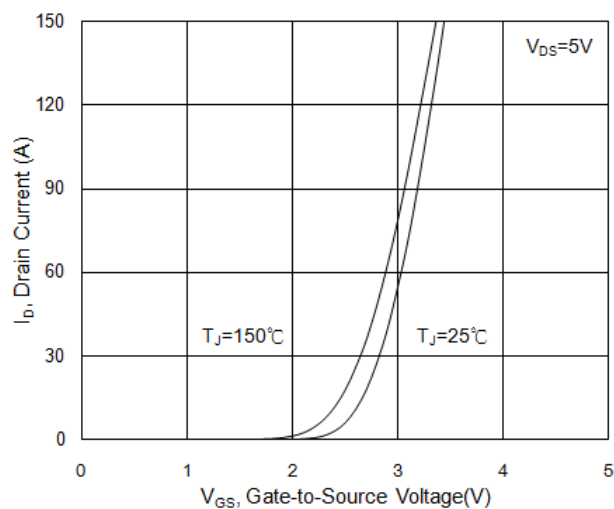


Fig.8: Transfer Characteristics

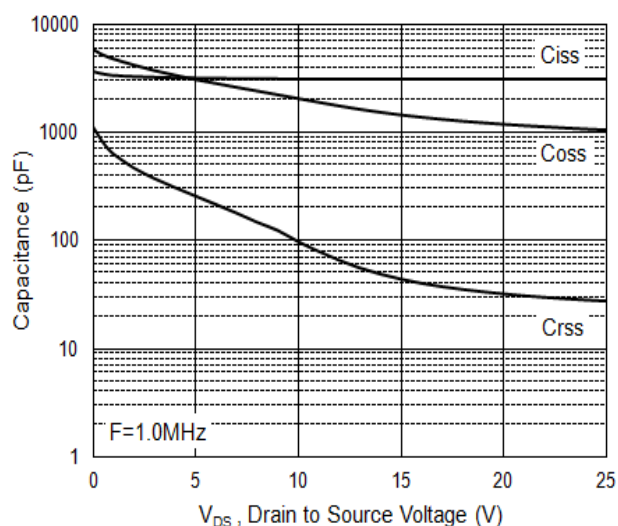


Fig.9: Capacitance

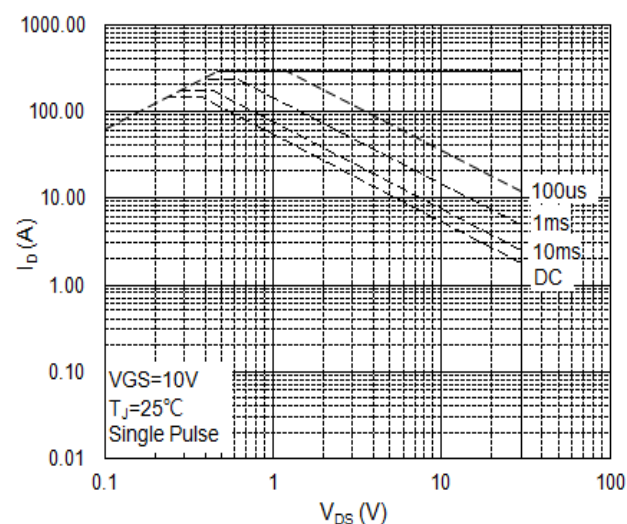


Fig.10: Safe Operating Area

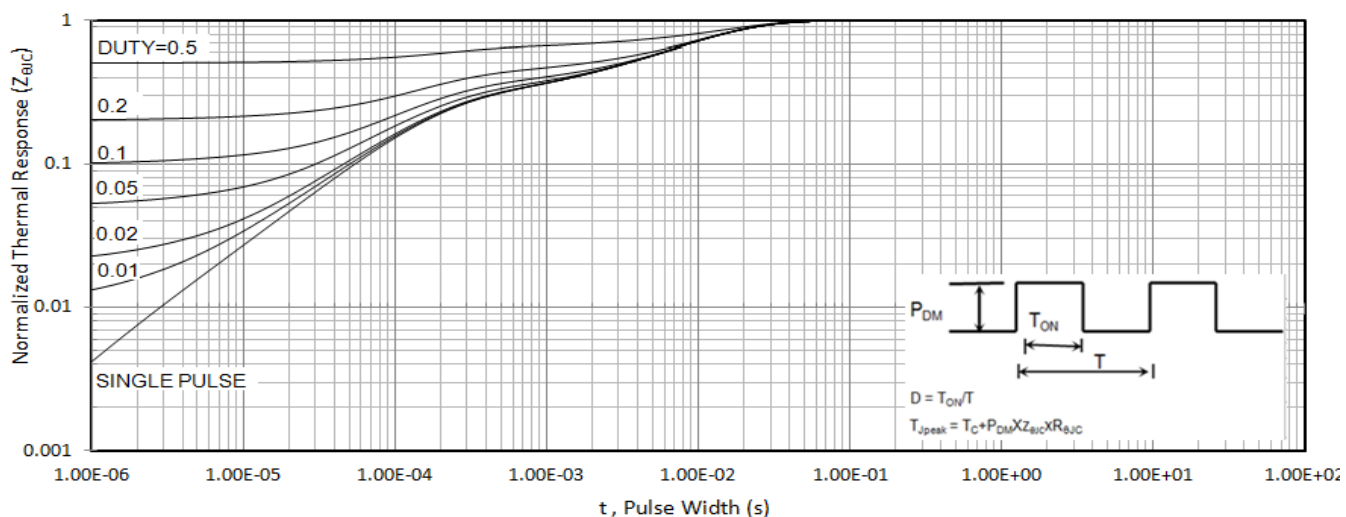


Fig.11: Transient Thermal Impedance

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