

Reverse Engineering Radio Signals

Zezadas

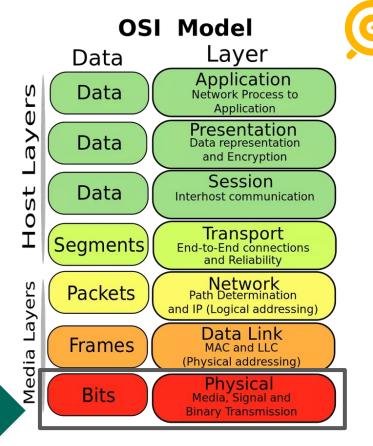
Currently working at S218

- https://peidei.me
- https://sefod.eu
- **9** @0xz3z4d45



Physical Layer

- In wire Voltage, timing, and wiring defining 1s and 0s
- In wireless Patterns of energy being sent over RF medium







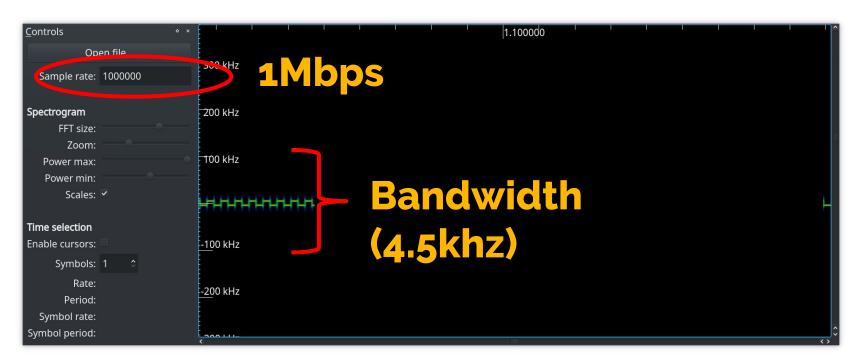
- OSINT
- Characterize the channel
- Identify Modulation
- Determine Symbol Rate
- Synchronize
- Extract Symbols





- Sample rate 1Mbps
- Frequency 153.350 Mhz



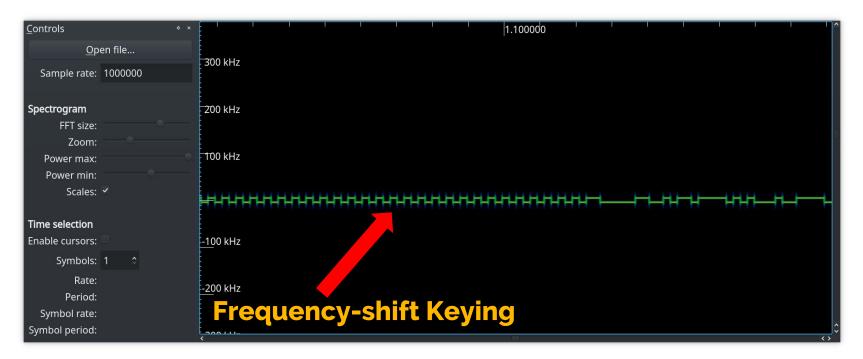




OSINT

 Characterize the channel Identify Modulation
 Determine Symbol Rate
 Synchronize
 Extract Symbols

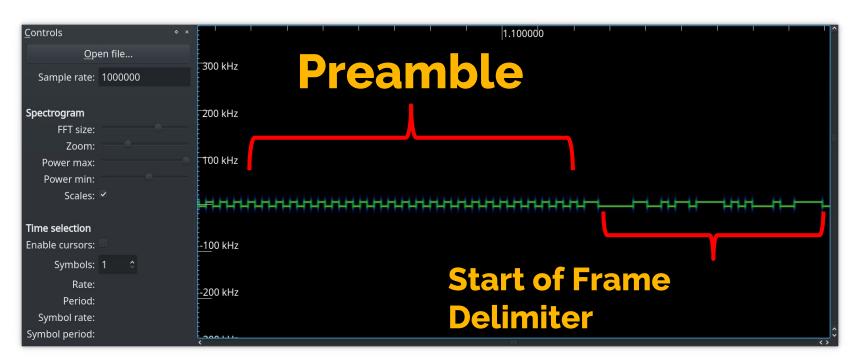






- Characterize the channel
- Identify Modulation Determine Symbol Rate Synchronize Extract Symbols







- Characterize the channel
- Identify ModulationDetermine Symbol Rate
- Synchronize Extract Symbols

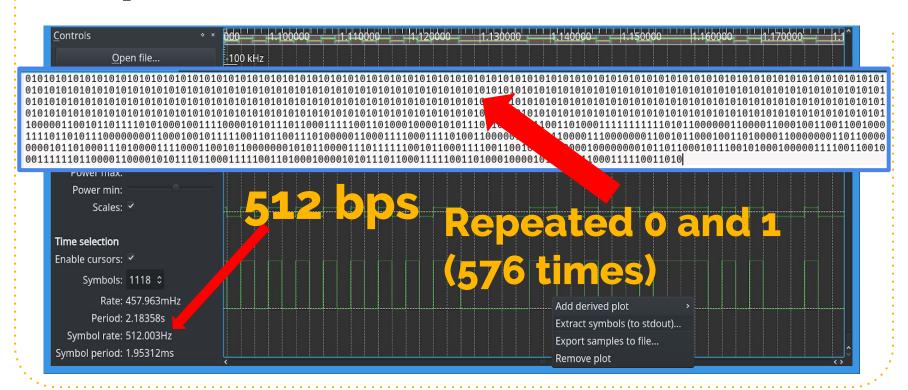






- Characterize the channel
- Identify Modulation
- Determine Symbol Rate
- Synchronize Extract Symbols



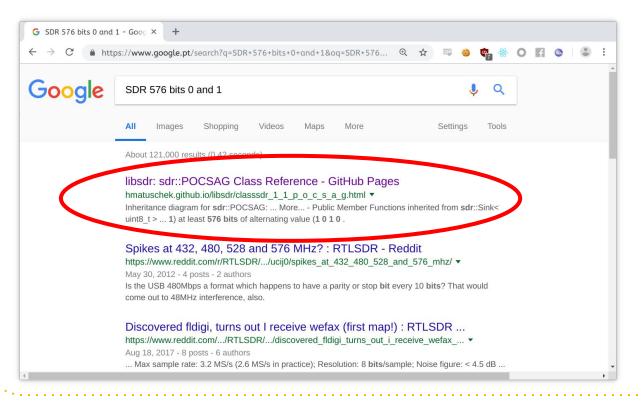




- Characterize the channel
- Identify Modulation
- Determine Symbol Rate
- Synchronize
- Extract Symbols











Pager





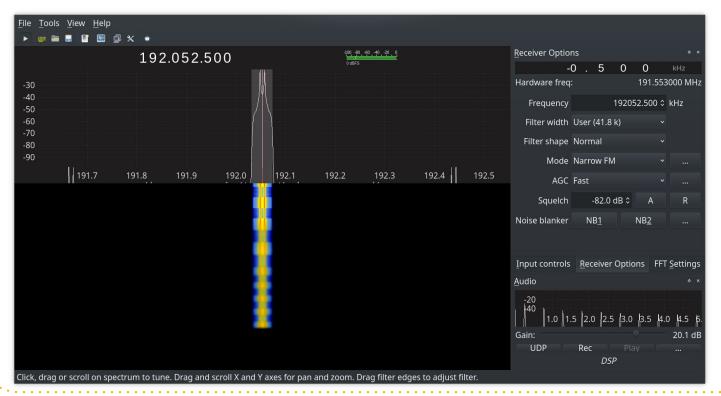




- OSINT
- Characterize the channel
- Identify Modulation
- Determine Symbol Rate
- Synchronize
- Extract Symbols







Decode:



```
File Edit View Bookmarks Settings Help
anon@unknown ~ nc -l -u -p 7355 | sox -t raw -esigned-integer -b 16 -r 48000 - -esigned-integer
-b 16 -r 22050 -t raw - | multimon-ng -t raw -a POCSAG512 -a POCSAG1200 -a POCSAG2400 -f alpha -
multimon-na 1.1.5
 (C) 1996/1997 by Tom Sailer HB9JNX/AE4WA
 (C) 2012-2018 by Elias Oenal
Available demodulators: POCSAG512 POCSAG1200 POCSAG2400 FLEX EAS UFSK1200 CLIPFSK FMSFSK AFSK1200 AFS
K2400 AFSK2400_2 AFSK2400_3 HAPN4800 FSK9600 DTMF ZVEI1 ZVEI2 ZVEI3 DZVEI PZVEI EEA EIA CCIR MORSE_CW
DUMPCSV X10 SCOPE
Enabled demodulators: POCSAG512 POCSAG1200 POCSAG2400
POCSAG512: Address: 1337 Function: 3 Alpha:
                                                 flag-POCSAG_is_the_real_deal
POCSAG512: Address: 1337 Function: 3 Alpha:
                                                 flag-POCSAG is the real deal
                                                 flag-POCSAG_is_the_real_deal
POCSAG512: Address: 1337 Function: 3 Alpha:
```





```
File Edit View Projects Bookmarks Sessions Tools Settings Help
                  #!/usr/bin/env python3
                                                                                                                                                                           64 ▼ def getMessage(bits):
                  #Author zezadas
                                                                                                                                                                           66 v » raw_data = bits.decode('utf-8')
                  #Credits nunohumberto
                 import sys
                                                                                                                                                                          69 V »
                                                                                                                                                                                            » raw_data = ''.join([["0","1"][i=="0"] for i in raw_data])
                  from ecc import bch, afield
                  import random
                                                                                                                                                                                             print verbose(raw data)
                  NRZ I = True
                                                                                                                                                                                             if raw data[0:576] != preamble:
                                                                                                                                                                                           » print("incorrect preamble")
                  VERBOSE = False
                  INJECT ERROR = False
                                                                                                                                                                                          » sys.exit()
                                                                                                                                                                           76
                  BCH M = 5
                                                                                                                                                                          77 v »
                                                                                                                                                                                             payload = raw data[576:]
        14
                  codeword_size=32 #32bits
                                                                                                                                                                          79 V »
                                                                                                                                                                                             if len(payload) < batch size:</pre>
                 frame size=2*codeword size #1frame=2words
                                                                                                                                                                           80 V »
                                                                                                                                                                                                     print("incorrect payload size %s" % len(payload))
        17 vbatch size=codeword size + (8*frame size) #sc + 8 frames
                                                                                                                                                                           81 V »
                                                                                                                                                                                         » sys.exit()
                iso7bit size=7 #ISO 7-bits ISO 646
                                                                                                                                                                           82
                 num bit size=4
                                                                                                                                                                                             for batch idx in range(int(len(payload)/batch size)):
                                                                                                                                                                                                     print verbose("new batch %s" % batch idx)
                  preamble = "10"*288 #576bits 1 and 0
                                                                                                                                                                                                     batch =
                  sync_word = "01111100110100100001010111011000" #0x7CD215D8
                                                                                                                                                                                                     payload[batch size*batch idx:batch size*(batch idx+1)]
                                                                                                                                                                                                     process_batch(batch)
                  idle word = "01111010100010011100000110010111" #0x7A89C197
                                                                                                                                                                           87
        24
                                                                                                                                                                          88 🔻 »
                                                                                                                                                                                             if msg type=="11":
                  addr=""
                  msa words=""
                                                                                                                                                                                                     for i in range(int(len(msg_words)/iso7bit_size)):
                  msq_type=""
                                                                                                                                                                                                              chr(int(msq words[iso7bit size*i:iso7bit size*(i+1)]
        29 ▼ def print_verbose(msg):
                                                                                                                                                                                                              [::-1],2))
        30 -
                           if VERBOSE:
                                                                                                                                                                                             elif msg_type=="00":
        31 🔻
                                     print(msq)
                                                                                                                                                                                                     dc=["<Spare>","<Urgent>","<space>","'","]","["]
                                                                                                                                                                                                     for i in range((len(msg_words)/num_bit_size)):
        33 ▼ def loadFile(filename):
                                                                                                                                                                                                             num = int(msg words[num bit size*i:num bit size*(i+1)]
        34 ▼» f = open(filename, 'rb')
                                                                                                                                                                                                             \Gamma::-11.2)
                          return f.read().strip()
                                                                                                                                                                                                             if num > 9:
        36
                                                                                                                                                                                                                      dc idx= num-10
     Line 16. Column 42
                                                                                                                                                                                                                                                                              INSERT Soft Tabs: 4 v UTF-8 v Python v

Search and Replace 

Current Project

Current Proje
```





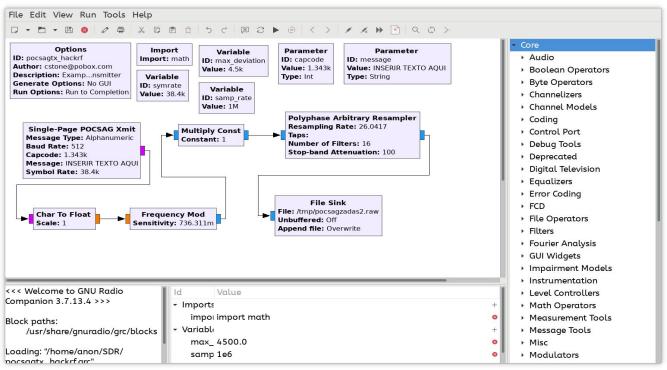
https://github.com/zezadas/pypocsag



Demo







Winner(s)



1. Nuno Humberto

Assets



- inspectrum (https://github.com/miek/inspectrum)
- gqrx (https://github.com/csete/gqrx)
- https://www.fontenay-ronan.fr/decoding-pocsag-on-ubuntu-witha-rtl-sdr-dongle/
- pypocsag (https://github.com/zezadas/pypocsag)
- gr-mixalot (https://github.com/unsynchronized/gr-mixalot)
- gr-mixalot arch package
 (https://aur.archlinux.org/packages/gr-mixalot-git/)
- Reverse Signals https://www.youtube.com/watch?v=QeoGQwToZ1Y



Thanks!

https://peidei.me

https://sefod.eu

9 @0xz3z4d45