

1. Description

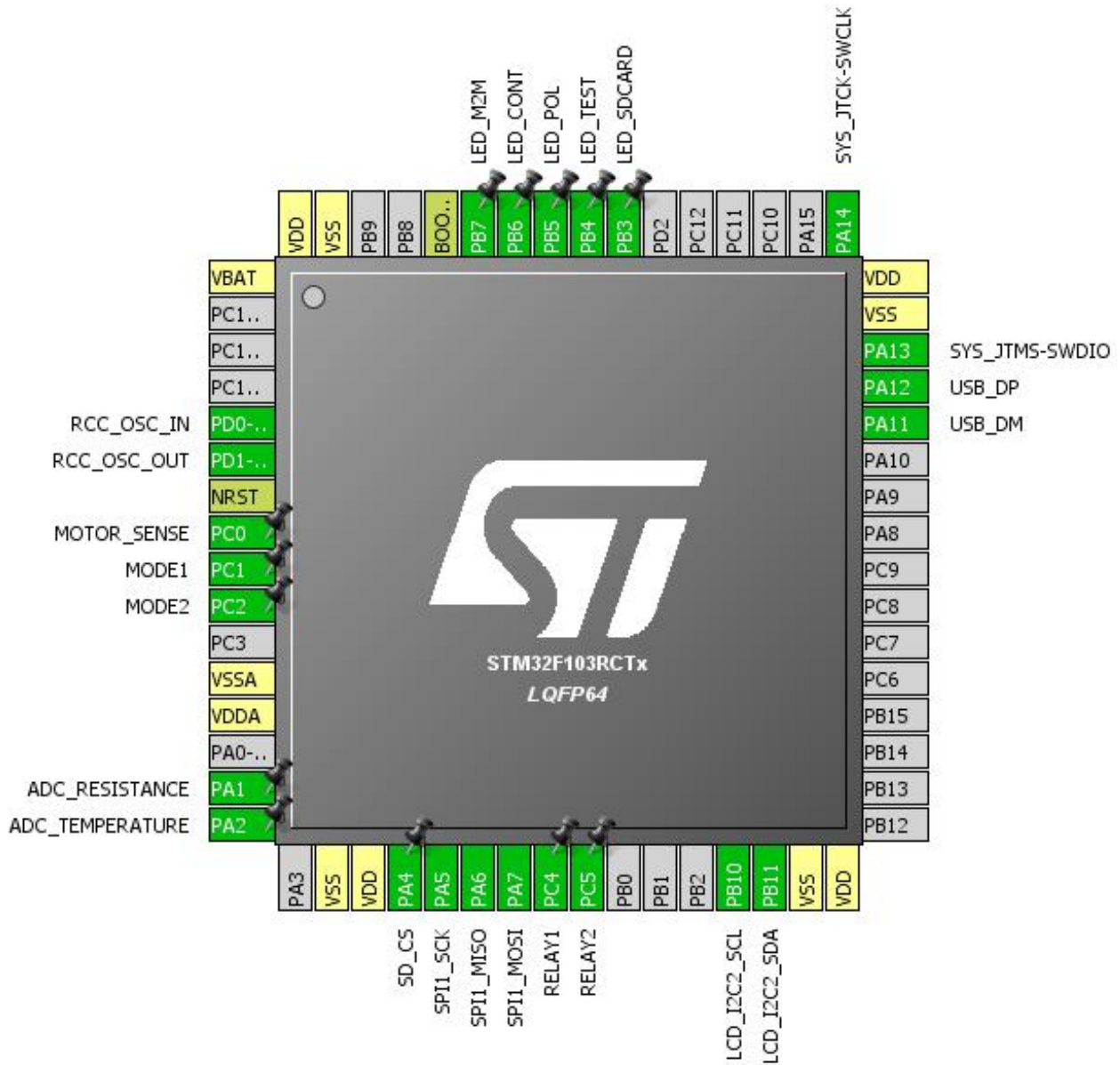
1.1. Project

Project Name	Vijay_Prj1
Board Name	custom
Generated with:	STM32CubeMX 4.27.0
Date	10/02/2019

1.2. MCU

MCU Series	STM32F1
MCU Line	STM32F103
MCU name	STM32F103RCTx
MCU Package	LQFP64
MCU Pin number	64

2. Pinout Configuration



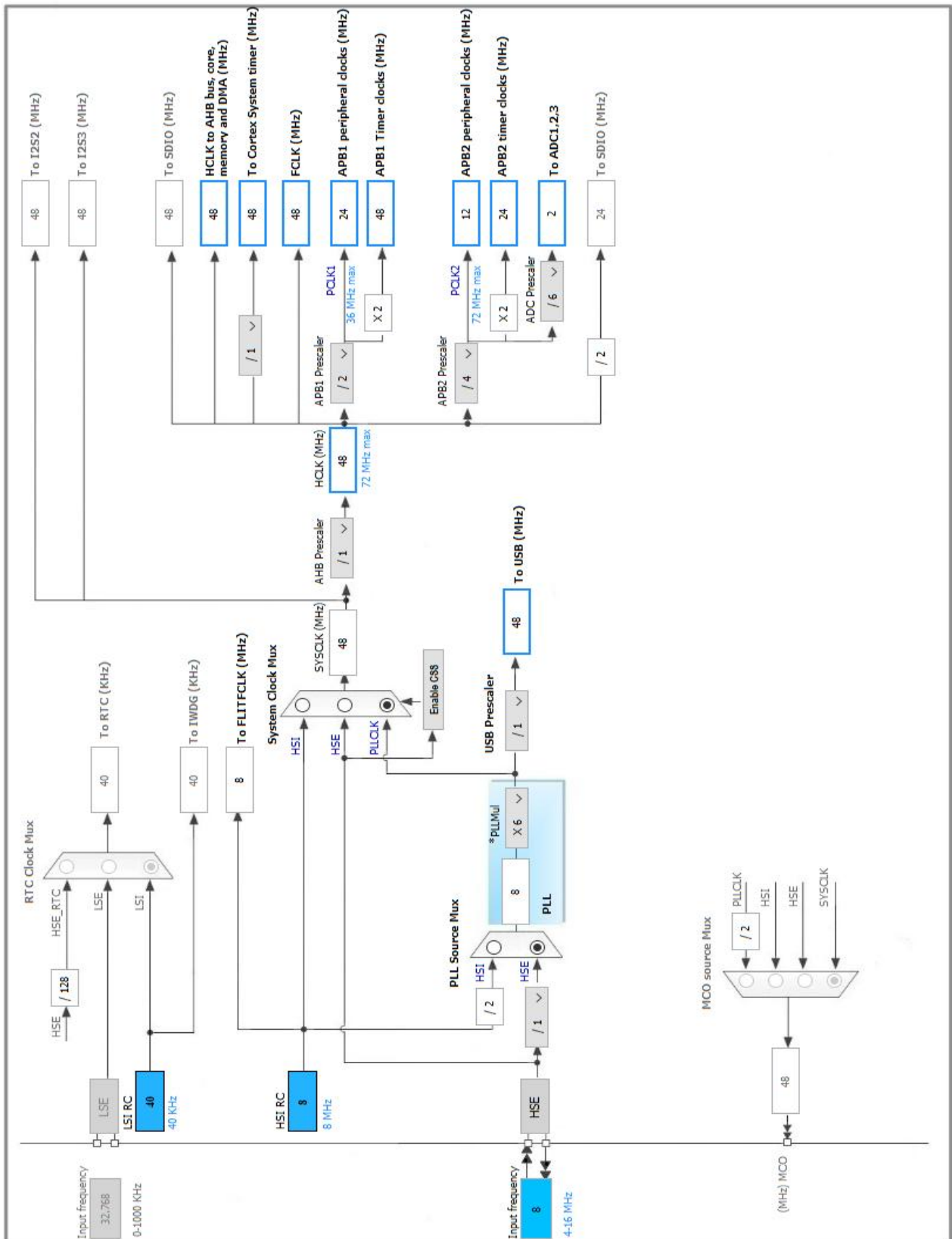
3. Pins Configuration

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
5	PD0-OSC_IN	I/O	RCC_OSC_IN	
6	PD1-OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
8	PC0 *	I/O	GPIO_Input	MOTOR_SENSE
9	PC1 *	I/O	GPIO_Input	MODE1
10	PC2 *	I/O	GPIO_Input	MODE2
12	VSSA	Power		
13	VDDA	Power		
15	PA1	I/O	ADC1_IN1	ADC_RESISTANCE
16	PA2	I/O	ADC1_IN2	ADC_TEMPERATURE
18	VSS	Power		
19	VDD	Power		
20	PA4 *	I/O	GPIO_Output	SD_CS
21	PA5	I/O	SPI1_SCK	
22	PA6	I/O	SPI1_MISO	
23	PA7	I/O	SPI1_MOSI	
24	PC4 *	I/O	GPIO_Output	RELAY1
25	PC5 *	I/O	GPIO_Output	RELAY2
29	PB10	I/O	I2C2_SCL	LCD_I2C2_SCL
30	PB11	I/O	I2C2_SDA	LCD_I2C2_SDA
31	VSS	Power		
32	VDD	Power		
44	PA11	I/O	USB_DM	
45	PA12	I/O	USB_DP	
46	PA13	I/O	SYS_JTMS-SWDIO	
47	VSS	Power		
48	VDD	Power		
49	PA14	I/O	SYS_JTCK-SWCLK	
55	PB3 *	I/O	GPIO_Output	LED_SDCARD
56	PB4 *	I/O	GPIO_Output	LED_TEST
57	PB5 *	I/O	GPIO_Output	LED_POL
58	PB6 *	I/O	GPIO_Output	LED_CONT
59	PB7 *	I/O	GPIO_Output	LED_M2M
60	BOOT0	Boot		
63	VSS	Power		

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
64	VDD	Power		

* The pin is affected with an I/O function

4. Clock Tree Configuration



5. IPs and Middleware Configuration

5.1. ADC1

mode: IN1

mode: IN2

5.1.1. Parameter Settings:

ADCs_Common_Settings:

Mode Independent mode

ADC_Settings:

Data Alignment Right alignment

Scan Conversion Mode Disabled

Continuous Conversion Mode Disabled

Discontinuous Conversion Mode Disabled

ADC_Regular_ConversionMode:

Enable Regular Conversions Enable

Number Of Conversion 1

External Trigger Conversion Source Regular Conversion launched by software

Rank 1

Channel Channel 1

Sampling Time 1.5 Cycles

ADC_Injected_ConversionMode:

Number Of Conversions 0

WatchDog:

Enable Analog WatchDog Mode false

5.2. I2C2

I2C: I2C

5.2.1. Parameter Settings:

Master Features:

I2C Speed Mode Standard Mode

I2C Clock Speed (Hz) 100000

Slave Features:

Clock No Stretch Mode Disabled

Primary Address Length selection 7-bit

Dual Address Acknowledged Disabled

Primary slave address 0

General Call address detection Disabled

5.3. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

5.3.1. Parameter Settings:

System Parameters:

VDD voltage (V)	3.3
Prefetch Buffer	Enabled
Flash Latency(WS)	1 WS (2 CPU cycle)

RCC Parameters:

HSI Calibration Value	16
HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	5000

5.4. SPI1

Mode: Full-Duplex Master

5.4.1. Parameter Settings:

Basic Parameters:

Frame Format	Motorola
Data Size	8 Bits
First Bit	MSB First

Clock Parameters:

Prescaler (for Baud Rate)	2
Baud Rate	6.0 MBits/s *
Clock Polarity (CPOL)	Low
Clock Phase (CPHA)	1 Edge

Advanced Parameters:

CRC Calculation	Disabled
NSS Signal Type	Software

5.5. SYS

Debug: Serial Wire

Timebase Source: SysTick

5.6. TIM2

Clock Source : Internal Clock

5.6.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value)	480-1 *
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value)	1000-1 *
Internal Clock Division (CKD)	No Division
auto-reload preload	Disable

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit)	Disable (Trigger input effect not delayed)
Trigger Event Selection	Reset (UG bit from TIMx_EGR)

5.7. TIM3

Clock Source : Internal Clock

5.7.1. Parameter Settings:

Counter Settings:

Prescaler (PSC - 16 bits value)	480-1 *
Counter Mode	Up
Counter Period (AutoReload Register - 16 bits value)	10000-1 *
Internal Clock Division (CKD)	No Division
auto-reload preload	Disable

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit)	Disable (Trigger input effect not delayed)
Trigger Event Selection	Reset (UG bit from TIMx_EGR)

5.8. USB

mode: Device (FS)

5.8.1. Parameter Settings:

Basic Parameters:

Speed	Full Speed 12MBit/s
Endpoint 0 Max Packet size	8 Bytes

Power Parameters:

Low Power	Disabled
Link Power Management	Disabled
Battery Charging	Disabled

5.9. USB_DEVICE

Class For FS IP: Communication Device Class (Virtual Port Com)

5.9.1. Parameter Settings:

Basic Parameters:

USBD_MAX_NUM_INTERFACES (Maximum number of supported interfaces)	1
USBD_MAX_NUM_CONFIGURATION (Maximum number of supported configuration)	1
USBD_MAX_STR_DESC_SIZ (Maximum size for the string descriptors)	512
USBD_SUPPORT_USER_STRING (Enable user string descriptor)	Disabled
USBD_SELF_POWERED (Enabled self power)	Enabled
USBD_DEBUG_LEVEL (USBD Debug Level)	0: No debug message

Class Parameters:

USB CDC Rx Buffer Size	1000
USB CDC Tx Buffer Size	1000

5.9.2. Device Descriptor:

Device Descriptor:

VID (Vendor Identifier)	1155
LANGID_STRING (Language Identifier)	English(United States)
MANUFACTURER_STRING (Manufacturer Identifier)	STMicroelectronics

Device Descriptor FS:

PID (Product Identifier)	22336
PRODUCT_STRING (Product Identifier)	STM32 Virtual ComPort
SERIALNUMBER_STRING (Serial number)	00000000001A
CONFIGURATION_STRING (Configuration Identifier)	CDC Config
INTERFACE_STRING (Interface Identifier)	CDC Interface

*** User modified value**

6. System Configuration

6.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
ADC1	PA1	ADC1_IN1	Analog mode	n/a	n/a	ADC_RESISTANCE
	PA2	ADC1_IN2	Analog mode	n/a	n/a	ADC_TEMPERATURE
I2C2	PB10	I2C2_SCL	Alternate Function Open Drain	n/a	High *	LCD_I2C2_SCL
	PB11	I2C2_SDA	Alternate Function Open Drain	n/a	High *	LCD_I2C2_SDA
RCC	PD0-OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PD1-OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SPI1	PA5	SPI1_SCK	Alternate Function Push Pull	n/a	High *	
	PA6	SPI1_MISO	Input mode	No pull-up and no pull-down	n/a	
	PA7	SPI1_MOSI	Alternate Function Push Pull	n/a	High *	
SYS	PA13	SYS_JTMS-SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK-SWCLK	n/a	n/a	n/a	
USB	PA11	USB_DM	n/a	n/a	n/a	
	PA12	USB_DP	n/a	n/a	n/a	
GPIO	PC0	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	MOTOR_SENSE
	PC1	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	MODE1
	PC2	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	MODE2
	PA4	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	SD_CS
	PC4	GPIO_Output	Output Push Pull	Pull-down *	Medium *	RELAY1
	PC5	GPIO_Output	Output Push Pull	Pull-down *	Medium *	RELAY2
	PB3	GPIO_Output	Output Push Pull	Pull-down *	Low	LED_SDCARD
	PB4	GPIO_Output	Output Push Pull	Pull-down *	Low	LED_TEST
	PB5	GPIO_Output	Output Push Pull	Pull-down *	Low	LED_POL
	PB6	GPIO_Output	Output Push Pull	Pull-down *	Medium *	LED_CONT
	PB7	GPIO_Output	Output Push Pull	Pull-down *	Medium *	LED_M2M

6.2. DMA configuration

nothing configured in DMA service

6.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Prefetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
ADC1 and ADC2 global interrupts	true	0	0
USB low priority or CAN RX0 interrupts	true	0	0
TIM2 global interrupt	true	0	0
TIM3 global interrupt	true	0	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
USB high priority or CAN TX interrupts	unused		
I2C2 event interrupt	unused		
I2C2 error interrupt	unused		
SPI1 global interrupt	unused		

* User modified value

7. Power Consumption Calculator report

7.1. Microcontroller Selection

Series	STM32F1
Line	STM32F103
MCU	STM32F103RCTx
Datasheet	14611_Rev12

7.2. Parameter Selection

Temperature	25
Vdd	3.3

8. Software Project

8.1. Project Settings

Name	Value
Project Name	Vijay_Prj1
Project Folder	D:\Dev_Project\Vijay_Prj\Vijay_Prj1
Toolchain / IDE	MDK-ARM V5
Firmware Package Name and Version	STM32Cube FW_F1 V1.6.1

8.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No

9. Software Pack Report