

Defects and Dark Current in Organic Photovoltaics and Impacts on Device Physics

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by

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PREVIEW

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### **Abstract**

In this dissertation, we discussed the shape, energy level and density of defects in Organic Photovoltaic (OPV) devices by both theoretical and experimental methods. The energy band structure and intrinsic junctions of general standard and inverted OPV devices are investigated in detail. Transportation of separated holes and electrons in both structures are well analyzed. In chapter II, we developed a Monte-Carlo model to simulate charge transport process in organic materials. Marcus theory is applied to calculate the carrier transfer rate to build up hopping process in ideal lattices. Results of this model showed several properties of carriers regarding mobility, reorganization energy and Poole-Frenkel behavior. Time-of-Flight (TOF) measurement is simulated by our Monte-Carlo model. Four typical phases of TOF are fitted in the simulation, corresponding well to the experiment data. In chapter III, we introduced Impedance Spectroscopy as a useful technique to examine overall electronic component and internal structure. Geometric capacitance and depletion regions are found to describe the equivalent circuit of OPV. Another section of this chapter showed that impedance data can be also used by Mott-Schottky analysis to calculate the doping density and built-in voltage of Schottky junction, which is widely considered existing in standard OPV devices. Degradation and lifetime studies are the major topics of chapter IV. We compared three devices within different thickness of active layers under different illumination intensity. Open-circuit voltage, short-circuit current and fill factor are deeply

analyzed to explain the decrease of conversion efficiency during degradation process. In addition, we utilize a simple method to estimate the lifetime of free carriers. In chapter V, impedance spectroscopy is used to investigate the electronic structure of OPV devices, with the goal of understanding the role of defect states. Capacitance-voltage measurements in the temperature range of 77 - 300 K are carried out to determine the activation energy of trap states. Preliminary results show at least one type of defect state residing at 166 meV in standard blends of P3HT:PCBM. Furthermore we compared three different molecular weight of P3HT in both standard and inverted devices. The result shows that higher molecular weight provides deeper defects energy level, and the density of defects generated during fabrication is different for standard and inverted structures. In chapter VI, JV curve behavior, capacitor-voltage analysis and efficiency comparisons before annealing and after annealing are performed to conclude that a p-n junction is formed at the ZnO/P3HT interface in inverted devices. We also discussed the free carrier transportation in both structure and explain why annealing leads to different conversion efficiency changes. Finally the equivalent circuit is confirmed by agreement with the geometric capacitance, as well as the structure and transportation analysis discussed in rest of the dissertation.

## Table of Contents

Chapter 1 Introduction .....	1
1.1 Motivation for OPV Study.....	1
1.2 Characteristics of Photovoltaics.....	3
1.2.1 Equivalent Circuits.....	3
1.2.2 Parameters to Evaluate Efficiency .....	6
1.2.3 Ideal Efficiency .....	8
1.3 Structure and Physics of Organic Photovoltaic Devices.....	11
Chapter 2 Examining the Impact of Defects on Charge Transport through Monte Carlo Simulations and Time-of-Flight Measurements .....	17
2.1 Introduction.....	17
2.2 Background of Monte-Carlo Simulation on Organic Materials .....	19
2.3 Monte Carlo Simulations .....	21
2.4 Monte Carlo Case Studies on the Impact of Traps on Charge Transport .....	23
2.4.1 Characterizing the Field Dependence of the Mobility .....	23
2.4.2 TOF Model.....	28
2.4.3 Trap Study & Analysis.....	29
2.5 Slope Study and Fitting to Experiment Data .....	33
2.6 Conclusions.....	39
Chapter 3 Impedance Spectroscopy for Equivalent Circuit and Mott-Schottky Analysis .....	40
3.1 Introduction of Impedance Spectroscopy .....	40
3.2 Experiment.....	43
3.3 Device Structure Analysis.....	45
3.4 Introduction of Mott-Schottky Analysis .....	50
3.5 Mott-Schottky Analysis Suitability.....	52
3.6 Spin-Coat VS Drop-cast .....	53
3.7 Mott-Schottky Results .....	55
3.8 Conclusion .....	55
Chapter 4 Degradation and Carriers' Lifetime Study .....	57
4.1 Introduction.....	57
4.2 Experiment.....	58
4.3 Degradation Study .....	58
4.3.1 Mott-Schottky Analysis in Degradation Study .....	58
4.3.2 Conversion Efficiency Analysis .....	60
4.4 Carrier's Lifetime Study .....	63
4.5 Conclusion .....	65
Chapter 5 Electrical Admittance Studies .....	67
5.1 Introduction.....	67

5.2 Admittance Study.....	67
5.3 Molecular Weights Comparison .....	71
5.4 Conclusion .....	73
Chapter 6 Band Structure Analysis.....	74
6.1 Band Structure Analysis of Standard Devices .....	74
6.2 Band Structure Analysis of Inverted Devices .....	80
6.2.1 Energy Band Level of Inverted Devices.....	80
6.2.2 Experiment.....	81
6.2.3 JV Curve .....	82
6.2.4 Mott-Schottky Analysis .....	84
6.2.5 Efficiency Comparison and Structure Analysis of Inverted Device .....	89
6.2.6 Examining the Equivalent Circuit Fitting.....	91
6.3 Conclusion .....	92
Chapter 7 Conclusion.....	94
Bibliography .....	96

## List of Tables

Table 2.1 Value of the first and second slopes as a function of the disorder of the main band, corresponding to the TOF plots in Fig. 2.12. ....	36
Table 2.2. Value of the first and second slopes as a function of the disorder of traps, corresponding to the TOF plots in Fig. 2.13 .....	37
Table 2.3 Experimental and simulation results for the two slopes at different bias voltage, corresponding to the TOF plots in Fig. 2.14.....	38
Table 3.1 Comparison of geometric capacitance by two methods. (Upper) Calculated by the parallel plate capacitor equation. (Lower) Fit by the simplified equivalent circuit. ....	49
Table 3.2 Doping density and built-in voltage of the 5409 set extracted by Mott-Schottky Analysis.....	55
Table 4.1 Doping density and built-in voltage of the series 5409 during the degradation study.....	59
Table 4.2 Device parameters of the 5409 series during the degradation study. ....	60
Table 5.1. Activation energy extracted from Figs. 5.5 and 5.6. The plots of standard device made by P3HT within mw331, varied too little to extract activation energy.....	72
Table 6.1. Doping density and built-in voltage of pristine standard devices, extracted by Mott-Schottky analysis. ....	85
Table 6.2. Doping density and built-in voltage of annealed standard devices, extracted by Mott-Schottky analysis. ....	85
Table 6.3. Efficiency comparison of standard devices, before and after annealing. ....	85
Table 6.4. Doping density and built-in voltage of pristine inverted devices, extracted by Mott-Schottky analysis. ....	87
Table 6.5 Efficiency comparison of inverted devices, before and after annealing.....	89
Table 6.6. Geometric capacitance, one calculated by the parallel-plate model and one fit by the equivalent circuit in Fig. 6.3. ....	92



## List of Figures

Figure 1.1. OPV power conversion efficiency world record chart. <a href="http://www.orgworld.de/">http://www.orgworld.de/</a> .....	3
Figure 1.2. The simplest equivalent circuit for a photovoltaic device.....	4
Figure 1.3. Equivalent circuit for a photovoltaic device including series dissipation and parallel leakage. ....	6
Figure 1.4. A typical J-V curve for a photovoltaic device.....	7
Figure 1.5. Schematic diagram of connections of devices, the converter and , and the load.....	8
Figure 1.6. a), Schematic diagram of standard OPV devices. <a href="http://today.slac.stanford.edu/a/2011/03-01.htm">http://today.slac.stanford.edu/a/2011/03-01.htm</a> b), Photograph of typical devices fabrication at NREL. Each substrate contains six devices with an area of $0.1 \text{ cm}^2$ each.	13
Figure 1.7. Simple model for current generation processes. <a href="http://eecs-newsletter.mit.edu/articles/2011-spring/bulovic-lab-eletha-flores/">http://eecs-newsletter.mit.edu/articles/2011-spring/bulovic-lab-eletha-flores/</a> .....	14
Figure 2.1. Typical Density of States in organic semiconductor materials. Both the main band and the trap band contain disorder states. ....	23
Figure 2.2. Simulation plots: Square root of electric field versus mobility. The two plots differ in the reorganization energy. a): 0.25 eV, b): 0.5 eV. Both data can be approximated by the Pool-Frenkel relationship over a limited range of electric fields....	25
Figure 2.3. Mobility versus reorganization energy. Different trap percentages are added into the simulations, while other parameters are kept the same. Mobility reaches the maximum value at the same reorganization energy for all trap percentages. ....	26
Figure 2.4. Mobility versus reorganization energy. Mobility has peak values determined by reorganization energy. Dashed lines in this figure show that electric potential energy is close to reorganization energy, leading to the maximum value of mobility.....	26
Figure 2.5. Disorder of DOS versus Mobility. Mobility is quite low without any disorder. After reaching some peak value, mobility begins to gradually decrease.....	27
Figure 2.6. Typical TOF experimental data for the film: ITO/PEDOT/P3HT/Al. It clearly shows four phases: Current Spike, Plateau, Surface Recombination, and Long tail. Data courtesy Dr. Alexandre Nardes, NREL. ....	28
Figure 2.7. (Red) Ideal model without traps. (Black) model with 0.03% traps with a Gaussian distribution. Top and bottom figures display the same data. The ideal model	

only displays two of the typical phases of TOF plots, while the Gaussian trap model shows the long tail. ....	31
Figure 2.8. Three types of density of states. The main energy bands of all three profiles are formed by Gaussian distribution. (Red) Ideal Gaussian model without traps. (Green) Exponential shape-d trap states added below -5.1 eV; a quasi-straight line is seen in the tail on this log-linear scale. (Black) Gaussian shaped trap states added with a peak at approximately -4.8 eV. ....	32
Figure 2.9. Comparison of TOF simulation plots corresponding to the energy profiles in Fig. 2.8. (Red) Ideal model without traps. (Blue) 0.02% Gaussian shaped trap states added. (Black) 0.02% exponential shaped trap states added. ....	32
Figure 2.10. Simulated TOF data with fixed trap energy state at -4.96 eV. Trap percentages vary from 0.15% to 2.5%. ....	34
Figure 2.11. Simulation TOF data based on Gaussian shape trap states. Trap percentages vary from 0.005% to 0.1%. ....	35
Figure 2.12. Simulation of TOF data based on disorder of the main band varying from 0.01 eV to 0.15 eV. The two slope values of the Plateau and Surface Recombination regions are listed in Table 2.1 ....	36
Figure 2.13. Simulation of TOF data based on the degree of disorder of the trap band varying from 0 eV to 0.125 eV. The two slope values of the Plateau and Surface Recombination regions are listed in Table 2.2. ....	37
Figure 2.14. Simulation of TOF data based on the applied bias voltage varying from 25 V to 100 V. The two slope values of the Plateau and Surface Recombination regions are listed in Table 2.3. ....	38
Figure 3.1. Simplified equivalent circuit used for impedance analysis. The diode is replaced by the combination of a capacitor and a resistor in parallel. ....	41
Figure 3.2. A Nyquist plot of a circuit with a single depletion region. ....	42
Figure 3.3. Simplified equivalent circuit with two diodes in series. ....	43
Figure 3.4. Nyquist plot of a circuit with two depletion regions. ....	43
Figure 3.5 simple set up of impedance spectroscopy ....	44
Figure 3.6 Layer structure for the devices tested from Plextronics. ....	44
Figure 3.7. A Nyquist plot fit by the equivalent circuit shown in Fig. 3.3 ....	45
Figure 3.8. Simplified equivalent circuit including two depletion regions and one geometric capacitance. ....	46

Figure 3.9. A Nyquist plot fit by the equivalent circuit shown in Fig. 3.8. ....	46
Figure 3.10. A simple parallel plate capacitance model. ....	48
Figure 3.11. Simplified equivalent circuit with one depletion region and one geometric capacitance. ....	49
Figure 3.12. Bias voltage versus geometric capacitance for the three devices of the 5409 set. Data courtesy Plextronics .....	49
Figure 3.13. Mott-Schottky plot of a fully depleted device. Data courtesy Dr. Alexandre Nardes, NREL .....	51
Figure 3.14. Mott-Schottky plot of a thick device, where the thickness is substantially larger than the width of depletion layer inside.....	51
Figure 3.15 Mott-Schottky plots of a) a thin device made by spin-coating and b) a thick device made by drop-casting. ....	54
Figure 4.1. Carrier lifetime versus $V_{oc}$ for the 5409 series and the 5232 series. ....	65
Figure 5.1. Simplified equivalent circuit of ohmic back-contact or tiny junction barrier back-contact devices, which is applicable to most efficient OPV solar cells. ....	67
Figure 5.2. A typical admittance study plot, in the range of temperatures from 124.29 K to 300 K.....	68
Figure 5.3. Plot of the frequency times the derivative of capacitance versus frequency, derived from Fig. 5.2. ....	69
Figure 5.4 Arrhenius representation of the data in Fig. 5.3. ....	71
Figure 5.5. Arrhenius representation of the standard device data set. ....	72
Figure 5.6 Arrhenius representation of the inverted device data set.....	72
Figure 6.1. Energy levels of typical standard device. ....	75
Figure 6.2. Electric potential distribution throughout a standard device. (Black) Pristine device and (red) pre-annealed device. Data courtesy Dr. Alexandre Nardes, NREL.....	76
Figure 6.3. Simplified equivalent circuit with one depletion region. ....	77
Figure 6.4 Mott-Schottky plots of pristine, pre-annealed, and post-annealed devices. Data courtesy Dr. Alexandre Nardes, NREL. ....	79
Figure 6.5. Energy levels of a typical inverted device.....	81

Figure 6.6. JV curves of standard devices spin-coated at a) 500 rpm, b) 1000 rpm, and c) 1500 rpm, respectively and of inverted devices spin-coated at d) 500 rpm, e) 100 rpm, and f) 1500 rpm respectively. ....	84
Figure 6.7. Mott-Schottky plots of a) pristine and b) annealed standard devices.....	88
Figure 6.8. Mott-Schottky plots of a) pristine and b) annealed inverted devices .....	89
Figure 6.9. Equivalent circuit of inverted device, with one p-n junction and another small reverse rectifying back-contact. ....	90

PREVIEW

## **Chapter 1 Introduction**

### **1.1 Motivation for OPV Study**

Advancing renewable energy technologies is of critical importance to fulfilling the future energy needs for the planet. Solar energy is one of the most significant, due to easily obtained and unlimited sun power during daytime. Traditional study is concentrated on monocrystalline solar cells, especially silicon based. Although such devices are more efficient compared with other methods, the cost to make sufficient purity of silicon crystal is quite high, and the process consumes tremendous energy and produces significant hazardous waste.. Thin film photovoltaics were devised to reduce the cost of solar cells in lab, but the manufacture of such devices needs to be implemented in vacuum and is difficult to easily upscale and industrialize. Although recently thin film photovoltaics in the form of CdTe have done well on the marketplace, it is not clear if they can ever be scaled to become a major contributor to the world's energy flow. To overcome such disadvantages, Organic Photovoltaic (OPV) devices have become a new research field of solar energy, which is a promising low-cost technology that is approaching commercialization. Organic materials are far less expensive compared to silicon crystal. The methods of printing OPV devices at high speed on flexible substrates, or making devices by roll-to-roll processing are attractive projects for

both research and industry fields. In all, we expect that OPV is a suitable balance between system cost and conversion efficiency.

Recently several institutes and research groups have efficiencies approaching 12%. Mitsubishi Chemical Group achieves 10% efficiency in a single-junction Organic device. By applying materials and a production technology different to those used for crystalline silicon-type cells, it has developed the world's first solution conversion type organic photovoltaic. Professor Yang Yang and his research team in UCLA fabricated a tandem polymer device having conversion efficiency of 10.6% [1], certificated by NREL in Feb. 2012. Heliatek also claimed to get 12.0 percentages efficiency as the world record until now. To date, this is the highest reported OPV efficiency and is based on a multijunction device utilizing small molecule and oligomer materials.

The long term goal of OPV field is to make 15%-20% efficiency devices by low-cost organic materials. Since the efficiency has not reached the ideal value for industrialization, lifetime is not a major topic in OPV research field until now, but we expect to optimize the durability to more than ten years. Many companies and research groups have fierce competition about the OPV conversion efficiency. The Company Heliatek in German is the leader of this year. However it cannot remain its honor for a long time, as the increasing growth rate of conversion efficiency shown in Fig. 1.1.

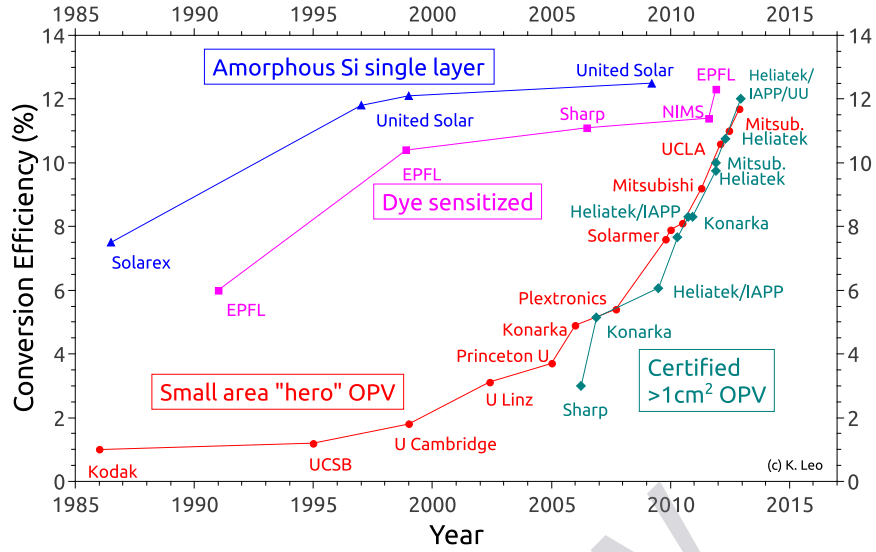


Figure 1.1. OPV power conversion efficiency world record chart.  
<http://www.orgworld.de/>

## 1.2 Characteristics of Photovoltaics

### 1.2.1 Equivalent Circuits

Several semiconductor books introduce the basic properties and simple equivalent circuit of solar cells [2, 3]. At short circuit condition, photocurrent generated by a solar cell under illumination is dependent on incident light.

$$J_{sc} = q \int b_s(\lambda) QE(\lambda) dx \quad (1.1)$$

where  $J_{sc}$  is recognized as short-circuit current density,  $b_s(\lambda)$  is the photon flux (the incident power intensity), and  $QE(\lambda)$  is the quantum efficiency of the device at a given wavelength. Generally devices at different wavelength have different conversion efficiency. Photons at a specific range of wavelengths, which provide sufficient energy to

overcome the intrinsic device's band gap, contribute to the short current that is expressed mathematically as integrating the product of the photon flux and quantum efficiency.

In dark, most solar cells perform as a diode, admitting charge transport through devices under forward bias and prohibiting conversely. The rectifying behavior is a feature of photovoltaic device, designed to choose material of suitable energy level to form junction, from where electron and hole are separated and driven to different directions. Current density through an ideal diode is expressed as,

$$J_{dark}(V) = J_0 \left( e^{qV/k_B T} - 1 \right) \quad (1.2)$$

Under illumination, photocurrent is generated in the solar cell and transport through the load. Electrically, we can describe the process and structure as a diode and a current source in parallel, representing the simple equivalent circuit of ideal device.

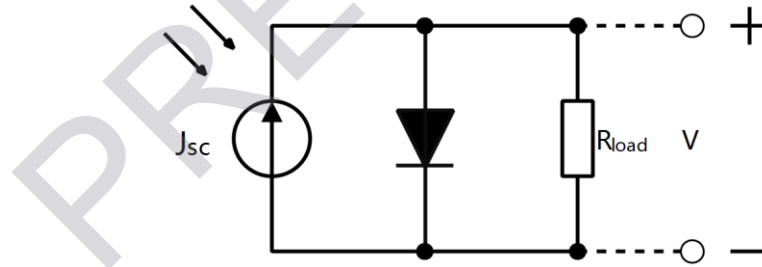


Figure 1.2. The simplest equivalent circuit for a photovoltaic device.

As shown in Fig. 1.2, the net current through  $R_{load}$  is the combination of  $J_{sc}$  generated by incident light and dark current through diode due to potential difference  $V$  on devices.



$$I(V) = I_{sc} - I_{dark}(V) \quad (1.3)$$

$$V = IR_{load} \quad (1.4)$$

Considering net current through ideal diode without charge recombination contribution in depletion layer,

$$I = I_{sc} - I_o \left( e^{qV/k_B T} - 1 \right) \quad (1.5)$$

where  $I_o$  is the saturation current. When the device does not connect any load, the potential difference reaches maximum value, open circuit voltage:

$$\begin{aligned} V_{oc} &= \frac{KT}{q} \ln \left( \frac{I_{sc}}{I_o} + 1 \right) \\ &\approx \frac{KT}{q} \ln \left( \frac{I_{sc}}{I_o} \right) \end{aligned} \quad (1.6)$$

$I_{sc} \gg I_o$ , Since reverse saturation current of most diodes in OPV devices is ignorable compared with short circuit current induced by invisible light.

In real solar cell devices, dissipation comes about from the resistance of contacts as well as leakage currents that shunt the device. Two parasitic resistances in series and in parallel are added into circuit to represent those effects (Fig. 1.3). Series resistance  $R_s$  comes from absorption materials, contact, connections and etc. It is a major cause for reduced current density. Leakage current through shunt resistance  $R_{sh}$  at the edges of cells, leads to poor rectifying property. Both dissipation mechanisms will lower the conversion efficiency.

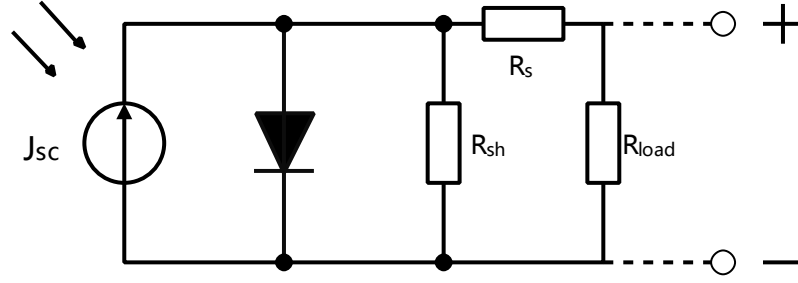


Figure 1.3. Equivalent circuit for a photovoltaic device including series dissipation and parallel leakage.

After  $R_s$  and  $R_{sh}$  are added into equivalent circuit, the net current through load is revised as

$$I = I_{sc} - I_o \left( e^{q(V + IR_s)/k_B T} - 1 \right) - \frac{V + IR_s}{R_{sh}} \quad (1.7)$$

It is obvious that we expect  $R_s$  to be small and to  $R_{sh}$  be large to produce larger photocurrent.

### 1.2.2 Parameters to Evaluate Efficiency

Several parameters are important to distinguish properties of solar cell. Output of one solar cell is in the range of voltage, from 0 to  $V_{oc}$ , current density from 0 to  $J_{sc}$ . The cell power density is determined by

$$P = JV \quad (1.8)$$

Generally we scan our devices by power supply and source measure units, in the range of bias voltage from some negative value to some positive value which should be

larger than  $V_{oc}$ . A series of current density and bias voltage in pair can be read from instrument. Note that those values are following Eq. 1.5 or Eq. 1.7 depended on ideal simplification or dissipation simplification. The plot of current versus bias is called J-V curve.

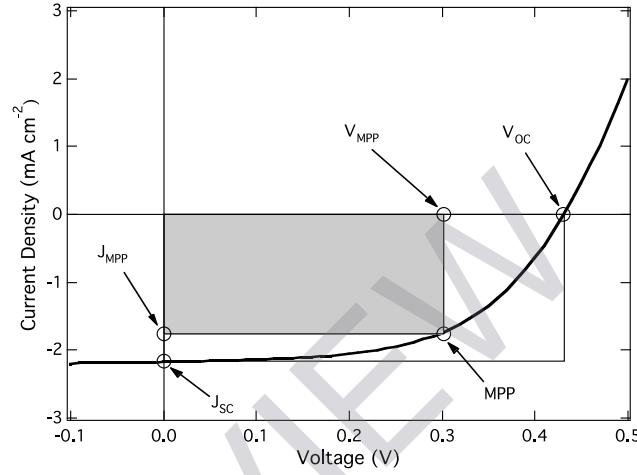


Figure 1.4. A typical J-V curve for a photovoltaic device.

Fig. 1.4 is a typical J-V cure plot. For one scan in the range of bias voltage from 0 to  $V_{oc}$ , maximum power density reaches at some point. This occurs at some voltage  $V_m$  with a corresponding current density  $J_m$ . We call it maximum power point (MPP), from which an important parameter named Fill factor ( $FF$ ) is defined as

$$FF = \frac{J_m V_M}{J_{sc} V_{oc}} \quad (1.9)$$

We also define the efficiency as maximum power density over incident light power density,

$$\begin{aligned}\eta &= \frac{J_m V_m}{P_s} \\ &= \frac{J_{sc} V_{oc} FF}{P_s} \quad (1.10)\end{aligned}$$

$J_{sc}$ ,  $V_{oc}$  and  $FF$  is determined by the internal characteristic of each device. They are the kernel parameters to calculate the efficiency. Relied on Eq. 1.7, lowering  $R_s$  or raising  $R_{sh}$  is of importance to achieve a great fill factor value.

Another issue is about how to achieve the maximum power point. An easy method is to add converters as intermediate circuits to assure the devices working near MMP.



Figure 1.5. Schematic diagram of connections of devices, the converter and , and the load.

### 1.2.3 Ideal Efficiency

According to Eq. 1.5, power of one cell device is expressed as,

$$\begin{aligned}p &= IV \\ &= I_{sc}V - I_o \left( e^{qv/kt} - 1 \right) V \quad (1.11)\end{aligned}$$

Maximum efficiency is achieved when

$$\frac{dP}{dV} = \frac{d}{dV} (IV) = 0 \quad (1.12)$$

We define  $\beta \equiv q/kT$  for simplification.  $I_m$  and  $V_m$  are the value of current and bias at maximum power point. As a result of Eq. 1.12,

$$I_{sc} - I_o (e^{\beta V_m} - 1) - I_o \beta V_m e^{\beta V_m} = 0 \quad (1.13)$$

Two exponential terms in the expression above are added together to determine the expression of  $V_m$ ,

$$\begin{aligned} V_m &= \frac{1}{\beta} \ln \left( \frac{I_{sc}}{I_o} + 1 \right) - \frac{1}{\beta} \ln (1 + \beta V_m) \\ &= V_{oc} - \frac{1}{\beta} \ln (1 + \beta V_m) \end{aligned} \quad (1.14)$$

We can also get the expression of  $I_m$  by combining Eq. 1.5 and Eq. 1.13.

$$I_m = I_o \beta V_m \exp(\beta V_m) \quad (1.15)$$

Therefore ideal efficiency is described in detail by inserting the expression of  $I_m$  into Eq. 1.10.

$$\eta = \frac{P_m}{P_s} = \frac{I_o \beta V_m^2 \exp(\beta V_m)}{P_s} \quad (1.16)$$

where  $V_m$  follows Eq. 1.14, and  $I_o$  is the saturation current of a general p-n junction, which is determined by intrinsic properties, like diffusion coefficient, carrier lifetime and energy gap  $E_g$ ,

$$I_o = AqN_cN_v \left( \frac{1}{N_A} \sqrt{\frac{D_n}{\tau_n}} + \frac{1}{N_D} \sqrt{\frac{D_p}{\tau_p}} \right) \exp \left( \frac{-E_g}{kT} \right) \quad (1.17)$$

Theoretically we can calculate the ideal efficiency, after we measure properties of diode behavior cells in dark.

Considering all the equations in this section, some relationships among those parameters need to be discussed in detail.

$$E_g \uparrow \rightarrow I_o \downarrow \rightarrow I \uparrow \ \& \ V_{oc} \uparrow \rightarrow V_m \uparrow$$

Eq. 1.17 determines the relationship between  $E_g$  and  $I_o$ . Following Eq. 1.5 and Eq. 1.6, we can straightforward conclude that net current ( $I$ ) and open circuit voltage ( $V_{oc}$ ) increase due to enhancement of  $E_g$ .  $V_m$ , which is determined by Eq. 1.14, and increase gradually by  $E_g$ , as a result of those recursive relationships. Hence the monotonicity of  $I_o$  and  $V_m$  is adverse. Based on the expression Eq. 1.16, certain energy gap would lead to optimal conversion efficiency theoretically. When we design a solar device, what is the suitable energy gap to achieve sufficient efficiency, whether the gap is too extreme for exciton separation, those details should be carefully considered.

However, practical efficiency is lowered sharply by several factors. Firstly series resistance and leakage resistance cannot be ignored. Secondly dark current is contributed more by recombination current in depletion region for real solar cell. Expression of dark current through diode is replaced as,

$$I_{dark} = I_o' \left[ \exp \left( \frac{qV}{nkT} \right) - 1 \right] \quad (1.18)$$

where  $n$  is in the range of 1 to 2 in real situations, since it is equal to 1 for ideal p-n junction, while the value is 2 for pure recombination current. The actual efficiency is lowered, as the output current is reduced within larger value of  $n$ .

### 1.3 Structure and Physics of Organic Photovoltaic Devices

Several reviews [2, 4] have discussed the complete process regarding how photon are absorbed by conjugated materials to generate excitons; excitons diffuse to interface to provide opportunity for separation of holes and electron; and free carriers travel through active layer to be collected at electrodes. We do not discuss here the more complicated but more accurate scenarios that have more recently been discussed in the literature, which depend on charge transfer state formation at the donor-acceptor interface and on more complicated morphologies than simply a mixture of pure donor and pure acceptor domains. Fig. 1.6 shows a standard OPV devices' structure for this discussion. First incident light travels through the bottom layer, which is a transparent substrate, typically made by glass. Second layer is some effective transparent conducting electrode, such as Indium Tin Oxide (ITO). ITO is historically one of the best-performing transparent, highly conductive and solid materials, widely used as electrode in OPV and OLED devices. PEDOT:PSS plays role as a buffering layer to provide intermediate work function between ITO and active layer. It has high transparency in visible range, considered as an excellent conducting and electro-optical polymer. The most important layer is the active layer that is made by blends of donor and acceptor materials, creating

bulk D/A heterojunction. The general method of fabricating such layer is to make mixture solution of donor and acceptor at a proper ratio, and spin-cast on ITO substrate. In Fig. 1.6, active layer is the thin red layer between metal electrode and ITO. The morphology of the donor and acceptor blend is determined by several factors, such as spin-cast speed, composition of two materials, solution concentration, and annealing treatment; however, the final morphology of most OPV devices is amorphous as the shape inside the active layer of Fig. 1.6 depicts. Such structure provides many “paths” for charge transport between anode and cathode, as well as tremendous junctions between donor and acceptor molecules at “everywhere” inside bulk layer. Generally, only one material (donor or acceptor) in active layer is conjugated polymer, which has great capability to absorb visible light. Typical OPV devices are made by p-type conjugated polymer and n-type fullerene derivatives, which are excellent electron acceptors. The top layer is metal electrode selected by work function. In Chapter VI, structure of standard and inverted device will be well discussed, elaborating how the work function of metal electrode affects performance.

