dOvs - Codegeneration

Group 9 Miran Hasanagić - 20084902 Jakob Graugaard Laursen - 20093220 Steven Astrup Sørensen - 201206081

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1 Instruction selection

How did you implement the missing elements in x86gen. Describe how the file works as a whole and explain a few interesting cases.

2 The support modules

Write how you implemented the missing elements in x86frame..

3 Problems encountered & experience gained

If you had any problems in the work process, write it here with what you gained.

4 5 tiger programs

5 interesting tiger programs that you find test your codegeneration in a good way.

4.1 test01.tig

4.2 test02.tig

```
/*
Test for loops,
that we update control variable correctly,
```

```
and that we do not loop to much, and that we do no enter loops where low > high */ let  \text{var a} := 0  in  \text{for i} := 1 \text{ to } 0 \text{ do a} := 99; \text{ for i} := 1 \text{ to } 3 \text{ do a} := a + i; \text{ a}  end
```

4.3 test03.tig

4.4 test04.tig

4.5 test05.tig