Md Moshiur Rahman Naeem

01860424939 miraz173r@gmail.com https://miraz173.github.io/
linkedin.com/in/miraz173/ github.com/miraz173 codeforces.com/Miraz173 leetcode/u/miraz173

EDUCATION

BSc in Computer Science and Engineering **Jan '20 – June'25**
Rajshahi University of Engineering and Technology, CGPA: 3.51/4.00 *Motihar, Rajshahi*

Higher Secondary School Certification **Jan '17 – July'19**
Bangladesh Navy College, Chattogram, GPA: 5.00/5.00 *Bandar, Chattogram*

EXPERIENCE

Junior Web Developer **June, 23 - Nov '23**
Niharon Technologies *Rajshahi*

- Developed responsive websites using ReactJS, NodeJS, HTML, and TailwindCSS,

PROJECTS (Project names are hyperlinked)

RUET Alumni Portal | *JavaScript, ReactJS, TailwindCSS, NodeJS, MySQL* **Nov'23**

- A web platform for RUET CSE alumni. Users can create, update profiles and search by batch, expertise, or location or other keywords.

Web Automation using Selenium | *Python, Selenium, BeautifulSoup* **Aug'24**

- Automated browser interactions to validate contest data availability across multiple platforms.
- Handled dynamic content loading, DOM traversal, and element synchronization.
- Designed reusable automation scripts for repetitive web workflows.

Assembly to Machine Code Interpreter and Program Simulator | *javascript, ReactJS, TailwindCSS* **Nov'23**

- Built a converter from simplified assembly to machine code, simulating flag/register updates.
- Implemented immediate, register, and branching modes (memory instructions pending).

Compiler for a hobby language | *C, Lex, Yacc, x86 Assembly* **Jan'24**

- Developed a compiler translating a C-like hobby language to x86 Assembly, generating Windows executables with MASM.
- Supported nested loops, if-else, logical/math operations, and I/O for integers/strings.

3bit computer architecture design | *digital logic design* **Sep'23**

- This project explores the fundamental design and interaction between a CPU and RAM, simulating how they work together to execute basic programs.
- Designed a 3bit CPU model (3bit ALU, 5x3bit Registers, 3bit Control Unit, carry flag) with 7x16bit RAM and ROM from basic gates.
- Simulated reading/storing values, branching, and basic math/logic operations.

5bit computer design using Verilog HDL language | *Verilog* **March'25**

- Constructed a 5bit CPU model (5bit ALU, 2bit Control Unit, 4bit flags) with a 10-bit ISA.
- Implemented SHL and NOT operations.

Object Avoiding and Line Following Robot | *Arduino Uno R3, C#* **Aug'23**

- Developed a robotic car using HC-SR04,IR sensors and SG-90,DC motors to follow lines and reroute around obstacles.

TECHNICAL SKILLS

- **Programming Languages:** C, C++, Python, JavaScript
- **Web Development:** ReactJS, NextJS, Tailwind CSS, HTML
- **Databases:** MySQL
- **Other:** Selenium, Bash, Git, Github, Docker, Linux, Verilog, x86 Assembly, Arduino, Lex/YACC

ACHIEVEMENTS

- Solved over **400** problems on Codeforces and other sites.
- **Pupil** on Codeforeces(max rating: **1259**).
- **Top 8 team** out of over 100 submissions in FOREWARN DISASTER HACKATHON 1.0, issued by FOREWARN – START NETWORK, Open Mapping Hub-Asia Pacific.