
How to Use the SAMA5D2 GPIO Under Linux®

Introduction

This application note describes how to get started using the SAMA5D2 GPIO under Linux.

Accessing the GPIO pins in user space is easier since the GPIO chip model has been introduced into the kernel.

Refer to the section [Hands-On](#) for the two ways to interact with a GPIO device in Linux user space: as GPIO sysfs is deprecated since Linux 4.8, user space should use the GPIO device node.

Refer to the section [Tools and Utilities](#) for an easier way to interact with the GPIO device node using *libgpiod*.

Reference Documents

Title	Reference	Available
SAMA5D2 Series Datasheet	DS60001476	https://www.microchip.com/design-centers/32-bit-mpus
SAMA5D27 SOM1 Kit1 User Guide	DS50002667	https://www.microchip.com/DevelopmentTools/ProductDetails/PartNO/ATSAMA5D27-SOM1-EK1

Prerequisites

- Hardware
 - PC
 - SAMA5D27 SOM1 Evaluation Kit (Part Number: ATSAMA5D27-SOM1-EK1)
 - SDCard
- Software

This demo runs on the AT91 Linux platform built by Buildroot. The first step is to set up the AT91 Buildroot development environment. Refer to the web site: <http://www.at91.com/linux4sam/bin/view/Linux4SAM/BuildRoot>

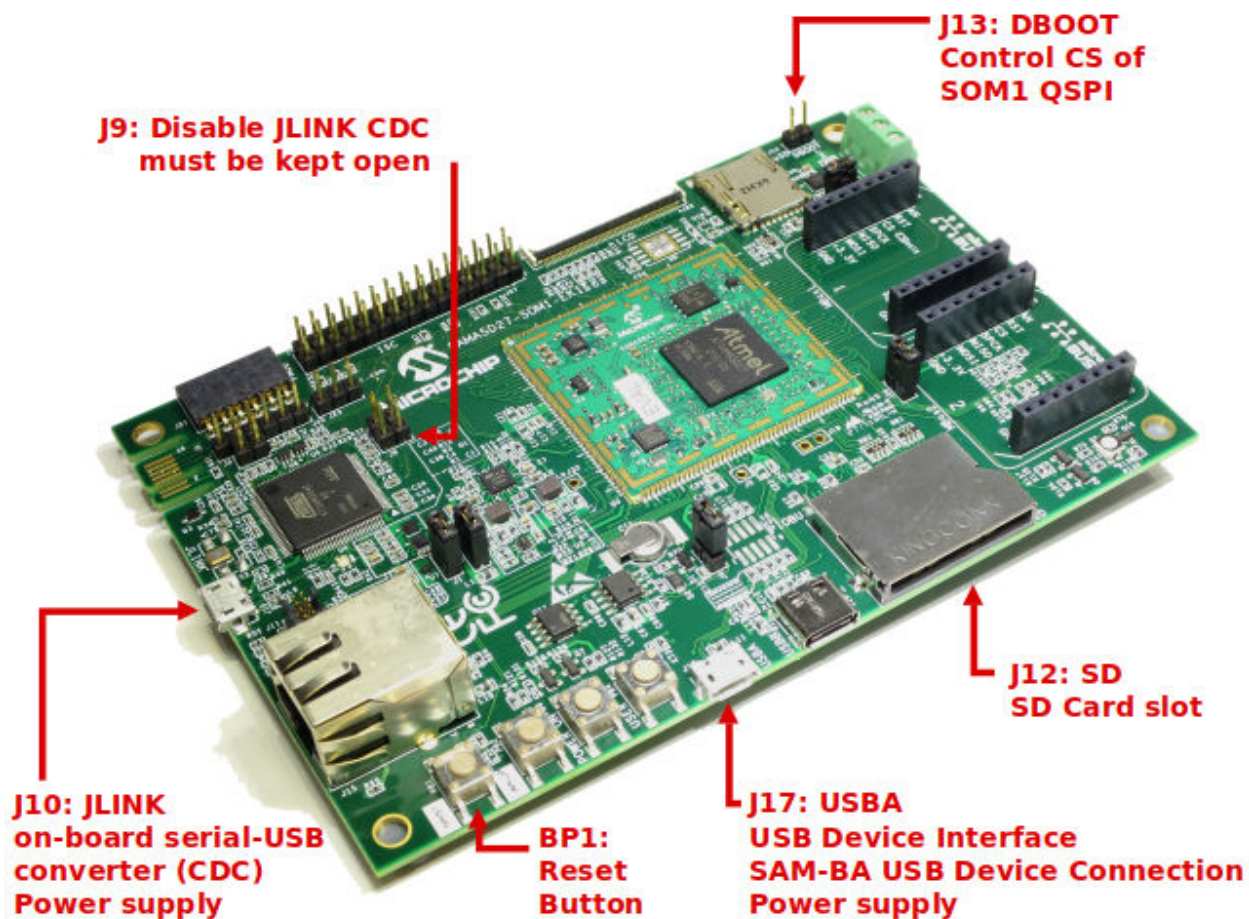
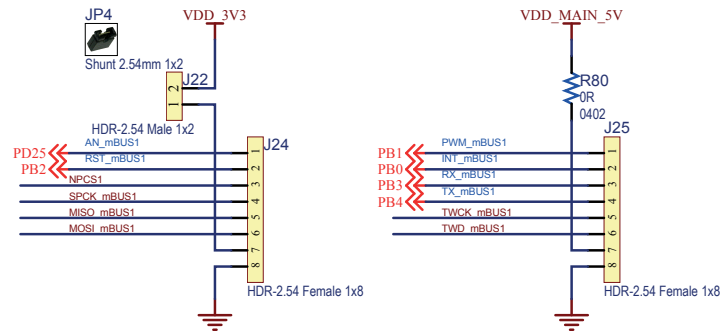


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1. Hardware Design

1.1 Interface



The connector mikroBUS1 is used for easy testing and monitoring.

This application note describes how to control GPIO PB2 (J24 pin 2) under Linux.

2. Software Design

The Microchip Linux platform was built using Buildroot with the following configuration:

```
atmel_sama5d27_som1_ek_mmc_dev_defconfig
```

The GPIO device driver works under this default configuration without any adjustment.

2.1 Device Tree

Action: no need to change

Location: buildroot-at91/output/build/linux-linux4sam_6.0/arch/arm/boot/dts

Sources: sama5d2.dtsi

Device Tree for GPIO:

```
pioA: pinctrl@fc038000 {
    compatible = "atmel,sama5d2-pinctrl";

    // specify which driver will be used for this pioA device

    reg = <0xfc038000 0x600>;
    // pioA base address is 0xfc038000, size is 0x600

    interrupts = <18 IRQ_TYPE_LEVEL_HIGH 7>,
    // 128 gpios were divided into four banks

    <68 IRQ_TYPE_LEVEL_HIGH 7>,
    // each gpio bank has its own irq line
    //check buildroot-at91/output/build/linux-linux4sam_6.0/include/dt-bindings/interrupt-
    controller/irq.h
    // for the definitions of IRQ_TYPE...

    <69 IRQ_TYPE_LEVEL_HIGH 7>,
    <70 IRQ_TYPE_LEVEL_HIGH 7>;
    interrupt-controller;
    #interrupt-cells = <2>;
    gpio-controller;
    #gpio-cells = <2>;
    clocks = <&pioA_clk>;

    // definition for pioA clock source

};

pioA_clk: pioA_clk {
    #clock-cells = <0>;
    reg = <18>;

    // PID of pioA is 18, this definition of offset will be used to enable pioA clock in PMC

    atmel,clk-output-range = <0 83000000>;

    // pioA input clock, max frequency is 83MHz

};
```

2.2 Kernel

Action: no need to change

Location: buildroot-at91/output/build/linux-linux4sam_6.0/

Defconfig: sama5_defconfig

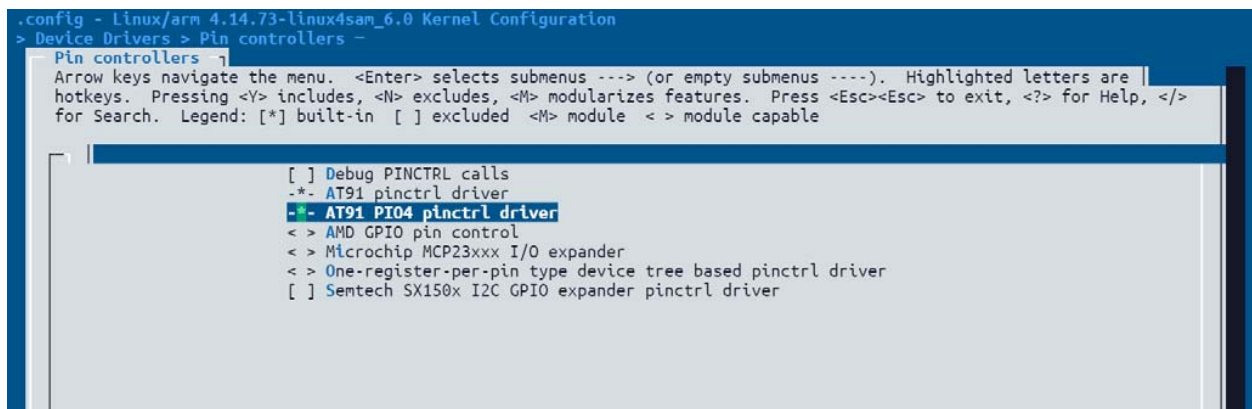
Driver files: drivers/pinctrl/pinctrl-at91-pio4.c

Check the kernel configuration for the GPIO function:

```
user@at91:~/buildroot-at91$ make linux-menuconfig
```

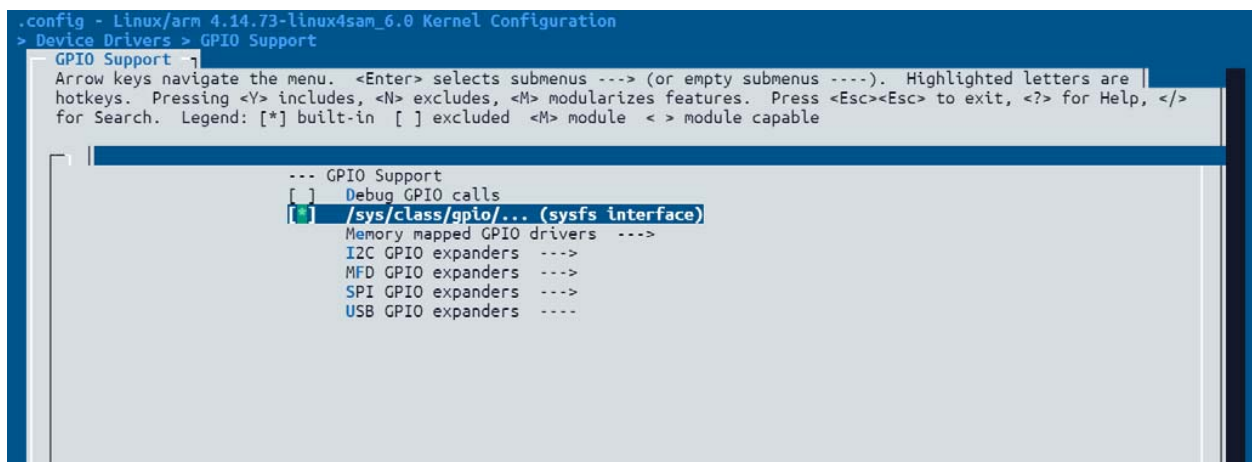
Device Drivers > Pin controllers > AT91 PIO4 pinctrl driver

With this setting, AT91 pinctrl and the GPIO driver are built into the kernel. The GPIO driver can then be accessed via a device node in rootfs (/dev/gpiochip0).



Device Drivers > GPIO Support > /sys/class/gpio/... (sysfs interface)

With this setting, sysfs for the GPIO feature is built into the kernel. The GPIO driver can then be accessed via sysfs in rootfs (/sys/class/gpio).



2.3 Rootfs

Action: no need to change

Location: buildroot-at91/output/images/rootfs.tar

Two paths (file nodes) can be used to access the GPIO driver:

- /dev/gpiochip0
 - The dev node interface can only be accessed by the C language, because most operations must be done by ioctl().
- /sys/class/gpio
 - NOT recommended: since Linux 4.8, the GPIO sysfs interface is deprecated. See [4. Tools and Utilities](#). The sysfs interface is more user-friendly, since all required operations can be done by read() and write(). It is normally used in script programs or on command lines.

2.4 Application

The following is a C language demo for accessing the GPIO driver based on a dev node:

- Compilation

```
user@at91:~$ buildroot-at91/output/host/bin/arm-buildroot-linux-uclibcgnueabi-hf-gcc
gpio.c -o gpio_test
```

- Source code

```
#include <stdio.h>
#include <stdlib.h>
#include <unistd.h>
#include <fcntl.h>
#include <string.h>
#include <linux/gpio.h>
#include <sys/ioctl.h>

#define DEV_GPIO "/dev/gpiochip0"

int main(int argc, char *argv[])
{
    int fd;
    int ret;

    struct gpiochip_info cinfo;
    struct gpioline_info linfo;
    struct gpiohandle_request req;
    struct gpiohandle_data data;

    /* open gpio */
    fd = open(DEV_GPIO, 0);
    if (fd < 0) {
        printf("ERROR: open %s ret=%d\n", DEV_GPIO, fd);
        return -1;
    }

    /* get gpio chip info */
    ret = ioctl(fd, GPIO_GET_CHIPINFO_IOCTL, &cinfo);
    if (ret < 0) {
        printf("ERROR get chip info ret=%d\n", ret);
        return -1;
    }
    printf("GPIO chip: %s, \"%s\", %u GPIO lines\n",
           cinfo.name, cinfo.label, cinfo.lines);
    ret = ioctl(fd, GPIO_GET_LINEINFO_IOCTL, &linfo);
    if (ret < 0) {
        printf("ERROR get line info ret=%d\n", ret);
        return -1;
    }
    printf("line %2d: %s\n", linfo.line_offset,
           linfo.name);

    /* set gpio_pb2 output */

    // 128 gpio in gpiochip0
    // 0 ~ 31 PA0 -> PA31
    // 32 ~ 63 PB0 -> PB31
    // 64 ~ 95 PC0 -> PC31
    // 96 ~ 127 PD0 -> PD31
```

```
req.lineoffsets[0] = 34;
req.lines = 1;
req.flags = GPIOHANDLE_REQUEST_OUTPUT;
strcpy(req.consumer_label, "RST_mBUS1");
int lhfd = ioctl(fd, GPIO_GET_LINEHANDLE_IOCTL, &req);
if (lhfd < 0) {
    printf("ERROR get line handle lhfd=%d\n", lhfd);
    return -1;
}
data.values[0] = 1;
ret = ioctl(req.fd, GPIOHANDLE_SET_LINE_VALUES_IOCTL, &data);
if (ret < 0) {
    printf("ERROR set line value ret=%d\n", ret);
    return -1;
}

while (1) {

// set gpio_pb2 low

    data.values[0] = 0;
    ioctl(req.fd, GPIOHANDLE_SET_LINE_VALUES_IOCTL, &data);
    usleep(5*1000);

// set gpio_pb2 high

    data.values[0] = 1;
    ioctl(req.fd, GPIOHANDLE_SET_LINE_VALUES_IOCTL, &data);
    usleep(5*1000);
}

/* close gpio */
close(fd);

return 0;
}
```


3. Hands-On

The GPIO driver can be accessed via dev node or sysfs.

3.1 Access Via dev node

Copy the gpio_test application to the target and execute it. This generates a 100 Hz wave via GPIO PB2 (J24 pin 2).

```
# chmod +x gpio_test
# ./gpio_test
```

3.2 Access Via sysfs

1. Export PB2.

```
# echo 34 > /sys/class/gpio/export
```

2. Set PB2 output.

```
# echo out > /sys/class/gpio/PB2/direction
```

3. Set PB2 low.

```
# echo 0 > /sys/class/gpio/PB2/value
```

4. Set PB2 high.

```
# echo 1 > /sys/class/gpio/PB2/value
```

Pins are identified by numbers recognizable by the Linux kernel. For example, the four GPIO ports are identified as PAX, PBx, PCx and PDx. Each port covers 32 GPIOs, so PA0 corresponds to 0, PA5 to 5, PB0 to 32, etc. The calculation is done according to the number of pins and ports of the MPU GPIOs.

The following table shows the correspondence between the SAMA5D27 pins and the GPIO port numbers.

Table 3-1. SAMA5D27 Pin Numbers vs. GPIO Port Numbers

SAMA5D27 Pin Numbers	GPIO Port Numbers
0 - 31	PA0 - PA31
32 - 63	PB0 - PB31
64 - 95	PC0 - PC31
96 - 127	PD0 - PD31

4. Tools and Utilities

libgpiod is a C library and tools for interacting with the Linux GPIO character device via the `/dev/gpiochipX` device node. It encapsulates ioctl calls and data structures behind a straightforward API. *libgpiod* makes it easy to access the GPIO driver with less ioctl calls.

4.1 *libgpiod* Support

With the default configuration `atmel_sama5d27_som1_ek_mmc_dev_defconfig`, the *libgpiod* feature was not enabled.

Enable *libgpiod* support in Buildroot:

```
user@at91:~/buildroot-at91$ make menuconfig
```

- Target packages > Libraries > Hardware handling > **[*] libgpiod**
- Target packages > Libraries > Hardware handling > **[*] install tools**

```
user@at91:~/buildroot-at91$ make
```

After building successfully, burn your SD card with an updated `sdcard.img`.

4.2 *libgpiod* APIs

libgpiod provides a simpler API to access the GPIO driver. Follow the C header file for these APIs:

```
user@at91:~/buildroot-at91$ vim output/build/libgpiod-0.3.2/include/gpiod.h
```

The *libgpiod* tools in the following folder can be used as demo codes to learn how to work with *libgpiod* APIs.

```
user@at91:~/buildroot-at91$ cd output/build/libgpiod-0.3.2/src/tools/
user@at91:~/buildroot-at91/output/build/libgpiod-0.3.2/src/tools$ ls *.c
gpiodetect.c gpiofind.c gpioget.c gpioinfo.c gpiomon.c gpioset.c tools-common.c
```

4.3 *libgpiod* Tools

libgpiod provides simple tools to access the GPIO driver via a command line.

It should be noted that for these *libgpiod* tools, the GPIO sysfs interface is deprecated from Linux 4.8. User space can use the character device instead. The *libgpiod* tools can then be used to access simply the GPIO driver via a command line instead of the GPIO sysfs interface.

Six commands are associated with the *libgpiod* tools:

- `gpiodetect` – lists all gpiochips present on the system, their names, labels and number of GPIO lines
- `gpioinfo` – lists all lines of specified gpiochips, their names, consumers, direction, active state and additional flags
- `gpioget` – reads values of specified GPIO lines
- `gpioset` – sets values of specified GPIO lines, potentially keeps the lines exported and waits until timeout, user input or signal
- `gpiofind` – finds the gpiochip name and line offset when given the line name
- `gpiomon` – waits for events on GPIO lines, specifies which events to watch, how many events to process before exiting or whether the events should be reported to the console

For details, visit <https://git.kernel.org/pub/scm/libs/libgpiod/libgpiod.git/about/>.

4.4 Using *libgpiod* Tools on SAMA5D27-SOM1-EK1

1. Detect the GPIO chip on the target.

```
# gpiodetect
gpiochip0 [fc038000.pinctrl] (128 lines)
```

2. Print information about each line.

```
# gpioinfo
gpiochip0 - 128 lines:
    line 0:      "PA0"      unused  input  active-high
    line 1:      "PA1"      unused  input  active-high
    line 2:      "PA2"      unused  input  active-high
    line 3:      "PA3"      unused  input  active-high
    line 4:      "PA4"      unused  input  active-high
    line 5:      "PA5"      unused  input  active-high
    line 6:      "PA6"      unused  input  active-high
    line 7:      "PA7"      unused  input  active-high
    line 8:      "PA8"      unused  input  active-high
    line 9:      "PA9"      unused  input  active-high
[...]
```

3. Find the GPIO chip name and offset for GPIO PB2.

```
# gpiofind PB2
gpiochip0 34
```

4. Set PB2 output high.

```
# gpioset gpiochip0 34=1
```

5. Set PB2 output low.

```
# gpioset gpiochip0 34=0
```

6. Toggle PB2 high for 1 second.

```
# gpioset --mode=time --sec=1 gpiochip0 34=0
```

7. Monitor the PB2 pin status.

```
# gpiomon gpiochip0 34
event: FALLING EDGE offset: 34 timestamp: [1325983345.255958082]
event: RISING EDGE offset: 34 timestamp: [1325983345.256686960]
event: FALLING EDGE offset: 34 timestamp: [1325983348.205010375]
event: RISING EDGE offset: 34 timestamp: [1325983348.577229302]
event: FALLING EDGE offset: 34 timestamp: [1325983348.657488131]
event: RISING EDGE offset: 34 timestamp: [1325983348.695700717]
event: FALLING EDGE offset: 34 timestamp: [1325983348.830615058]
event: RISING EDGE offset: 34 timestamp: [1325983349.395371156]
event: FALLING EDGE offset: 34 timestamp: [1325983349.938427156]
event: RISING EDGE offset: 34 timestamp: [1325983349.938456229]
```

5. Microchip Peripheral I/O Python® (MPIO)

The Microchip Peripheral I/O (MPIO) Python package provides easy access to various hardware peripherals found on Microchip MPU processors and evaluation boards running Linux. The API is clean, consistent, flexible, documented, and well tested. It makes navigating and exercising even the most complex hardware peripherals a trivial task.

For more information, see <https://github.com/linux4sam/mpio>. Code examples showing how to work with the MPIO interface modules are provided in the folder `mpio/examples`.

5.1 MPIO in buildroot

In order to benefit from MPIO in your buildroot configuration, follow the steps below:

1. Enable Python

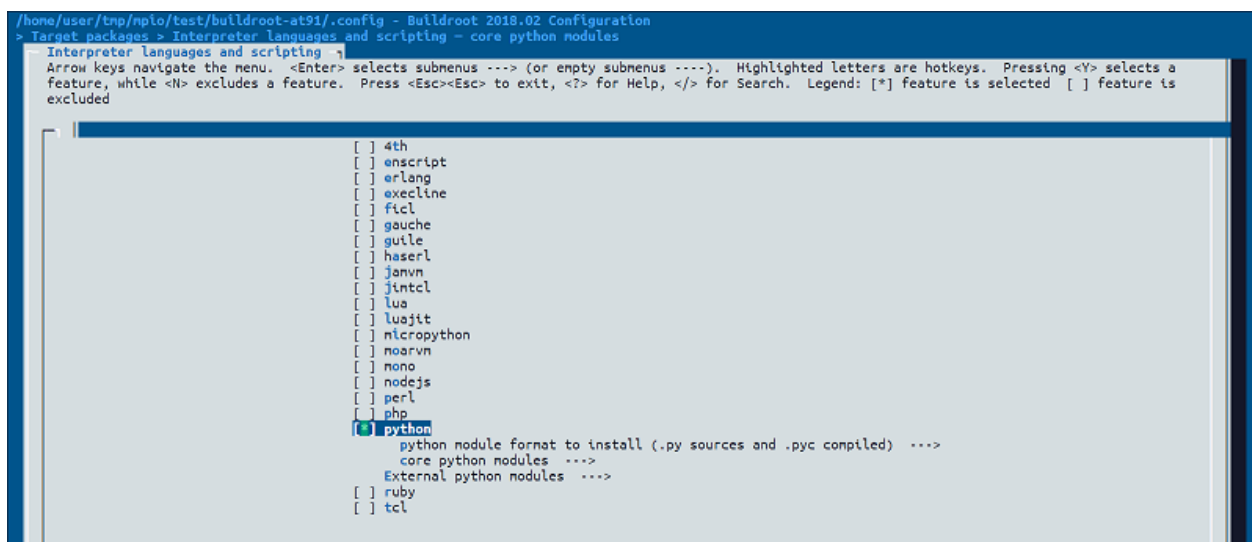
```
user@at91:~/buildroot-at91$ make menuconfig
```

Select “**python**” to enable python support:

- Target packages > Interpreter languages and scripting > [*] **python**

Then enter “**python module format to install**” and select “**.py sources and .pyc compiled**”.

- Target packages > Interpreter languages and scripting > python > python module format to install > **.py sources and .pyc compiled**



Some additional python modules must be selected. Enter “**core python modules**” and select “**curses module**”, “**readline**” and “**hashlib module**”.

- Target packages > Interpreter languages and scripting > core python modules > [*] **curses module**
- Target packages > Interpreter languages and scripting > core python modules > [*] **readline**

- Target packages > Interpreter languages and scripting > core python modules > [*] hashlib module

```

/home/user/tmp/mpio/test/buildroot-at91/.config - Buildroot 2018.02 Configuration
> Target packages > Interpreter languages and scripting > core python modules
core python modules
Arrow keys navigate the menu. <Enter> selects submenus ---> (or empty submenu ----). Highlighted letters are hotkeys. Pressing <Y> selects a feature, while <N> excludes a feature. Press <Esc><Esc> to exit, <?> for Help, </> for Search. Legend: [*] feature is selected [ ] feature is excluded

*** The following modules are unusual or require extra libraries ***
[ ] bzip2 module (NEW)
[ ] bsddb module (NEW)
[ ] codecs module (NEW)
[*] curses module
[ ] ossaudiodev module (NEW)
[*] readline
[ ] ssl (NEW)
[ ] unicodedata module
[ ] sqlite module
[ ] xml module (NEW)
[ ] zlib module (NEW)
[*] hashlib module

```

Enter “External python modules” and select “python-setuptools”.

- Target packages > Interpreter languages and scripting > External python modules > [*] python-setuptools

```

/home/user/tmp/mpio/test/buildroot-at91/.config - Buildroot 2018.02 Configuration
> Target packages > Interpreter languages and scripting > External python modules
External python modules
Arrow keys navigate the menu. <Enter> selects submenus ---> (or empty submenu ----). Highlighted letters are hotkeys. Pressing <Y> selects a feature, while <N> excludes a feature. Press <Esc><Esc> to exit, <?> for Help, </> for Search. Legend: [*] feature is selected [ ] feature is excluded

^(-)=
[ ] python-pyudev (NEW)
[ ] python-pyusb (NEW)
[ ] python-pyxb (NEW)
[ ] python-pyxml (NEW)
[ ] python-pyzmq (NEW)
[ ] python-raven (NEW)
[ ] python-remi (NEW)
[ ] python-requests (NEW)
[ ] python-requests-oauthlib (NEW)
[ ] python-requests-toolbelt (NEW)
[ ] python-rpi-gpio (NEW)
[ ] python-rtslib-fb (NEW)
[ ] python-scandir (NEW)
[ ] python-schedule (NEW)
[ ] python-sdnotify (NEW)
[ ] python-secretstorage (NEW)
[ ] python-see (NEW)
[ ] python-serial (NEW)
[ ] python-service-identity (NEW)
[ ] python-setuptools (NEW)
[*] python-setuptools
[ ] python-sh (NEW)
[ ] python-shutilwhich (NEW)

```

2. Enable the MPIO Module

Enter “External options” and select “python-mpio”.

- External options > [*] python-mpio

```

/home/user/tmp/mpio/test/buildroot-at91/.config - Buildroot 2018.02 Configuration
> External options
External options
Arrow keys navigate the menu. <Enter> selects submenus ---> (or empty submenu ----). Highlighted letters are hotkeys. Pressing <Y> selects a feature, while <N> excludes a feature. Press <Esc><Esc> to exit, <?> for Help, </> for Search. Legend: [*] feature is selected [ ] feature is excluded

*** MCHP (in /home/user/tmp/mpio/test/buildroot-external-microchip) ***
[ ] gl-decoder
*** gst1-at91 needs a toolchain w/ glibc ***
*** libplanes depends on libdrm, cairo, lua, and cJSON ***
[ ] ptc_examples
*** python-iocontrol depends on pyqt5 and mpio ***
[*] python-mpio
[ ] dt-overlay-at91
Init configuration (none) ----

```

3. Finish the buildroot Configuration and Build

Enter “Filesystem images” and set the exact size of rootfs to 120MB.

- Filesystem images > (120M) exact size

```

/home/user/tmp/mpio/test/buildroot-at91/.config - Buildroot 2018.02 Configuration
Filesystem Images
Filesystem Images
Arrow keys navigate the menu. <Enter> selects submenus --- (or empty submenus ---). Highlighted letters are hotkeys. Pressing <Y> selects a
feature, while <N> excludes a feature. Press <Esc><Esc> to exit, <?> for Help, </> for Search. Legend: [*] feature is selected [ ] feature is
excluded

[ ] axfs root filesystem
[ ] cloop root filesystem for the target device
[ ] cpio the root filesystem (for use as an initial RAM filesystem)
[ ] cramfs root filesystem
[*] ext2/3/4 root filesystem
    ext2/3/4 variant (ext4) --->
    ( ) filesystem label
    (120M) exact size
    (0) exact number of inodes (leave at 0 for auto calculation)
    (5) reserved blocks percentage
    (-O ^64bit) additional mke2fs options
    Compression method (no compression) --->

```

After saving, the following new settings are added to the configuration file of buildroot:

```

.....
BR2_PACKAGE_PYTHON=y
BR2_PACKAGE_PYTHON_PY_PYC=y
BR2_PACKAGE_PYTHON_CURSES=y
BR2_PACKAGE_PYTHON_READLINE=y
BR2_PACKAGE_PYTHON_HASHLIB=y
BR2_PACKAGE_PYTHON_SETUPTOOLS=y
BR2_PACKAGE_PYTHON_MPIO=y
BR2_TARGET_ROOTFS_EXT2_SIZE="120M"
.....

```

Then re-configure and build buildroot:

```

user@at91:~/buildroot-at91$ make atmel_sama5d27_som1_ek_mmc_dev_defconfig
user@at91:~/buildroot-at91$ make

```

5.2 Examples

After building successfully, burn your SD card with buildroot-at91/output/images/sdcard.img.

Execute the python codes on the target board, for example:

```

# ./adc2.py DEVICE
# ./gpio1.py PIN
# ./pwm_led.py DEVICE CHANNEL
.....

```

Note: The python example code can be found in <https://github.com/linux4sam/mpio/examples>

6. Revision History

6.1 Rev. A - 10/2019

First issue.

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