

Pnp HD Tuner part number - PN3034HT



2.2 SPI Timing Diagram

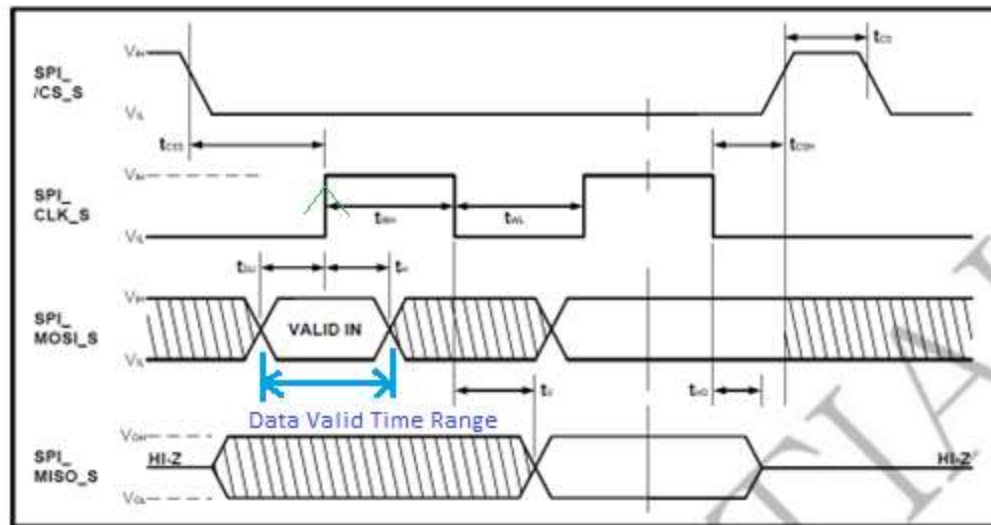


Figure - 1

From above figure in case of HD tuner data is sampled at rising edge because data is only valid t_{SU} (setup time) before occurring the rising edge and it is valid for t_{H} (hold time) after rising edge.

Before and after the $(t_{RISING_EDGE} + t_{H})$ and $(t_{RISING_EDGE} - t_{SU})$, data is not valid and it may change or latched, it is shown as slant parallel lines in above figure.

Thus it confirms that HD tuner operates in SPI Mode – 0, if CPOL = 0.