Q: Can we change the three pins PCIE_CLK_REQ, PCIE_WAKE_N and PCIE_RST_N to other GPIO for the same PCIe functionality?

A: **PCIE_CLK_REQ** is dedicated pin and bound with PCIe controller, customer can **NOT** change other GPIO for it. But if it is allowed, it can be reused as GPIO if PCIe functionality is not enabled for that target. Please check each target's device specification about such GPIO's configuration function list for double confirm.

PCIE_WAKE_N is bit-bang based and there is no limitation to swap to other **wake-up capable** GPIOs.

For PCIE_PERST, it **depends**.

a) For **MDM/SDX targets**, PCIE_PERST is hardwiredd to PCIe controller, so it could **NOT** be swapped.

b) For **MSM/SM targets**, where it is SW driven output from RC. it is **pulled down by default**, which helps guarantee the endpoint not to see PERST# deasserted before PCIe SW loaded. So the candidate should be also with such attribute. Also, please check target's device specification's PIn characteristics table, then we will know.