

Travlendar⁺

Requirement Analysis and Specification Document

Mirko Salaris, Piervincenzo Ventrella, Pietro Cassarino October 11, 2017 Deliverable: RASD

Title: Requirement Analysis and Verification DocumentAuthors: Mirko Salaris, Piervincenzo Ventrella, Pietro Cassarino

Version: 1.0

Date: 27-October-2017

 ${\bf Download~page:}~~{\rm https://github.com/mirkosalaris/CassarinoSalarisVentrella/}$

Copyright: Copyright 2017, M. Salaris, P. Ventrella, P. Cassarino

All rights reserved

Table of Contents

List of Figures List of Tables		
2	Overall Description 2.1 Product perspective	5 5 5 5 5
3	Specific Requirements 3.1 External Interface Requirements 3.1.1 User Interfaces 3.1.2 Hardware Interfaces 3.1.3 Software Interfaces 3.1.4 Communication Interfaces 3.1.5 Functional Requirements 3.1 Performance Requirements 3.1 Design Constraints 3.1 Standard compliance 3.1 Standard compliance 3.1 Standard compliance 3.1 Reliability 3.1 Reliability 3.2 Availability 3.3 Security 3.3 Security 3.5 Portability	6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6
4	Formal Analysis Using Alloy	7
5	Effort Spent	8
6	References	9

List of Figures

List of Tables

1 Introduction

- 1.1 Purpose
- 1.2 Scope
- 1.3 Definitions, Acronyms, Abbreviations
- 1.4 Revision history
- 1.5 Reference documents
- 1.6 Document Structure

2 Overall Description

- 2.1 Product perspective
- 2.2 Product functions
- 2.3 User characteristics
- 2.4 Assumptions, dependencies and constraints

3 Specific Requirements

- 3.1 External Interface Requirements
- 3.1.1 User Interfaces
- 3.1.2 Hardware Interfaces
- 3.1.3 Software Interfaces
- 3.1.4 Communication Interfaces
- 3.2 Functional Requirements
- 3.3 Performance Requirements
- 3.4 Design Constraints
- 3.4.1 Standard compliance
- 3.4.2 Hardware limitations
- 3.4.3 Any other constraint
- 3.5 Software System Attributes
- 3.5.1 Reliability
- 3.5.2 Availability
- 3.5.3 Security
- 3.5.4 Maintainability
- 3.5.5 Portability

4 Formal Analysis Using Alloy

5 Effort Spent

6 References