WEBSITE

mirokuratczyk.github.io

MOBILE

+1 (416) 729-2982

EMAIL

miro.kuratczyk@gmail.com ADDRESS

71 Bernard Ave., Toronto, ON

AMD

Verification Engineer Toronto ON, CANADA May 2012 - August 2013

University of Toronto + Toronto Workers' Health & Safety Legal Clinic

Team Member Capstone Project September 2014 - April 2015

DRUHC

Web Developer Toronto ON, CANADA July 2011 - August 2011

Familiar with

MIRO KURATCZYK

software / hardware / advertising / producing / mixing

Bachelor of Applied Science and Engineering University of Toronto, Department of Electrical and Computer Engineering Degree Completed: December 2015, Convocation: Spring 2016

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EXPERIENCE

- Played a key role in the company rebranding process by independently constructing the tagline that is now used globally by AMD
- Maintained ownership of a testsuite focused on verifying dataflow through the system on multiple large-scale projects
- · Lead a focus group in implementing a testbench overhaul with OVM
- Position required an in-depth understanding of various IPs (including PCle) and their interactions on a system level
- Developed a startup which would provide a service that allows companies to identify and monitor any hazardous airborne chemicals employees are being exposed to both qualitatively and quantitatively
- The service pairs M908 handheld mass spectrometers with a centralized database where all detection data is regularly uploaded for future analysis
- After identifying a market for this service we flushed out a business plan for a startup which explores provincial grants and other avenues of accruing startup capital
- Project leader for the complete redesign of company website
- Lead a focus group in conceptualizing and constructing the new website
- Developed an application to automate the transfer of content to the new design
- Scheduled weekly meetings with executive director to track project progress
- Computer Security (ECE568) + NIST Cybersecurity Framework + SSL + OAUTH
- Compilers and Interpreters (CSC467) + Flex + Bison
- Computer Architecture (ECE552) + Inst./Data/Thread-level parallelism + Cache + VM
- Distributed Systems (ECE419) + MapReduce

TOOLS

GIT, AWS GDB, VERDI DOCKER, VALGRIND

PROGRAMMING LANGUAGES

C (+assembly) VERILOG VERILOG

SQL (+alchemy) HASKELL

PYTHON (+flask) JAVA

INTERESTS

- Music (self-taught mixer, producer and artist)
- UofT Documentary and Discussion Society Administrative Director (2013 2015)
- SkateLife Canada Skateboard Club (2003 Present)