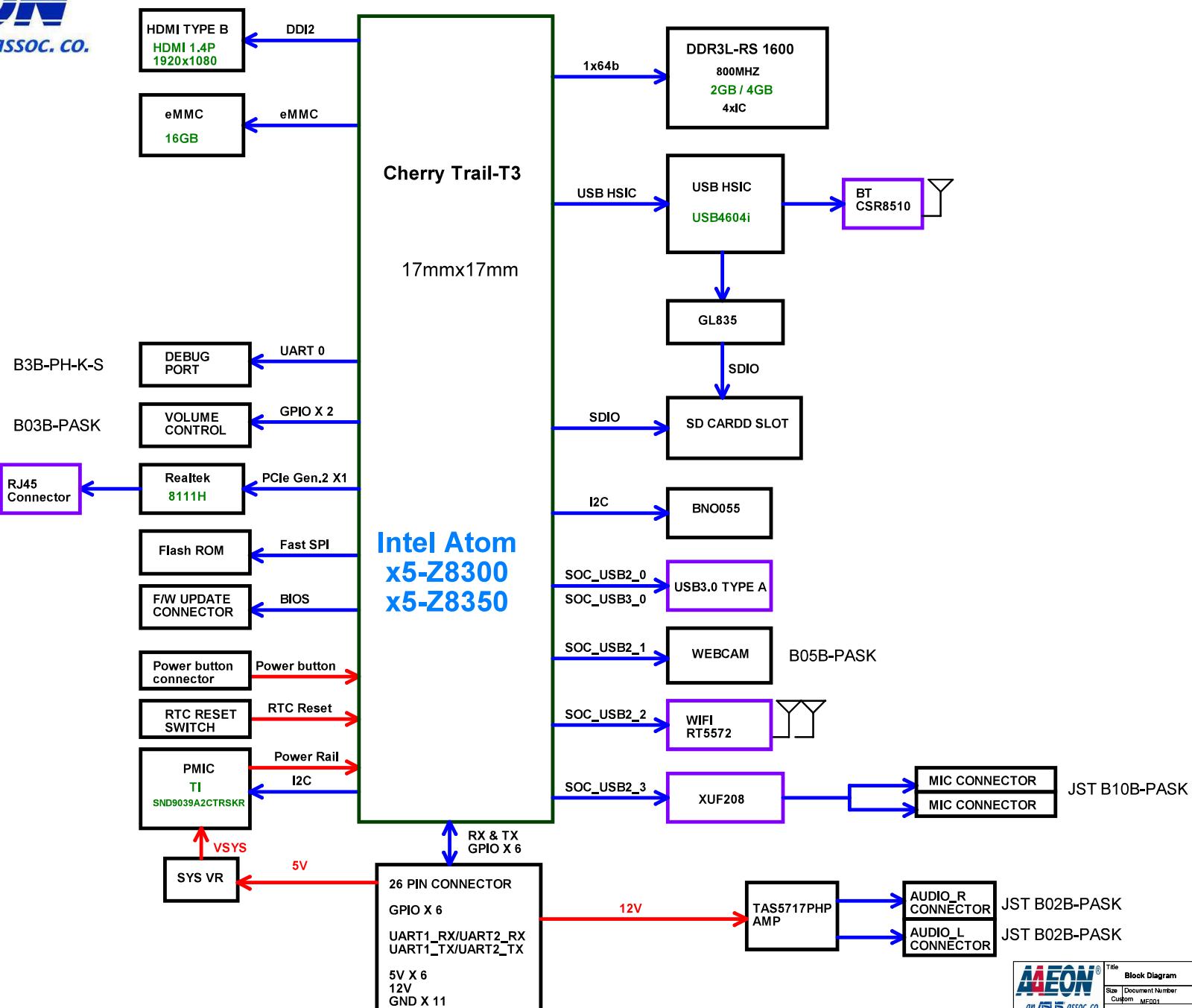


01	INDEX	21	LEVEL SHIFT CIRCUITRY	41	HISTORY	
02	BLOCK DIAGRAM	22	SPI FLASH ROM			
03	POWER TREE	23	EMMC			d
04	POWER SEQUENCE	24	BNO055			
05	SOC MEMORY	25	WIFI_RT5572N			
06	SOC POWER1	26	BT_CSR CRS8510			
07	SOC DISPLAY/CAMERA	27	USB3.0_TYPE-A			
08	SOC PCIE/SDMMC/I2S AUDIO	28	USB HSIC			
09	SOC USB/HSIC/I2C/ISH	29	RTL8111G-CG			c
10	SOC CLK/SPI/GPIO/HSUART	30	EDP TO HDMI			
11	SOC GND	31	AUDIO1			
12	SOC POWER2	32	AUDIO2			
13	SOC DECOUPLING CAPS	33	FUNCTION CONN			
14	SOC POWER TRIBUTARIES	34	3V VOLTAGE REGULATOR			
15	SD CARD	35	PMIC 1of3/CONTROL			b
16	HARDWARE STRAP	36	PMIC 2of3/SWITCHING POWER			
17	POWER & RESET PIN HEADER	37	PMIC 1of3/CLDO			
18	MEMORY 1/2	38	VTT VOLTAGE REGULATOR			
19	MEMORY 3/4	39	DC IN 26 PIN CONNECTOR			
20	DDR3L-RS VREF&Termination	40	MECHANICAL COMPONENT			

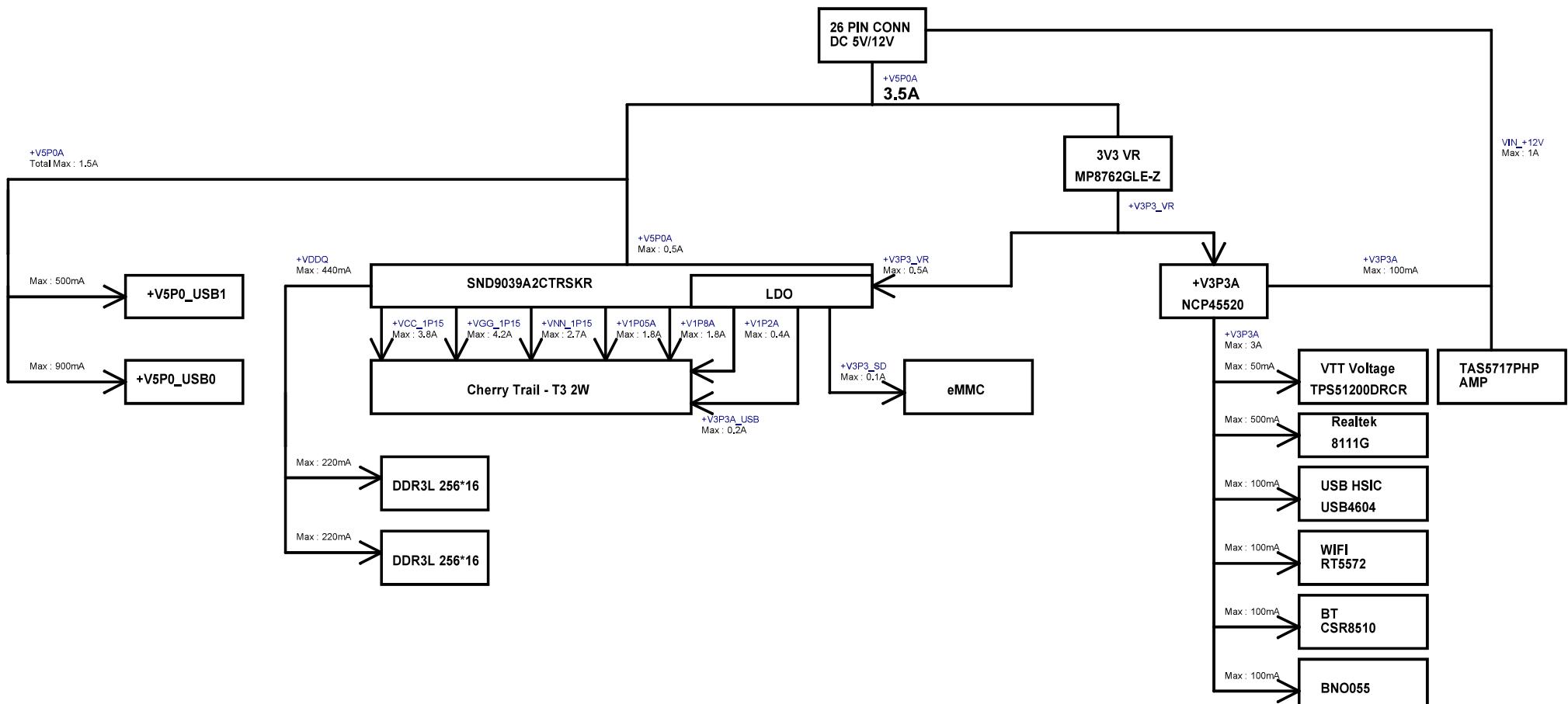


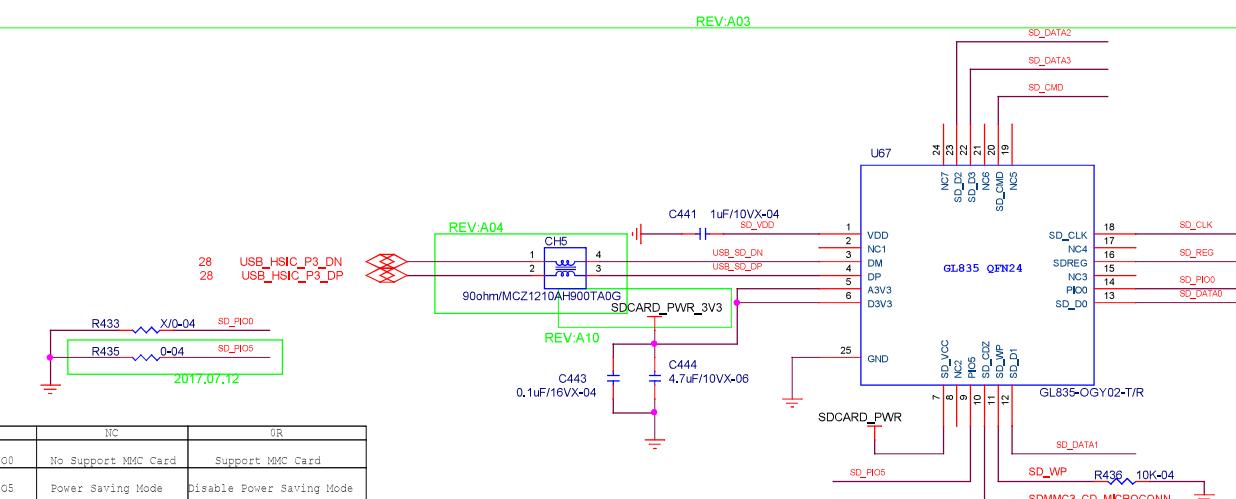
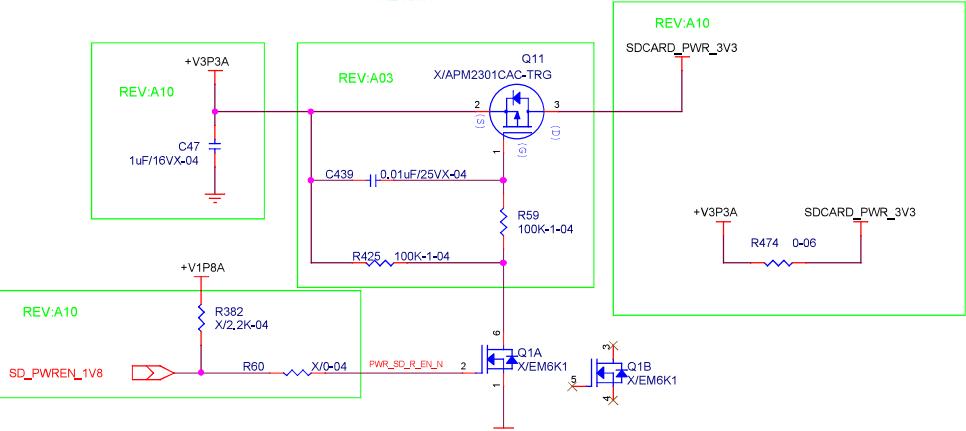
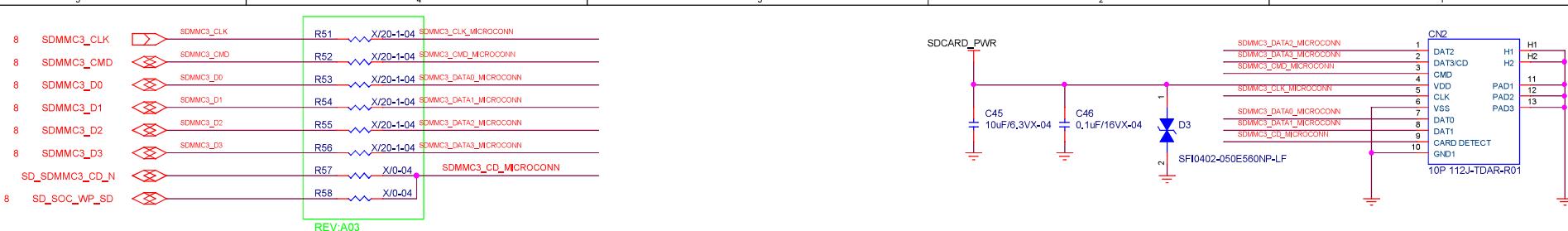
Title Index		
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## Block Diagram

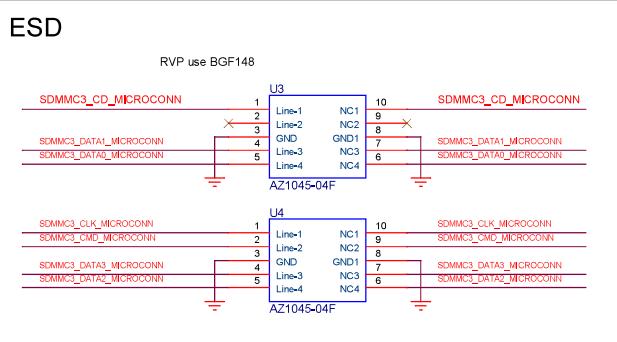


## Power Tree

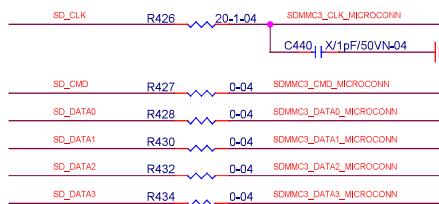


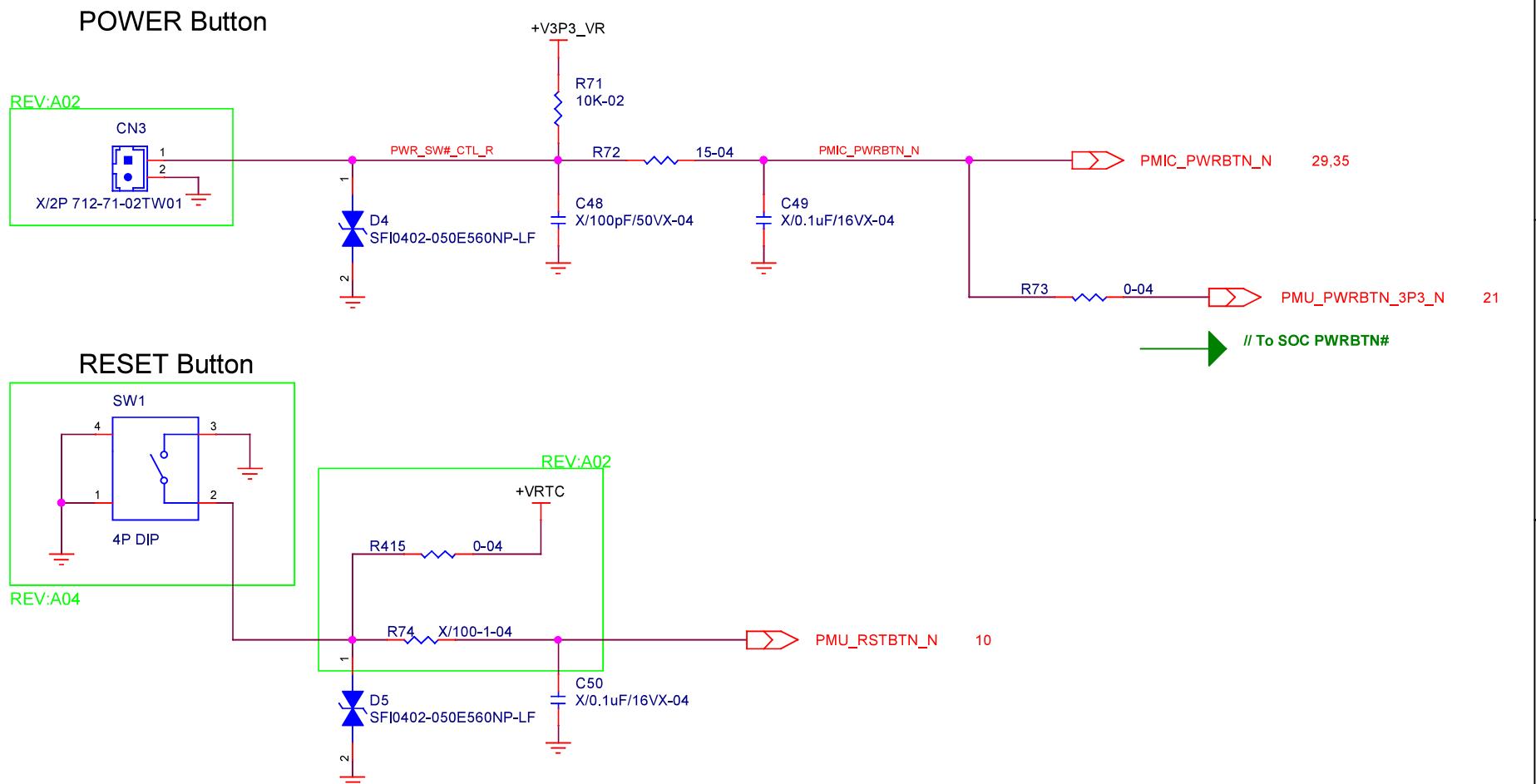


	NC	OR
SD_PIO0	No Support MMC Card	Support MMC Card
SD_PIO5	Power Saving Mode	Disable Power Saving Mode



Reserved R and C for EMI  
The RC value need to fine tune for different PCB layout  
Close to GL835

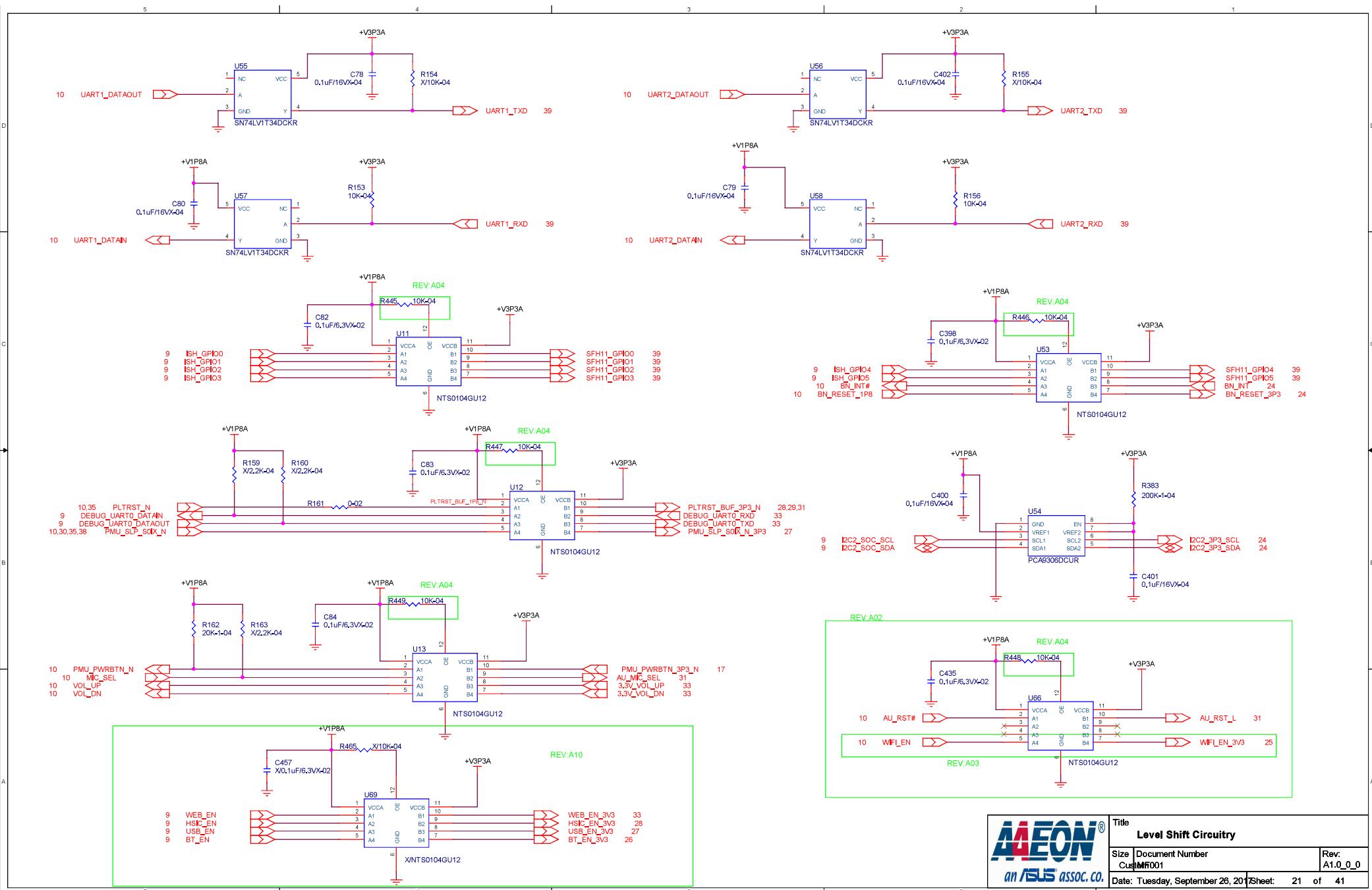


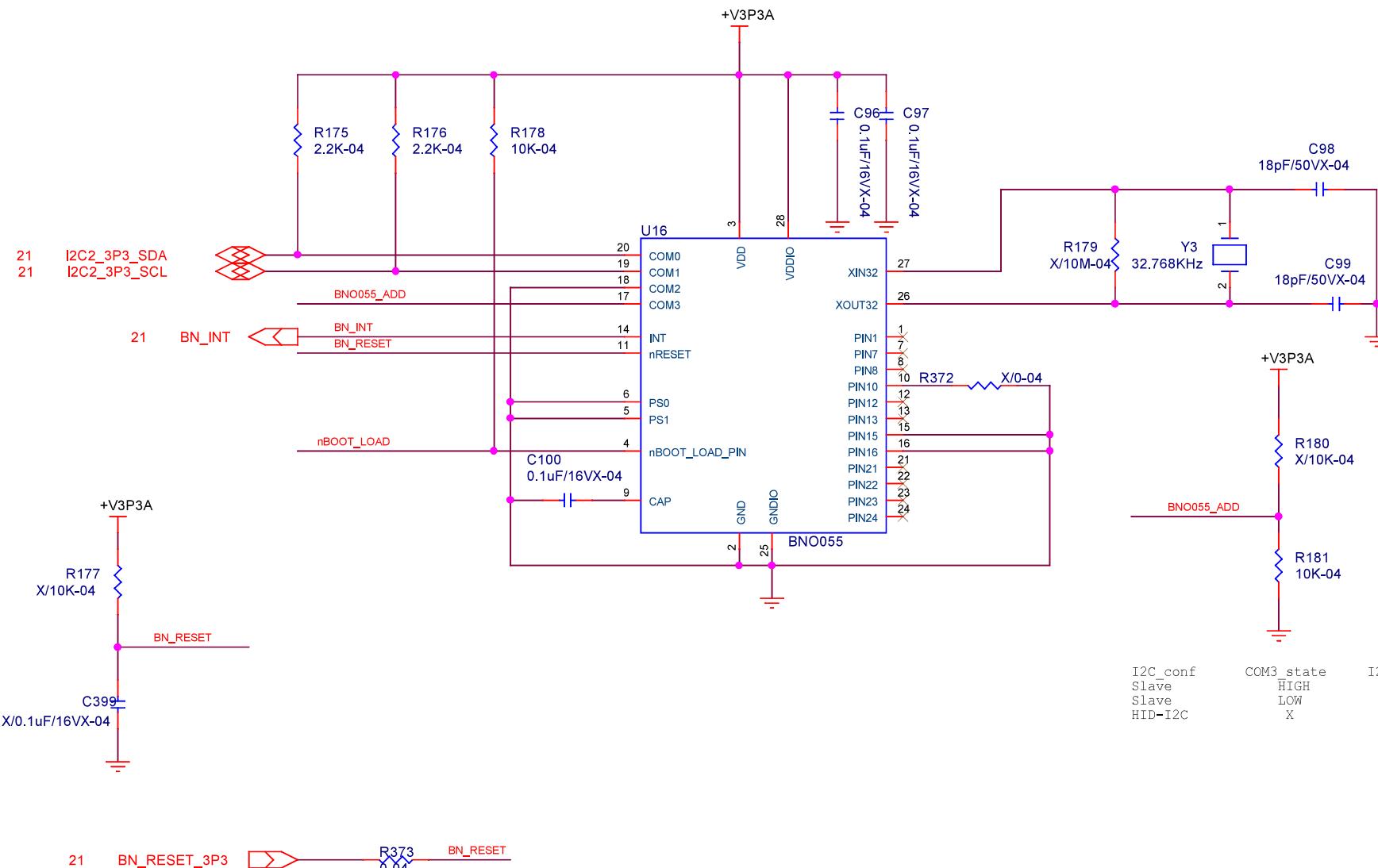


Title: **POWER & RESET PIN HEADER**

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I<sub>2</sub>C\_conf  
Slave

COM3\_state  
HIGH  
LOW  
HID-I<sub>2</sub>C

I<sub>2</sub>C\_address  
0x29  
0x28  
0x40

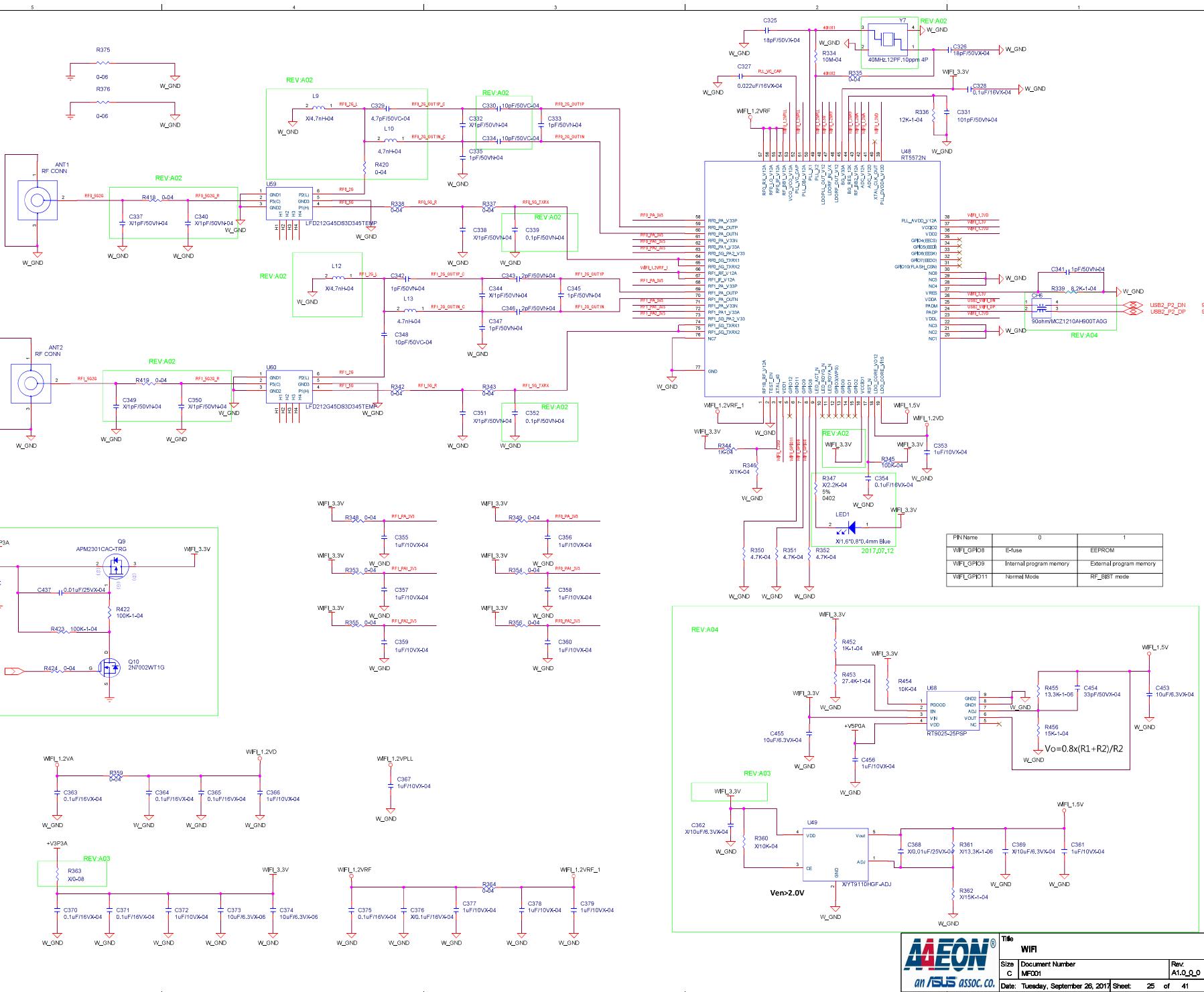


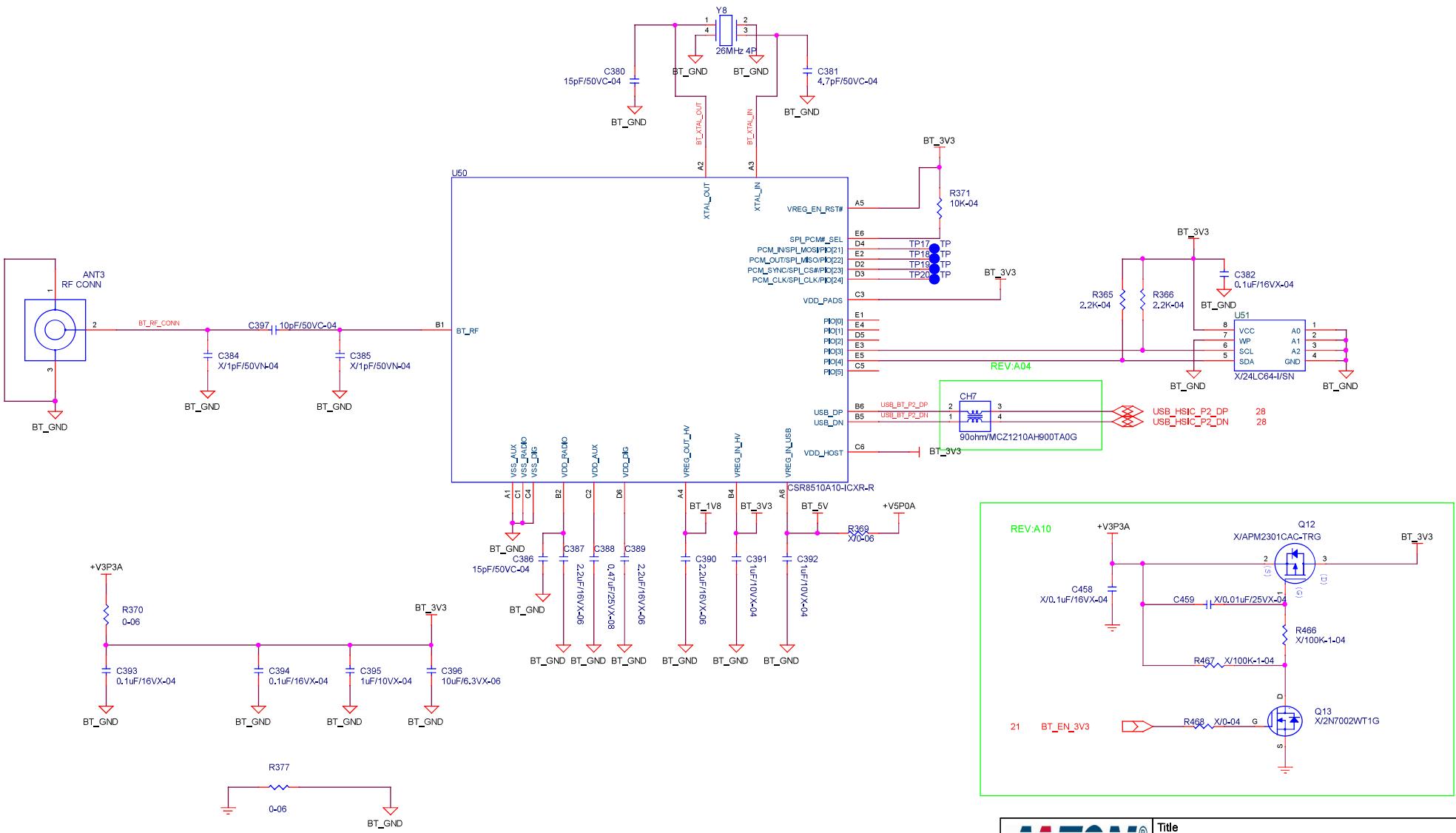
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**BNO055**

Size: Document Number  
A MF001

Rev:  
A1.0\_0\_0

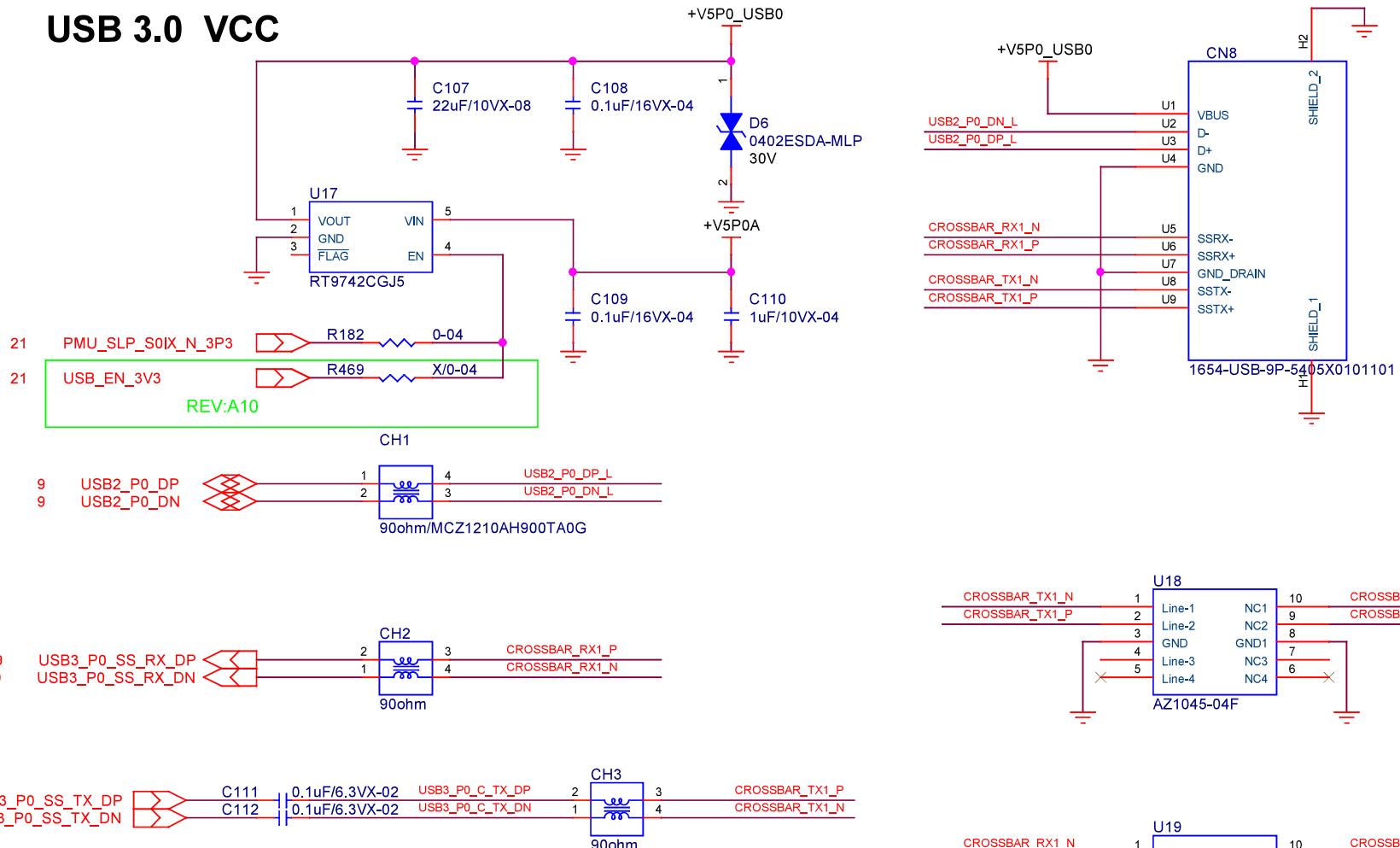
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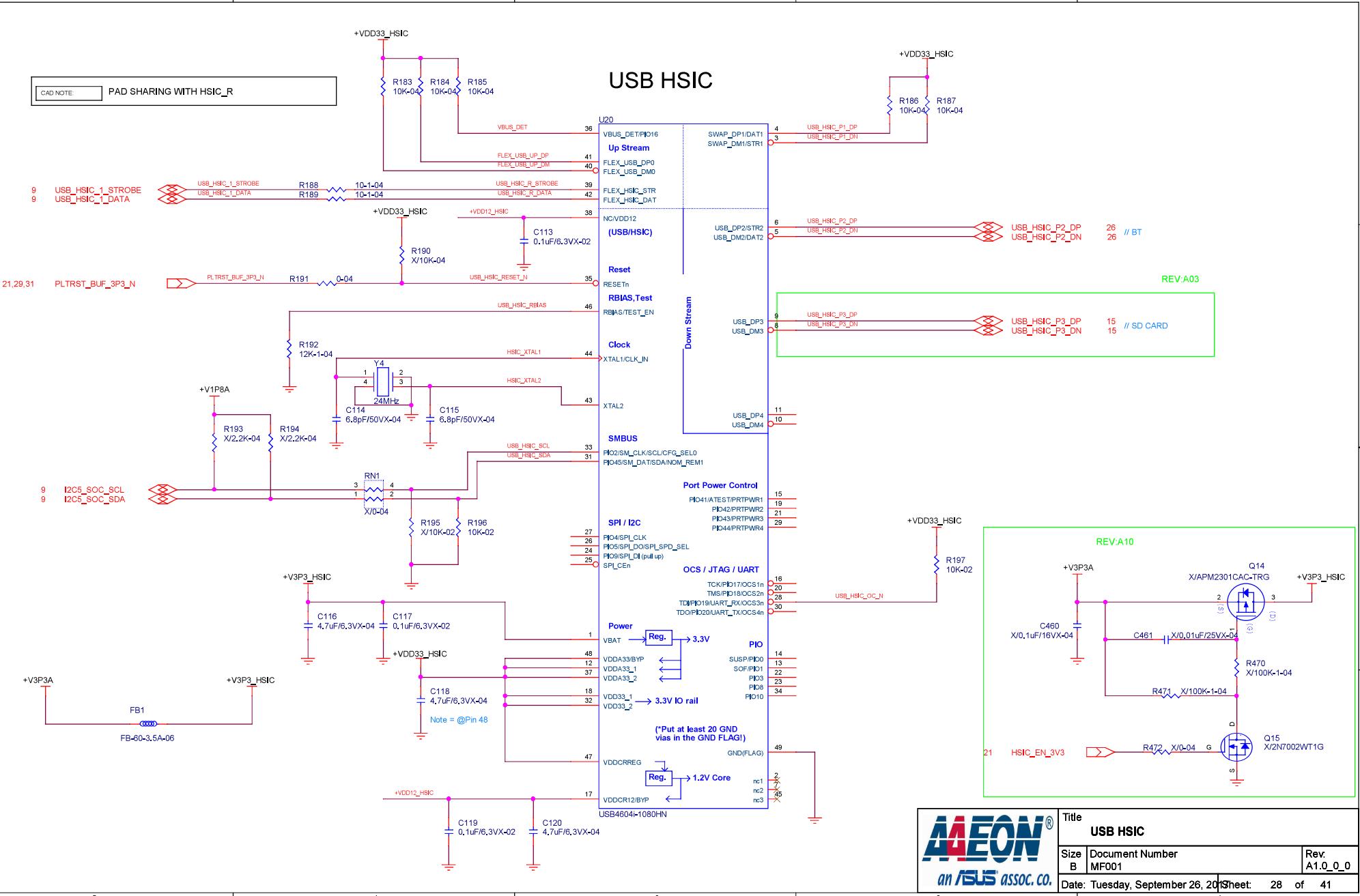
# USB 3.0 VCC



**AEON**  
an **ASUS** assoc. co.

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<b>USB 3.0 Type-A</b>		
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# USB HSIC



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