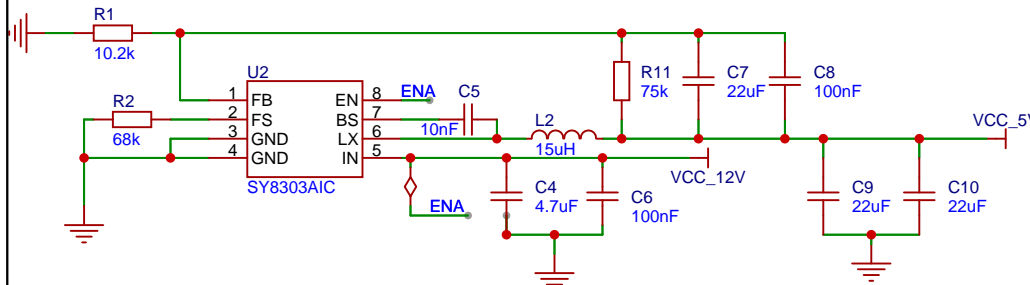
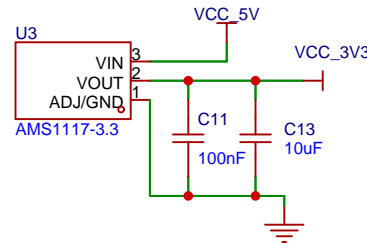


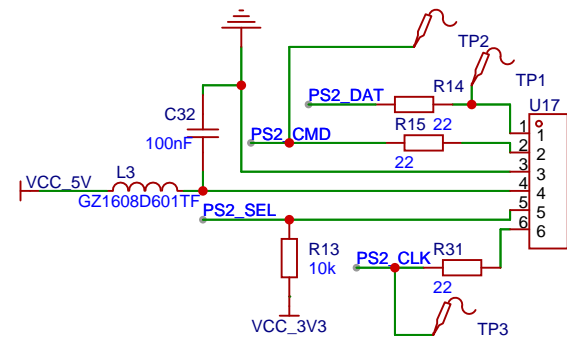
XT30输入



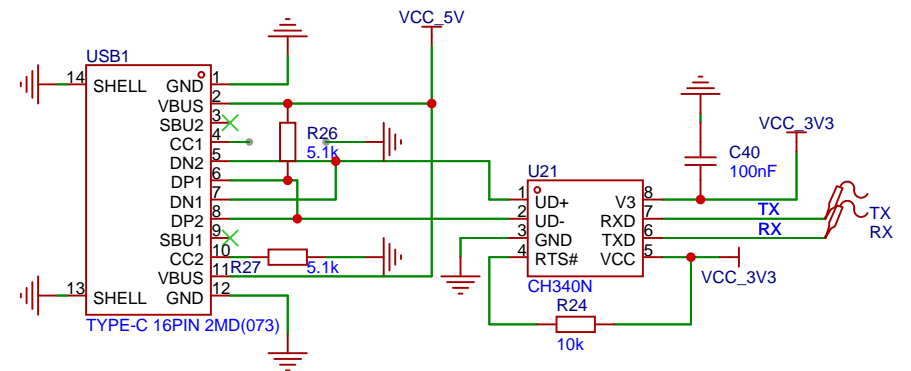
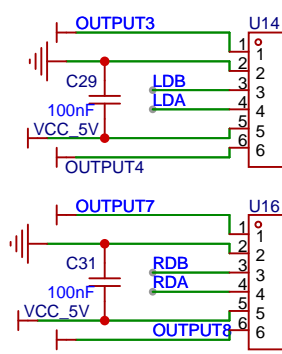
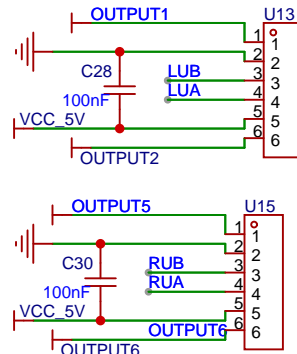
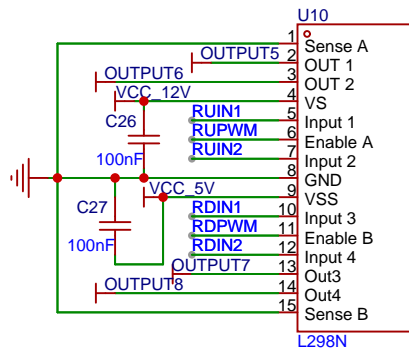
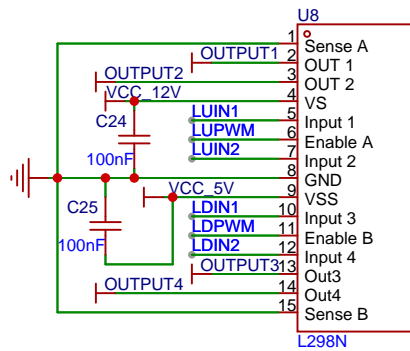
12V转5V输出



5V转3V3



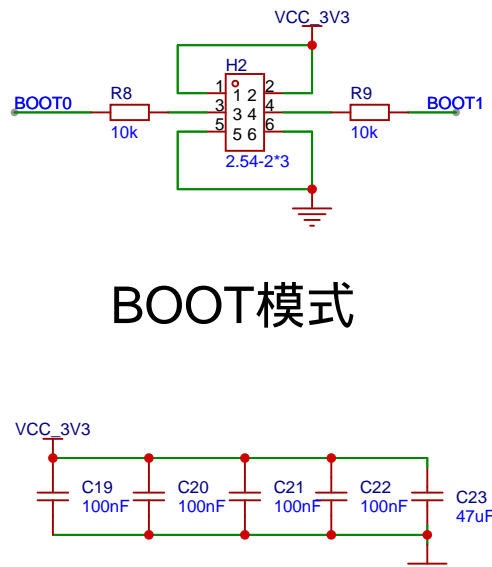
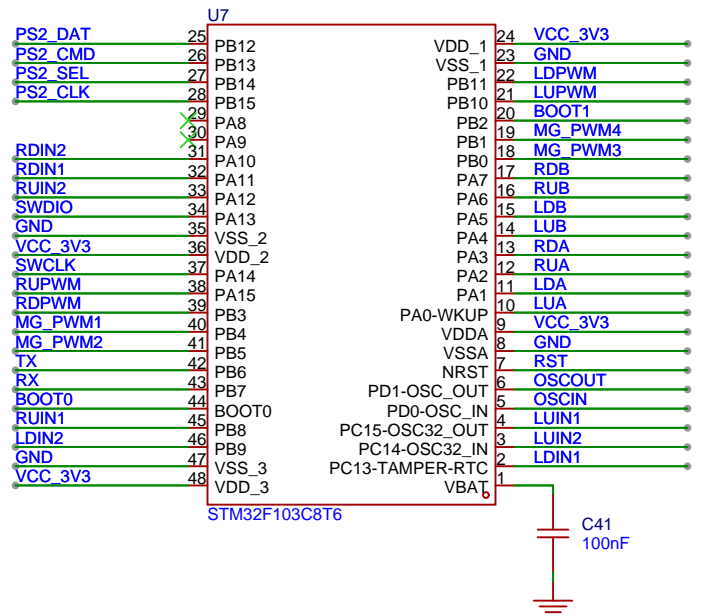
PS2接收器



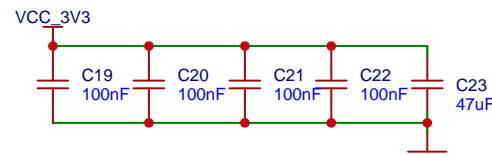
type-c转ttl

电机驱动

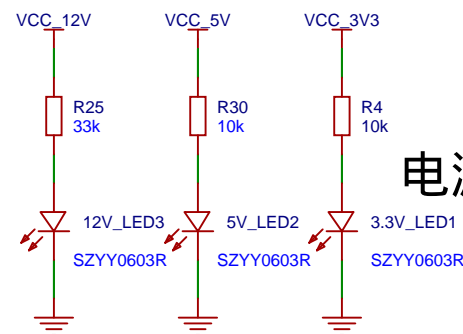
MCU



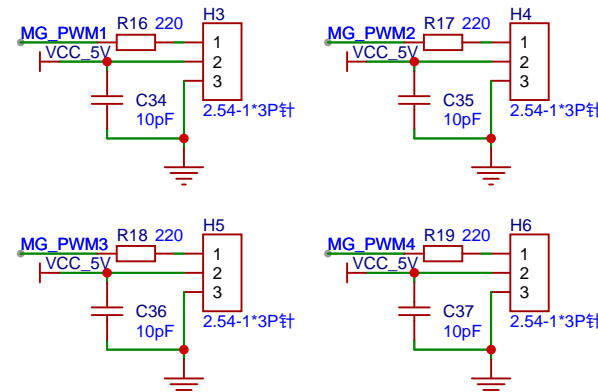
BOOT模式



滤波电路



电源指示灯



舵机

SWD下载电路

晶振电路

复位电路

原理图	Schematic2		创建日期	2025-10-29
板子	Board2		更新日期	2025-11-13
绘制			图页	P1
审阅			校赛123	
		版本	尺寸	页 1 共 1
嘉立创EDA		V1.0	A4	嘉立创EDA