

1

2

3

4

A

A

B

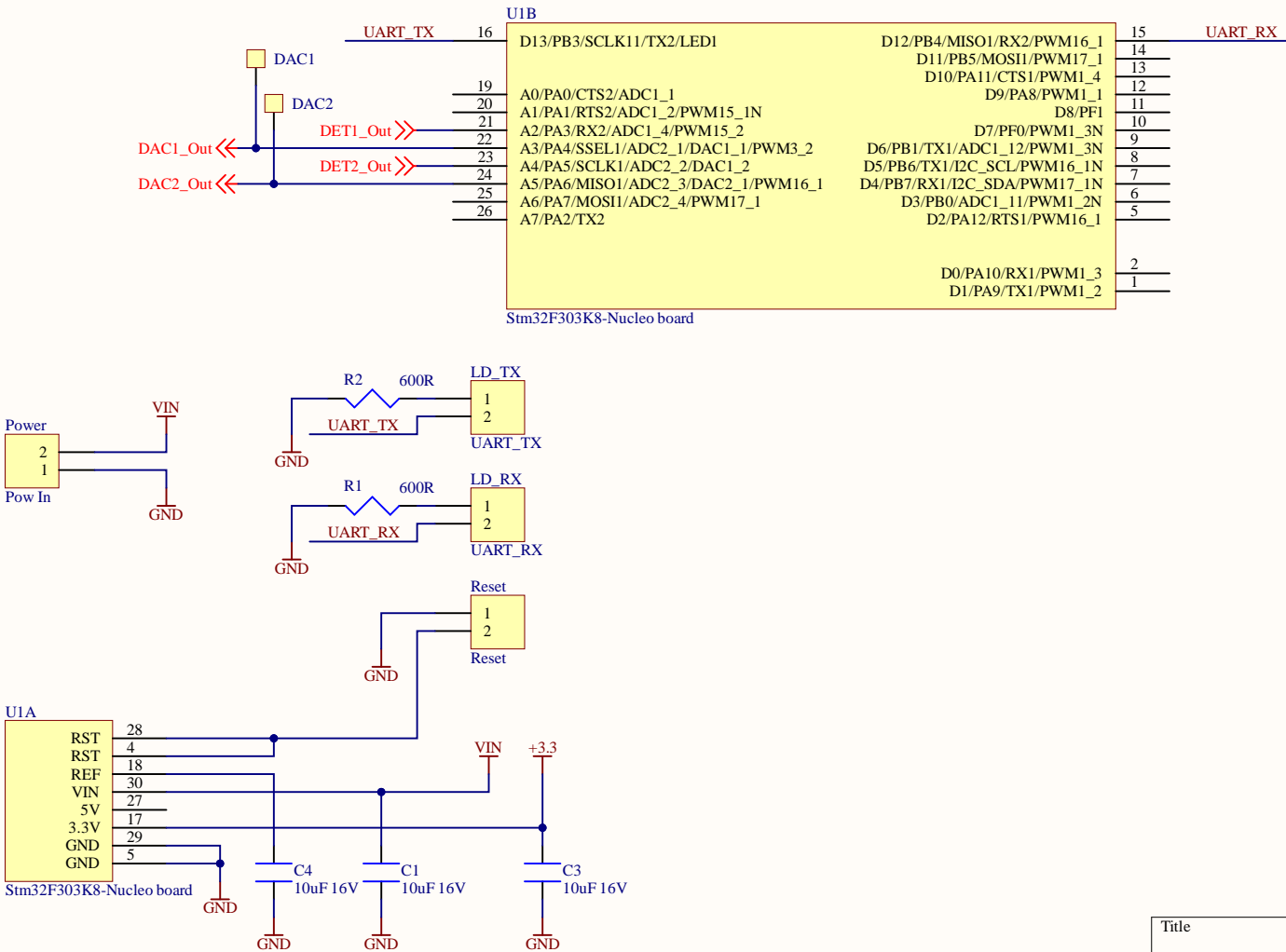
B

C

C

D

D



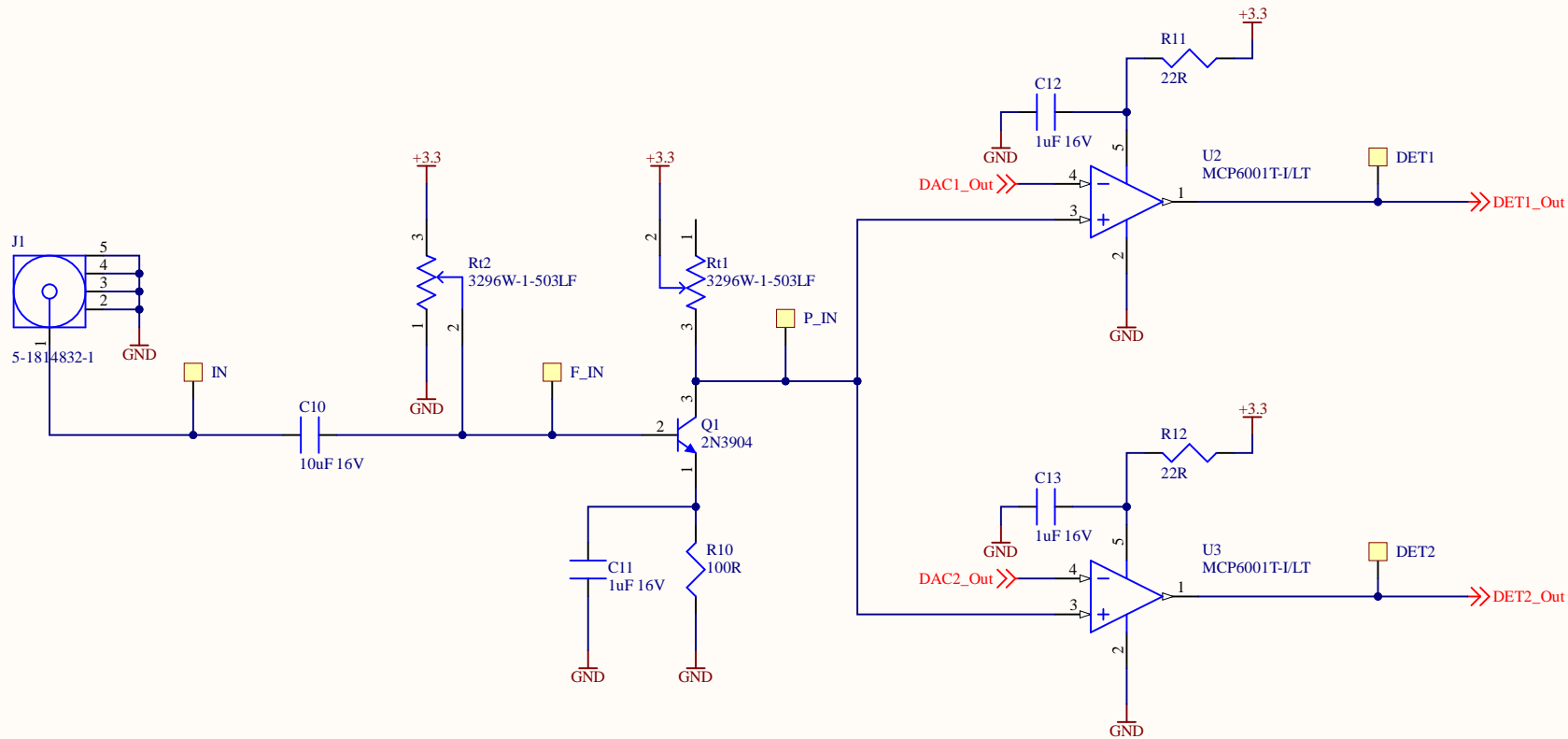
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|-------|-------------------------------|-----------|
| Size | Number | Revision |
| A4 | | |
| Date: | 7.10.2021 | Sheet of |
| File: | D:\Projects\..\Control.SchDoc | Drawn By: |

1

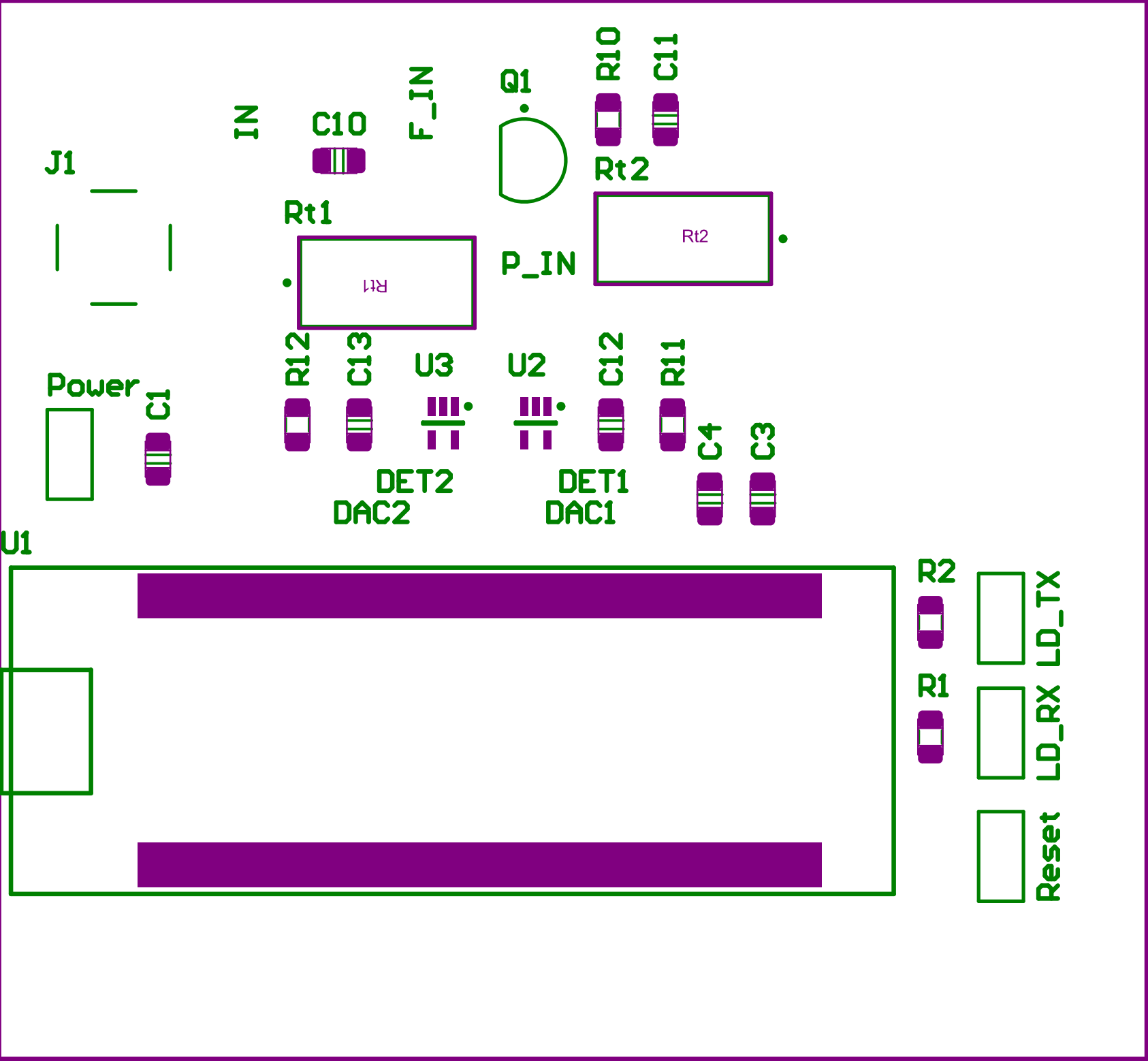
2

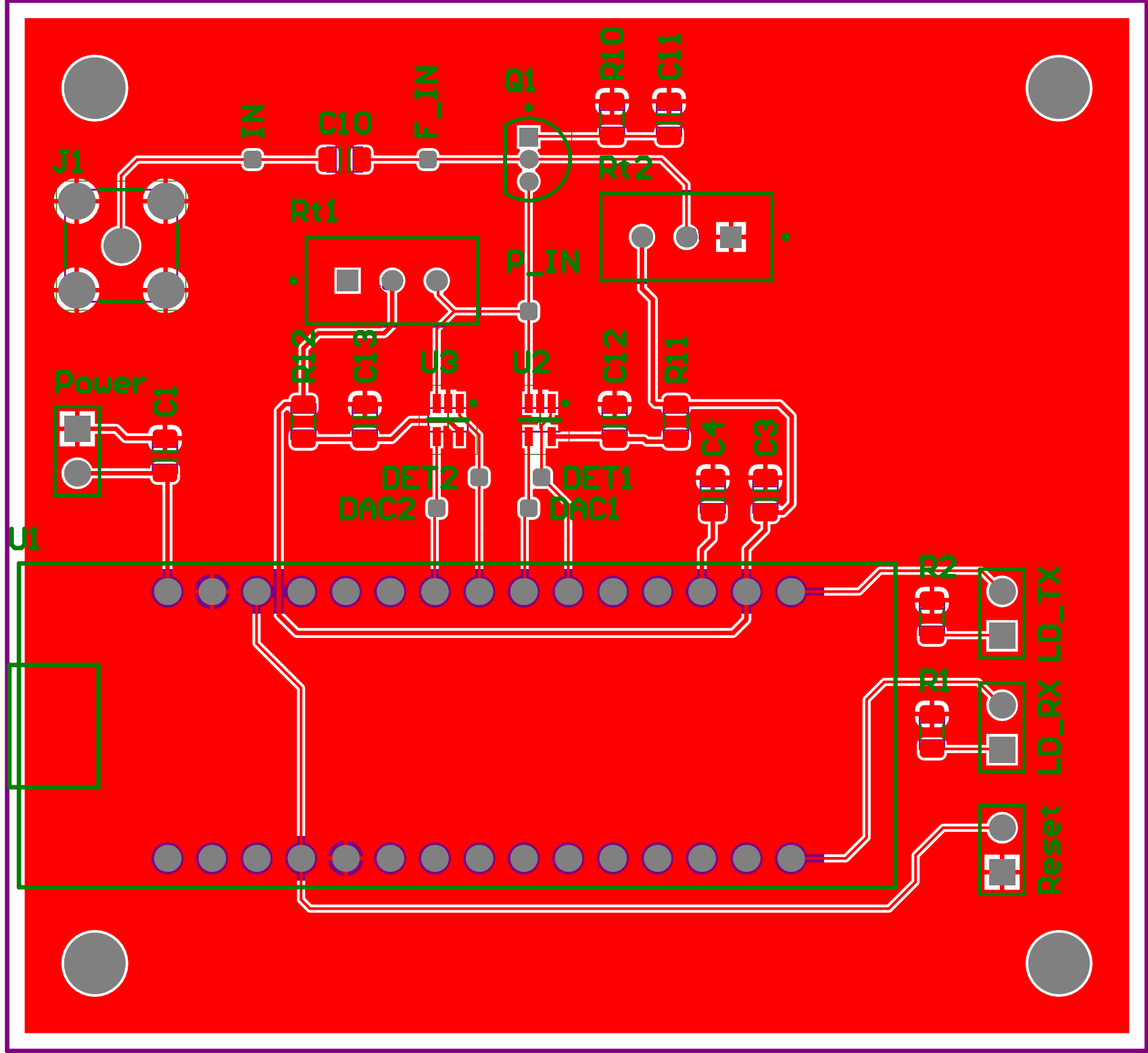
3

4



| Title | | |
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| Size | Number | Revision |
| A4 | | |
| Date: | 7.10.2021 | Sheet of |
| File: | D:\Projects\...\Input.SchDoc | Drawn By: |





| Comment | Description | Designator | Footprint | LibRef | Quantity |
|--------------------------|--|--|--------------------|------------------|----------|
| 10uF 16V | | C1, C3, C4, C10 | 0805_C | Cap | 4 |
| 1uF 16V | | C11, C12, C13 | 0805_C | Cap | 3 |
| Point | | DAC1, DAC2, DET1, DET2, F_IN, IN, P_IN | Test_Point | Point | 7 |
| 5-1814832-1 | SMA Connector, Pitch 5.08 mm, 1 Position, Height 9.5 mm, Tail Length 4 mm, -65 to 165 degC, RoHS, Bulk | J1 | TECO-5-1814832-1_V | CMP-2000-05415-1 | 1 |
| UART_RX | Header, 2-Pin | LD_RX | HDR1X2 | Header 2 | 1 |
| UART_TX | Header, 2-Pin | LD_TX | HDR1X2 | Header 2 | 1 |
| Pow In | Header, 2-Pin | Power | HDR1X2 | Header 2 | 1 |
| 2N3904 | General Purpose Transistor, NPN Silicon, 200 mA, 60 V, 55 to 150 degC, 3-Pin TO-92, RoHS, Bulk | Q1 | ONSC-TO-92-3-29-11 | CMP-2000-05562-1 | 1 |
| 600R | | R1, R2 | 0805_R | Res | 2 |
| 100R | | R10 | 0805_R | Res | 1 |
| 22R | | R11, R12 | 0805_R | Res | 2 |
| Reset | Header, 2-Pin | Reset | HDR1X2 | Header 2 | 1 |
| 3296W-1-503LF | TRIMMER 50K OHM 0.5W PC PIN TOP | Rt1, Rt2 | FP-3296W-MFG | CMP-2000-05583-2 | 2 |
| Stm32F303K8-Nucleo board | | U1 | F303-Nucleo | F303-Nucleo | 1 |
| MCP6001T-I/LT | 1 MHz, Low-Power Operational Amplifier, 5-Pin SC-70, Industrial Temperature, Tape and Reel | U2, U3 | SC70-LT5_M | CMP-0187-00457-2 | 2 |