

ELEC 302 Lab 1 Prelab

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Question 1: Read the description of Tasks 1-3 assigned in these sheets.

Done

Question 2: Review the lecture notes and the sections in Chapter 3 of the text for the following circuits: half wave rectifier, full wave rectifier using a center tap transformer, and full wave rectifier using a bridge circuit. Find the formula for each circuit to calculate the corresponding filter capacitor. Also review relevant supplementary materials referred to in class.

These formulae can be found in the lecture 5 notes. Assuming nonideal diodes, they are:

- Half wave rectifier: $C = \frac{V_p - V_d}{f R_L V_r}$
- Center tap full wave rectifier: $C = \frac{V_p - V_d}{2f R_L V_r}$
- Bridge full wave rectifier: $C = \frac{V_p - 2V_d}{2f R_L V_r}$

Question 3: Find the data sheets for the rectifier diode and the zener diode listed above.

These can found at the following urls:

- <https://www.vishay.com/docs/88503/1n4001.pdf>
- <https://media.digikey.com/pdf/Data%20Sheets/EIC%20PDF's/1N5338B-88B.pdf>

Question 4: For Task 3, draw the circuit diagram of a 5.1-V Zener voltage regulator. Calculate the resistance coupled in series with the Zener diode so to have a standby (no load) current of 66.7 mA. Assume the input voltage to the regulator is around 15 V.