

ELEC 302 Lab 1 Report

Power Supplies and Voltage Regulators

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1 Task 1

Part 1.

For part 1 we simply measured the waveform coming out of a transformer. The setup can be seen in the left half of figure 1, before the diode and resistor were added. The resulting waveform can be seen in figure 2. From this we see the RMS voltage was 12V.

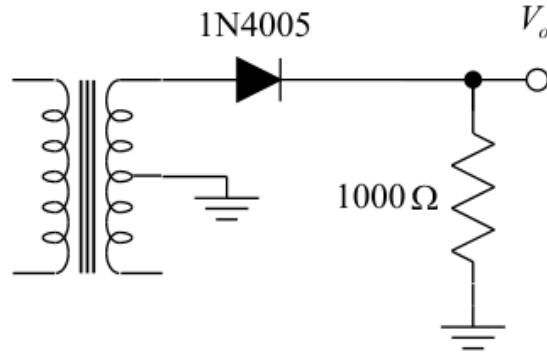


Figure 1: Setup for task 1. The first part of task 1 occurred before the diode and resistor were added, and for the second part additional capacitors were added between V_0 and ground. Diagram taken from the lab manual.

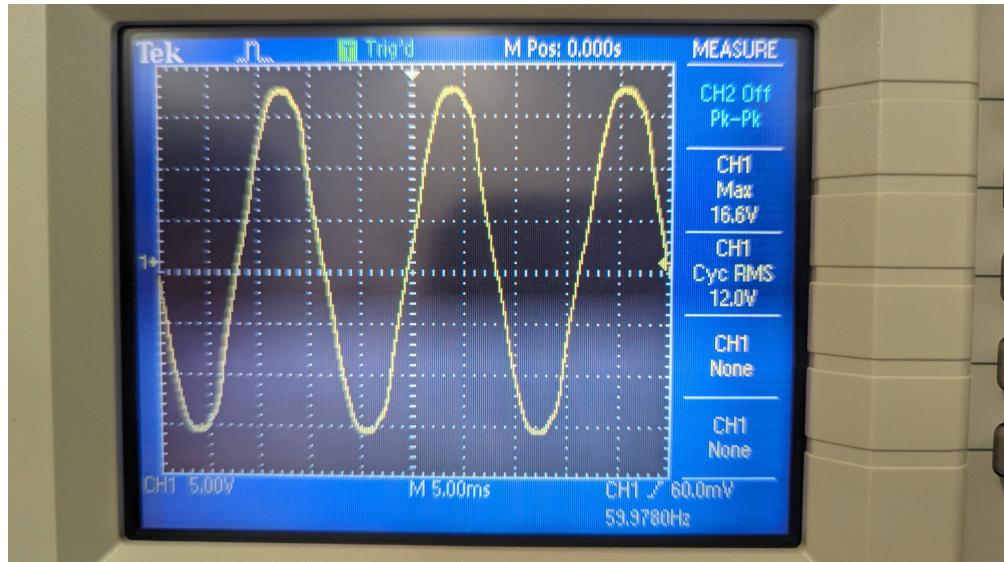


Figure 2: Task 1, part 1 waveform.

Part 2. In this section, we added the diode and resistor and measured the output voltage. The resulting waveform can be seen in figure 3. Before adding the capacitors, we see the peak voltage is 16V. This is expected, since for a half wave rectifier the peak voltage is $V_p - V_d = 16.6 - 0.6 \approx 16$.

For the capacitor, we can use the formula given in class to calculate the value:

$$C = \frac{V_p - V_d}{f R_L V_r} = \frac{16.8 - 0.7}{60 \cdot 1000 \cdot 1} = 268.3\mu\text{F}.$$

Since we don't have any capacitor values close to that, we instead used one $220\mu\text{F}$ and two $33\mu\text{F}$ capacitors in parallel which has an equivalent capacitance of $286\mu\text{F}$. The resulting waveform can be seen in figure 3, the average DC output voltage is 15.3V , and as expected the ripple voltage is 920mV which is just under the required 1V .



Figure 3: Task 1, part 2 waveforms. The left is the V_0 without the capacitors, while the right is V_0 with capacitors, zoomed in to see the ripple. The ripple (i.e. Pk-Pk) can be seen to be 920mV , less than the 1V as required.

2 Task 2

Part 1. The setup can be seen in figure 4.

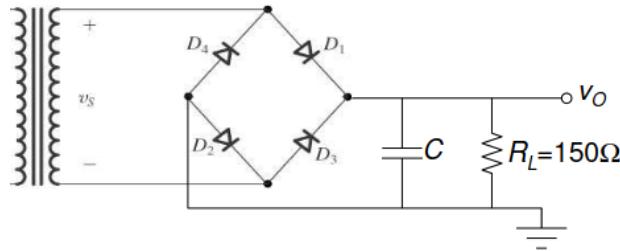


Figure 4: Setup for task 2, part 1. Note that in our case $R_L = 1\text{k}\Omega$, this diagram was taken from the lecture slides.

For the capacitor using the equation:

$$C = \frac{V_p - 2V_d}{2f R_L V_r} = \frac{33.2 - 1.4}{2 \cdot 60 \cdot 1000 \cdot 1} = 265\mu\text{F}.$$

Again we used the same capacitor configuration as before of $286\mu\text{F}$ since we didn't have a single capacitor of the correct value. The resulting waveforms can be seen in figure 5. The peak voltage before adding the capacitors was 31.2V , while after adding them the average voltage was 30.6V . This makes sense, as after adding the capacitor we'd expect the mean to be $V_p - \frac{V_r}{2} \approx 31.2 - 0.5 \approx 31.6$. The ripple was 832mV as expected.

Comparing with the previous part, the ripple was the same. Given the capacitors were specifically chosen to achieve the same target ripple though, this isn't too surprising. The output voltage for the bridge rectifier is twice as high for the same capacitors to achieve the same ripple though, which could make it a better fit for many applications.

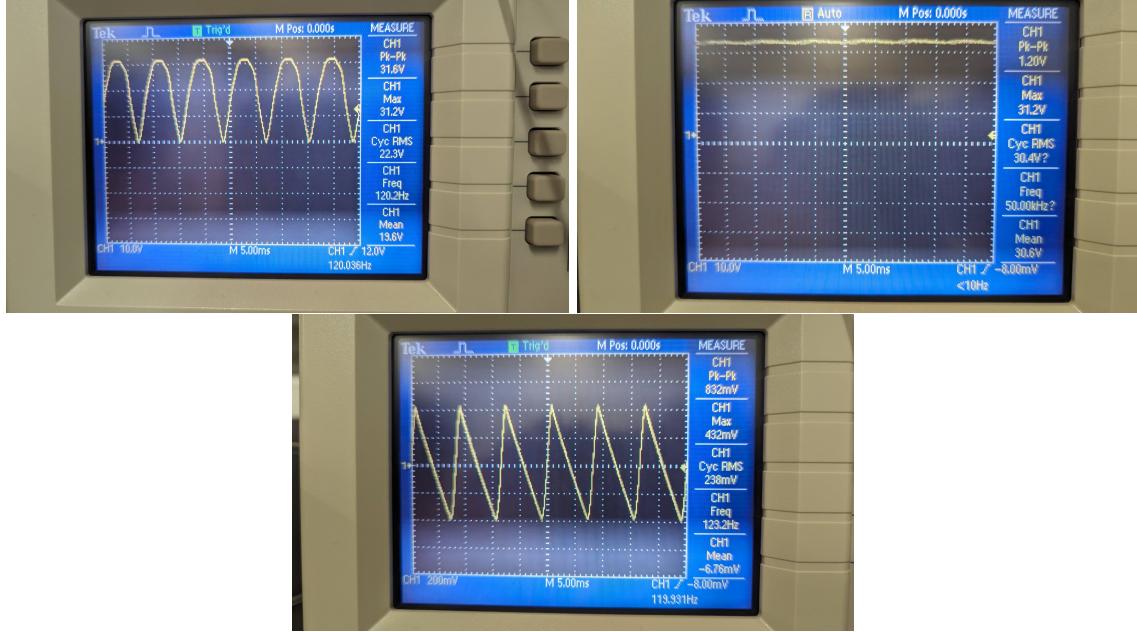


Figure 5: Task 2, part 1 waveforms. Left top is V_0 with the capacitors, while the right top is V_0 without capacitors. Bottom is the AC-coupled ripple zoomed in, again the ripple (i.e. Pk-Pk) of 832mV is less than 1V.

Part 2. The procedure and setup was exactly the same as for part 1 of this task, see figure 6 for the setup.

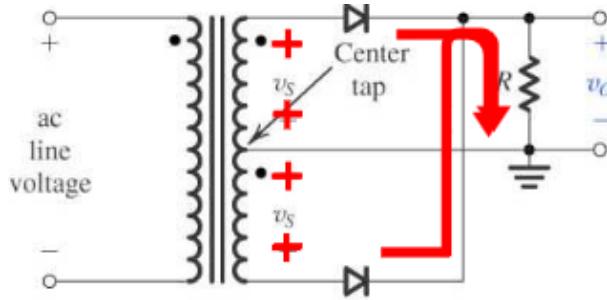


Figure 6: Setup for part 2 of task 2. This diagram was taken from the lecture slides.

Again, the capacitor comes from the equation:

$$C = \frac{V_p - V_d}{2fR_LV_r} = \frac{16.8 - 0.7}{2 \cdot 60 \cdot 1000 \cdot 1} = 134\mu F.$$

Three $33\mu F$ capacitors were used in parallel to simulate this. The waveforms are in figure 7. The peak voltage before the capacitors was 16.4V. After adding them, the average voltage of the

smoothed wave was 15.9V. This is consistent with the expectation that the average voltage should be $V_p - \frac{V_r}{2} = 16.4 - 0.5 = 15.9$ V. Finally the ripple was 944mV.

In terms of comparison, this setup is very similar to task 1 with the half wave rectifier, with the only difference being that, as the name implies, it rectifies the entire wave rather than every second peak.

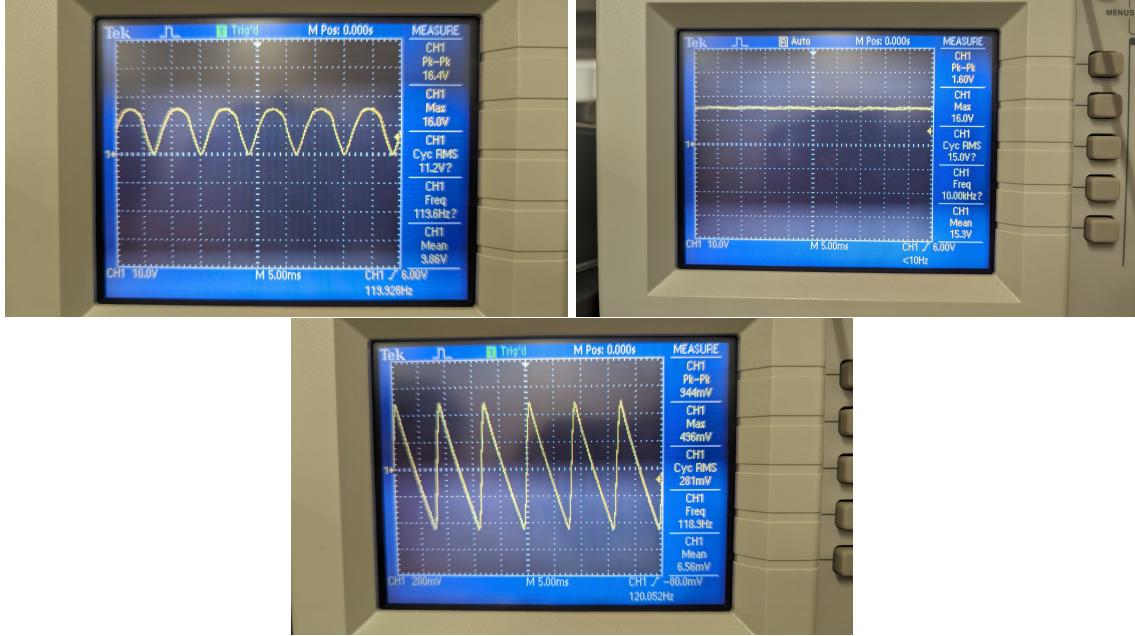


Figure 7: Task 2, part 2 waveforms. Left top is V_0 with the capacitors, while the right top is V_0 without capacitors. Bottom is the AC-coupled ripple zoomed in, again the ripple (i.e. Pk-Pk) of 944mV is less than 1V.

Question 3. The diagram of the setup can be seen in figure 8. After setting up this circuit, we first measured the DC voltage by getting the outputted waveform. This was found to be 4.81. Next we found the output ripple voltage by using AC coupling, which was found to be 118mV. The waveforms used can be seen in figure 9

Obviously the ripple is an order of magnitude smaller than what was seen in tasks 1 or 2, which is a major difference. However, doing the math using the formula found in the lecture 4 slides page 5, one would actually expect a much smaller ripple:

$$\left. \frac{\Delta V_0}{\Delta V_{in}} \right|_{I=\text{constant}} = \frac{r_z}{R_L + r_z} = \frac{1.5}{1000 + 1.5} = 1.47 \text{mV/V.}$$

There are a few possible reasons for the discrepancy. It could be a faulty Zender diode, although I consider this fairly unlikely. We also could have measured the wrong line for this measurement, but there are only a few possible voltages to measure in a circuit this simple and none of them have a value close to 100mV. Another possibility is that either a resistor or capacitor was mixed up and mistakenly used despite us attempting to double check using the DMM beforehand, which would definitely affect the reading. Finally, the datasheet for the zener diode isn't entirely clear on what exactly its zener resistance is, so there's a possibility that it was misinterpreted here which would make the above calculation irrelevant. Regardless of the exact reason, the difference wasn't noticed until after the conclusion of the lab so a remeasurement wasn't possible.

This task showcases the zener diodes capabilities as a fantastic filter for a voltage regulator. By adding it, we reduced the ripple voltage by an order of magnitude. This is very useful for DC applications where a smooth voltage is required.

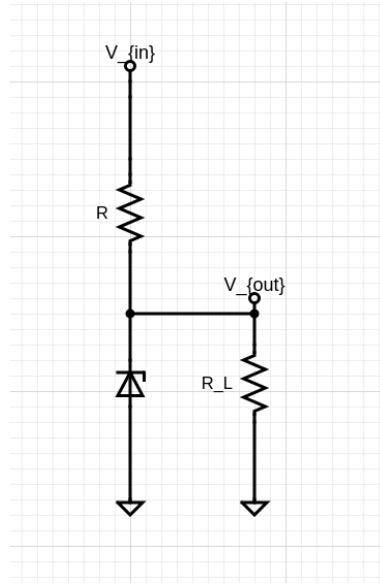


Figure 8: Circuit diagram for part 3 (same as prelab), with V_{in} being attached to V_0 in figure 7.



Figure 9: Task 3 waveforms. Left is the DC waveform, while right is the AC coupled component. See the main text for more detailed analysis.