



深圳市拓淘电子科技有限公司
Shenzhen Toppop Electronic Co.,Ltd

LCM APPROVAL SHEET

模组承认书

版本(Version): V0

Project No. 项目型号	TT178ERC10B		
Customer 客户名称			
Module No. 客户型号			
Product type 产品类型	Type : AMOLED Display		
	Resolution : 368x448 Dots		
	Screen Size : 1.78 inch		
Signature by customer: 客户确认签字盖章:			
Structure size: 结构尺寸:	<input type="checkbox"/> OK _____	备注:	
	<input type="checkbox"/> NG _____		
Electric property: 电气性能:	<input type="checkbox"/> OK _____	备注:	
	<input type="checkbox"/> NG _____		
Company 公司	Designed by 设计	Checked by 审核	Approved by 批准
Signature 签名			
Rev. 版本	Date 日期	Description 变更内容	
V0	2019-1-9	Preliminary Specification Release	



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1. Scope

This Specification defines AMOLED manufactured by EverDisplay Optonics(Shanghai) Limited, from here on refer as EDO. In the case of any unspecified item, it may require both EDO and the party designs this module into its product to work out a solution.

2. Features

2.1 Product Applications Smart Watch

2.2 Product Features

- 1)Display color: 16.7M (RGB x 8bits)
- 2)Display format: 1.78"(368RGBx448)
- 3)Pixel arrangement: Real RGB arrangement
- 4)Interface: MIPI/SPI

3. Mechanical Specifications

Item	Specification	unit
LTPS Glass outline	30.6*37.99	mm
Encapsulation Glass outline	30.6*37.24	mm
Number of dots	368(W) x RGB x 448(H)	dots
Active area	28.7*34.94	mm
Diagonal size	1.78	inch
Pixel pitch	78*78	μm
Glass thickness(LTPS/Encap. Glass)	0.2 / 0.3	mm
Weight	2.10±10%	g

4. Maximum Rating

Parameter	Symbol	Spec			Unit	Note
		Min.	Typ.	Max.		
Analog/boost power voltage	VCI	-0.3	-	5.5	V	-
I/O voltage	VDDIO	-0.3	-	5.5	V	-
Operating temperature	Top	-20	-	70	°C	-
Storage temperature	Tstg	-40	-	80	°C	-



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5. Electrical Specifications

5.1 Electrical Characteristics

5.1.1 Power Characteristic:

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Battery power Voltage	Vbat	2.9	3.7	4.8	V	-
Digital Power supply	VDDIO	1.65	1.8	1.95	V	Ref

1)Normal Mode

Power Supply: VDDIO=1.8V Vbat=3.7V

Frame Frequency: Fframe =60HZ @ 25degC, Brightness 350 nits, Command Mode,

Display Condition	Symbol	Min.	Typ.	Max.	Unit	Remark
100% Pixel On,350nits	Normal mode	-	186	210	mW	Ref

2)Idle Mode

Power Supply: VDDIO=1.8V Vbat=3.7V

Frame Frequency: Fframe =15HZ @ 25degC, Brightness 30 nits,

Display Condition	Symbol	Min.	Typ.	Max.	Unit	Remark
10% Pixel On,30nits	Idle mode	-	12	16	mW	Ref

3)Deep Standby Mode

Display Condition	Symbol	Min.	Typ.	Max.	Unit	Remark
All Pixel Off,0nits/ Vci off/Vddio on	Standby mode	-	-	50	μW	-

5.1.2 Driver IC

RM69090 (refer to the datasheet).



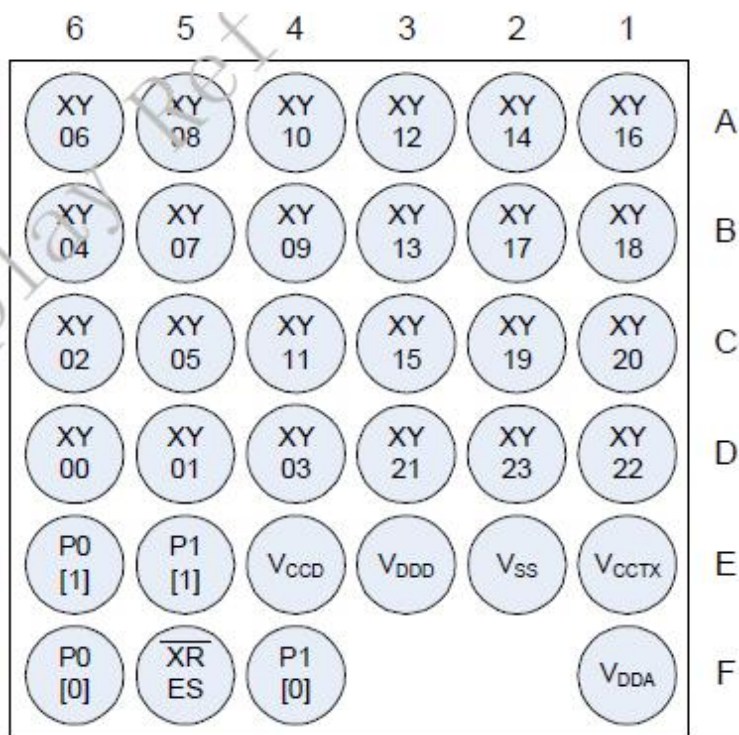
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5.2 TP IC Recommended Operating Conditions

Touch IC: TMA525C (refer to the datasheet)

Symbol	Description	Min	TYP	MAX	UNIT
VCCA	Analog power supply voltage	2.7	2.8	3.6	V
V _{IN} (I2C)	Input voltage range	0	-	3.6	V
V _{OUT} (I2C)	Output voltage range	0	-	3.6	V
V _{IN} (INT)	Input voltage range	0	-	3.6	V
V _{OUT} (INT)	Output voltage range	0	-	3.6	V
V _{OUT} (TX)	Output voltage range	0	-	VCCA	V
V _{OUT} (RX)	Input voltage range	0	-	VCCA	V



Item	Spec	Remark
Operating voltage	2.7-3.6V	
Operating current	2mA	
Linearity	Center part≤1mm	Test tool: φ7mm copper cylinder
	The peripheral position ≤2mm	
Sensitivity	No broken line	Lineation with 5mm/s&20mm/s respectively by φ7mm copper cylinder
Response time	≤10ms	



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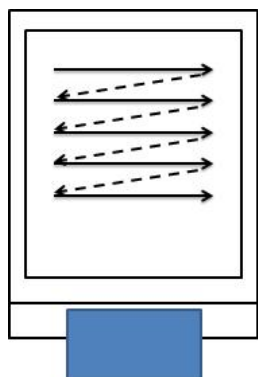
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5.3 I/O Connection

No.	Pin name	I/O	Description
1	VCI_EN	I	VCI enable signal for power ic
2	GND	Power	Ground
3	TP_I2C_SDA	I/O	Touch IC I2C data
4	TP_I2C_SCL	I/O	Touch IC I2C clock
5	TP_SWDIO(NC)	-	Serial Wire Debug Input/Output, leave the pin to open when not in use.
6	TP_RST	I	This signal will reset Touch IC.Signal is active low.
7	TP_INT	O	Touch IC's interrupt to Host
8	TP_VDD	Power	Power supply for Touch IC
9	GND	Power	Ground
10	LCD_RST	I	Driver IC reset signal (0 : enable ; 1 : Disable)
11	LCD_TE	O	Synchronous signal output from panel to avoid tearing effect
12	GND	Power	Ground
13	VIO18	Power	Power supply for interface system except MIPI interface
14	VIO18	Power	Power supply for interface system except MIPI interface
15	VPP(NC)	-	Power supply for OTP. Leave the pin to open when not in use.
16	NFC_ANT2	I/O	Near Field Communication Antenna 2
17	NFC_ANT1	I/O	Near Field Communication Antenna 1
18	GND	Power	Ground
19	SPI_SDO	I/O	SPI interface, Serial output signal in SPI I/F. The data is output on the rising/falling edge of the SCL signal.
20	SPI_SDI	I/O	SPI interface, Serial input signal in SPI I/F. The data is input on the rising edge of the SCL signal.
21	SPI_DCX	I	SPI interface, Display data / command selection in 80-series MPU I/F and 4-wire SPI I/F. D/CX = "0" : Command D/CX = "1" : Display data or Parameter
22	SPI_CLK	I	SPI interface, A synchronous clock signal in SPI I/F.
23	SPI_CS	I	SPI interface, Chip select input pin ("Low" enable)
24	GND	Power	Ground
25	MIPI_CLKP	I	MIPI strobe positive signal
26	MIPI_CLKN	I	MIPI strobe negative signal
27	GND	Power	Ground
28	MIPI_D0P	I/O	MIPI data positive signal
29	MIPI_D0N	I/O	MIPI data negative signal
30	GND	Power	Ground
31	VPH_PWR	Power	AMOLED power
32	VPH_PWR	Power	AMOLED power
33	GND	Power	Ground
34	GND	Power	Ground



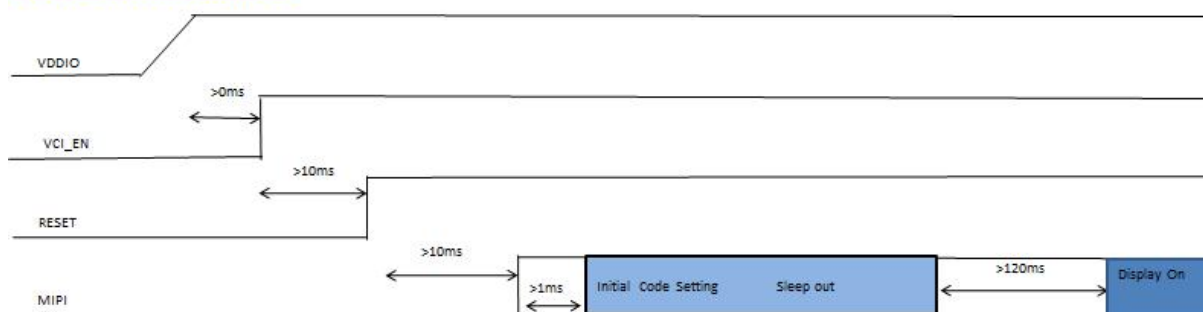
5.4 Graphic memory writing direction



5.5 Recommended Operating Sequence

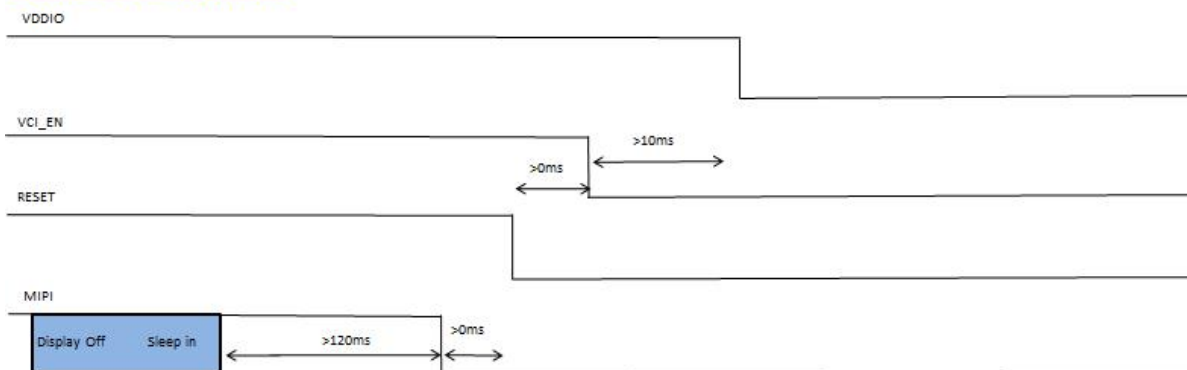
5.5.1 Power on sequence

Power On Sequence

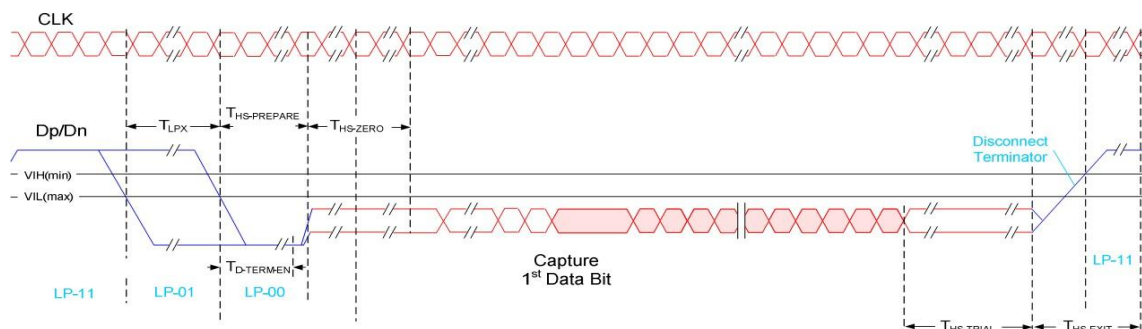


5.5.2 Power off sequence

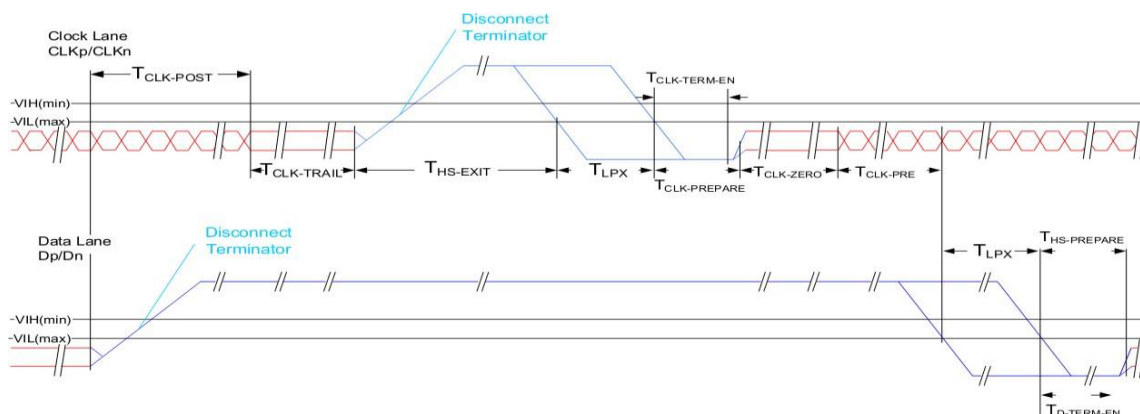
Power Off Sequence



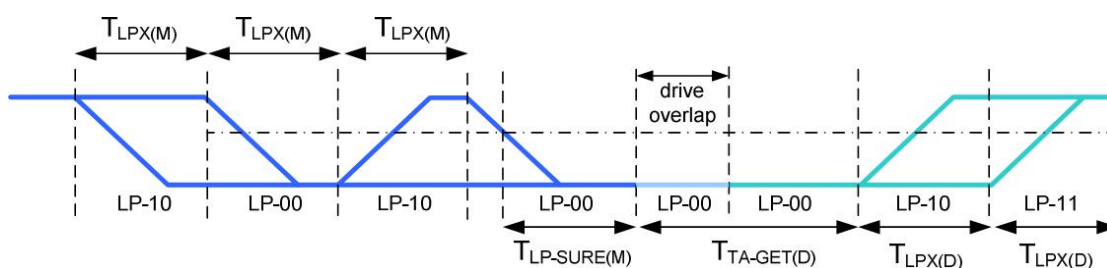
5.5.3 AC Characteristics (MIPI) HS Data Transmission Burst



HS Clock Transmission



Turnaround Procedure





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Timing Parameters

Symbol	Description	Min	Typ	Max	Unit
TREOT	30%-85% rise time and fall time	-	-	35	ns
TCLK-MISS	Timeout for receiver to detect absence of Clock transitions and disable the Clock Lane HS-RX.	-	-	60	ns
TCLK-POST*1	Time that the transmitter continues to send HS clock after the last associated Data Lane has transitioned to LP Mode. Interval is defined as the period from the end of THS-TRAIL to the beginning of TCLK-TRAIL.	60ns + 52*UI (For DCS)	-	-	ns
TCLK-PRE	Time that the HS clock shall be driven by the transmitter prior to any associated Data Lane beginning the transition from LP to HS mode.	8	-	-	ns
TCLK-SETTLE	Time interval during which the HS receiver shall ignore any Clock Lane HS transitions, starting from the beginning of TCLK-PRE.	95	-	300	ns
TCLK-TERM-EN	Time for the Clock Lane receiver to enable the HS line termination, starting from the time point when Dn crosses VIL, MAX.	Time for Dn to reach VTERM-EN		38	ns
THS-SETTLE	Time interval during which the HS receiver shall ignore any Data Lane HS transitions, starting from the beginning of THSPREPARE.	85 ns + 6*UI		145 ns + 10*UI	ns
TEOT	Time from start of THS-TRAIL or TCLK-TRAIL period to start of LP-11 state	-	-	105ns+48*UI	ns
THS-EXIT(1)	time to drive LP-11 after HS burst	100	-	-	ns



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THS-PREPARE	Time to drive LP-00 to prepare for HS transmission	40ns + 4*UI	-	85ns+6*UI	ns
THS-PREPARE + THS-ZERO	THS-PREPARE + Time to drive HS-0 before the Sync sequence	145ns + 10*UI	-	-	ns
THS-SKIP	Time-out at RX to ignore transition period of EoT	40	-	55ns+4*UI	ns
THS-TRAIL	Time to drive flipped differential state after last payload data bit of a HS transmission burst	60 + 4*UI	-	-	ns
TLPX	Length of any Low-Power state period	50	-	-	ns
Ratio TLPX	Ratio of TLPX(MASTER)/TLPS(SLAVE) between Master and Slave side	2/3	-	3/2	ns
TTA-GET	Time to drive LP-00 by new TX	5*TLPX	5*TLPX	5*TLPX	ns
TTA-GO	Time to drive LP-00 after Turnaround Request	4*TLPX	4*TLPX	4*TLPX	ns
TTA-SURE	Time-out before new TX side starts driving	TLPX	-	2*TLPX	ns

Timing requirements for RESETB

When RESETB of the reset pin equals to Low, it will be in the condition of reset. When it is in the condition of reset, it will make the device recover the initial set.

However, in order to avoid the reset noise cause reset, there is a mechanism to judge about whether the reset is needed or not.

The closed interval of Low can be shown as the following.

(Test condition: VDDIO=1.65V~3.6V, VSS=0V, TA=-20°C~+70°C)

Parameter	Symbol	Conditions	Spec			Unit
			Min.	Typ.	Max.	
Reset low pulse width	Trst	-	20	-	-	μs

Table: Reset timing





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6. Electro-Optical Specification

Test condition: 25℃±3℃, 65±20%RH, darkroom

No	Item		Symbol	Condition	Value			Unit	Remark
					Min.	Typ.	Max.		
1	Brightness		L	Full white Without CG	315	350	385	cd/m ²	Note1.
2	HBM		L	High brightness mode	900	1000	-	cd/m ²	Note1
3	Brightness Uniformity		UL	Full white	80	85	-	%	Note4
4	Contrast Ratio		CR	Normal Θ=Φ=0°	10000	100000	-	-	Note3.
5	Response time		Ton+Tof	Normal Θ=Φ=0°	-	2	4	ms	Note2.
6	Color Coordinate of CIE1931	White	X	Normal Θ=Φ=0°	0.28	0.30	0.32	-	Note1.
			Y		0.29	0.31	0.33		
		Red	X		0.668	0.688	0.708		
			Y		0.292	0.312	0.332		
		Green	X		0.18	0.22	0.26		
			Y		0.695	0.735	0.775		
		Blue	X		0.122	0.142	0.162		
			Y		0.022	0.042	0.062		
7	Color Gamut		NTSC	CIE1931	90	105	-	%	
8	Viewing Angle			Top/Bottom/Right/Left CR ratio ≥1000	80			°	Note3.
9	Gamma			Log(Lv-Lb)=log(V)+log(a) V(Gray)= 48,72,104,132, 164,192,224 Lum(gray255)=350nit	2.0	2.2	2.4	-	
10	Flicker			Normal Θ=Φ=0°	-	-35	-30	dB	Note6.
11	Crosstalk			-	-	-	3	%	Note7.
12	Color shift			θL=30°		5	5.5		
13	OLED Life Time			0.95*(TYP brightness) At 25°C,with white color pattern	240			hrs	Note8.
				With 8*8 black-white chess board test	8*8 black-white chess board 10min, to				

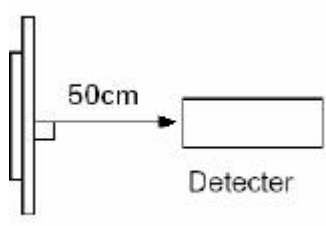
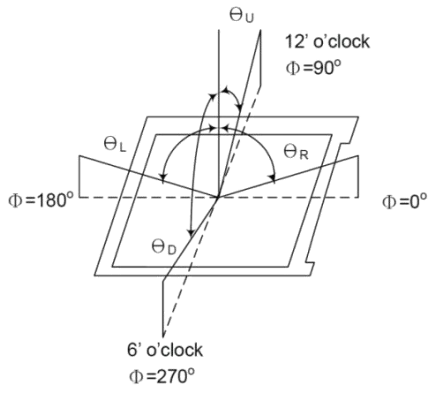


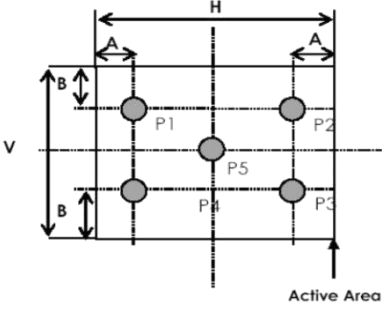
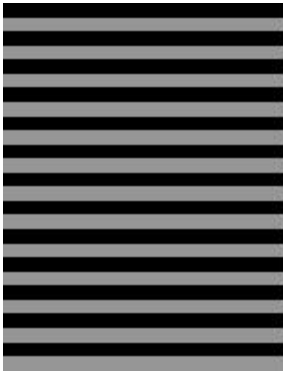
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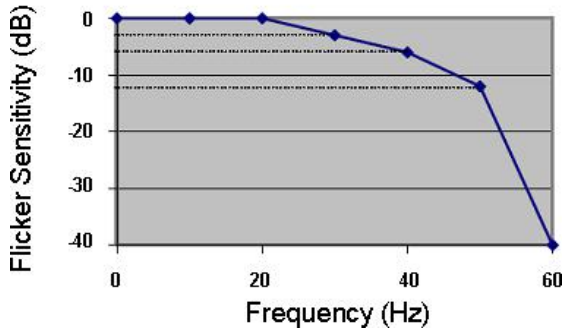
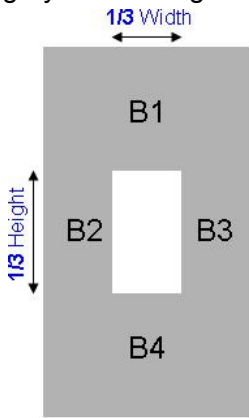
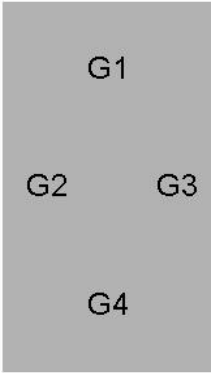
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14	Image Retention	image, lighting on with maximum luminance for 10min	G128,20s disappear	
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See the note in the table below:

No	Item	Details
Note1	Brightness	
Note 2	Response time	
Note 3	Viewing Angle	<p>Contrast Ratio: Dark Room C.R=LW/LB LW: full white brightness of display center P0; LB: full black brightness of display center P0.</p> 

Note 4	Brightness Uniformity	 <p>A: 1/4H B: 1/4V H, V: Active Area</p>
Note 5	Luminance decrease ratio	<p>Definition of Luminance decrease ratio Test pattern : Full White The luminance decrease ratio is calculated by using following formula:</p>
Note 6	Flicker	<p>Suggested Instruments: Konica Minolta CA-310 or Klein Instruments K-8</p>  <p>Odd row : L0 Black Even row : L186 gray level</p> <p>Flicker Test Pattern</p> <p>The flicker level is defined by Fast Fourier Transformation (FTT) as follows:</p> $Flicker = 20 \log_{10} \left(2 \frac{f_{FFTC}(n)}{f_{FFTC}(0)} \right) + FS(Hz) \quad (dB)$ <p>Where fFFTC(n) is the n-th FFT coefficient. fFFTC(0) is the 0-th FFT coefficient which is DC component. FS(Hz) is the flicker sensitivity as a function of frequency.</p> <p>The peak flicker level shall be reported based on the calculation using above formula in which FS(Hz) is determined by the flicker weighing factor shown below.</p>

		<p style="text-align: center;">Flicker Weighing Factor</p>  <p style="text-align: center;">Flicker Sensitivity (dB)</p> <p style="text-align: center;">Frequency (Hz)</p>
Note 7	Crosstalk	<p>Crosstalk shall be calculated by the luminance of B1~B4 and G1~G4 in the patterns shown below.</p> <p>Box Pattern: L128 gray level background with a L255 White window in the central area.</p> <p>Gray Pattern: L128 gray level background only.</p> <div style="display: flex; justify-content: space-around; align-items: center;"> <div style="text-align: center;">  <p>Box Pattern</p> </div> <div style="text-align: center;">  <p>Gray Pattern</p> </div> </div> <p><i>Crosstalk</i></p> $\equiv \text{Maximum} : \left\{ \frac{ B1 - G1 }{G1}, \frac{ B2 - G2 }{G2}, \frac{ B3 - G3 }{G3}, \frac{ B4 - G4 }{G4} \right\} \times 100\%$
Note 8	Life Time	<p>OLED life time is defined by the Minimum Duration Time that the luminance is decayed to a specific ratio (ex. 92%) of initial state.</p> <p>Test Pattern under duration period: L255 White</p>



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7. Reliability

7.1 Environmental Test

Item	Main spec	No. of failures / No. of examinations
High Temperature Operation	70°C/ 240hrs	0/10
Low Temperature Operation	-20°C/ 240hrs	0/10
High Temperature Storage	80°C/ 240hrs	0/10
Low Temperature Storage	-40°C/ 240hrs	0/10
High Temperature Humidity Operation	60°C/93%RH/ 240hrs	0/10
Thermal Shock	-40°C~85°C dwell time=0.5hr, 100 cycles.	0/10

7.2 Electrical Test

Item	Main spec	Note
Air Discharge	±4/±6/±8kV 150pF/330Ω (Module level; without CG)	5Points, Each 10times. After one time discharge, panel and gun touch the ground, through the whole test, turn on ion fan. No degradation of OLED performance after this test.
Contact Discharge	±1/±2kV, 150pF/330Ω (Module level; without CG)	

7.3 Mechanical Test

Test item	Test condition	Note
Packing vibration-proof test	2g, f=10->55->10Hz apply in each of X, Y, and Z direction for 30 min	Package
Packing Drop test	Drop the packing from 60cm height, 6-faces, 3-edges and 1-corner(one time for each)	Package

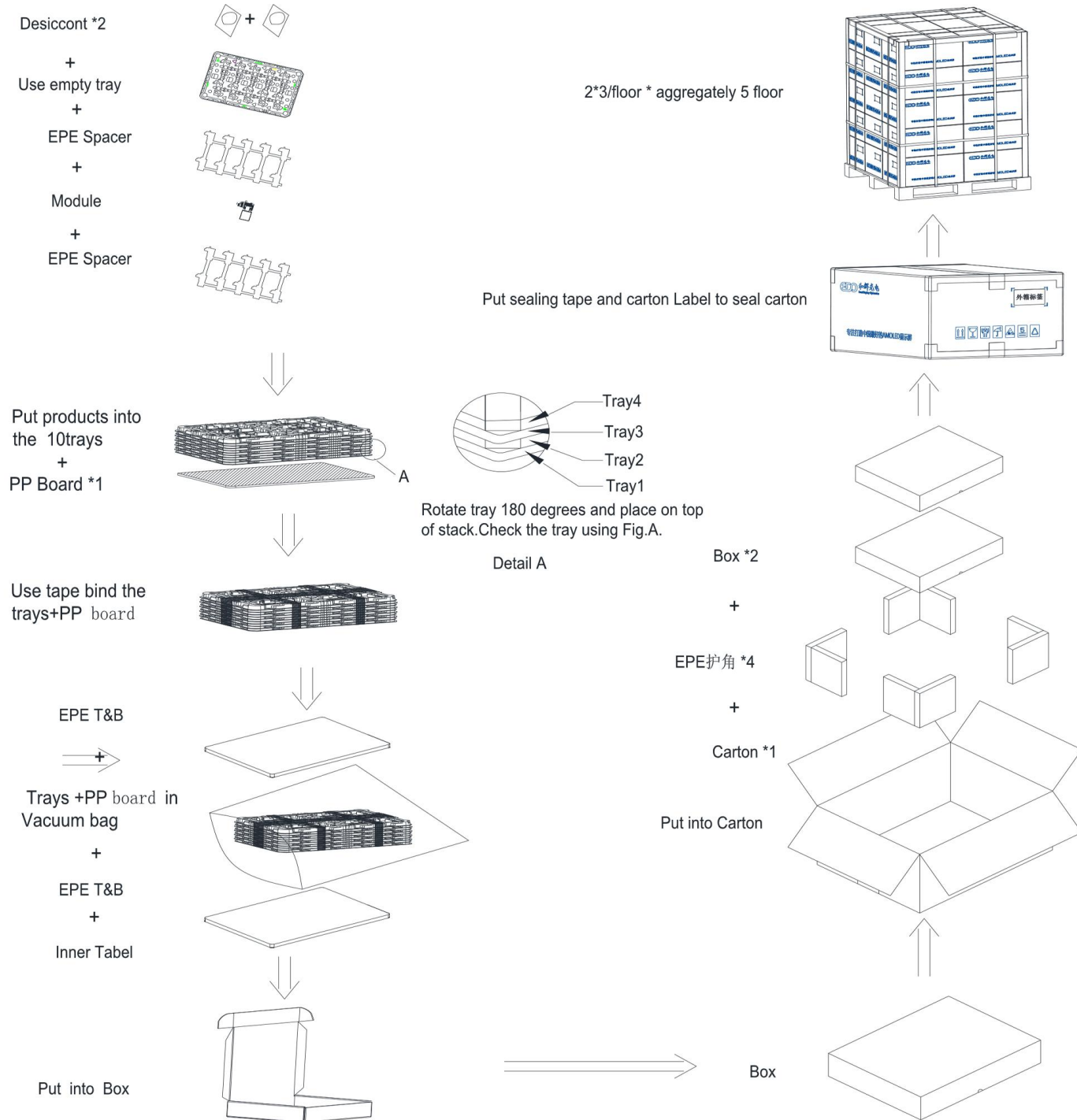




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9. Packing Specification





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10 The Control of Hazardous Substances

The control of Hazardous substances refer to EDO document 《有害物质管控标准书》 (Standard document for the Control of Hazardous substances) EDO –IS- 110, the latest version.