Figure 1: Prefix Sum in C++

```
Edit Execute
riscv1.asm prefix_sum_array.txt*
    addi s0 x0 10
    add t0 x0 x0
 2
    slli t1 t0 2
 3
    sw t0 0(t1)
 4
    addi t0 t0 1
 5
    bne t0 s0 -8
    addi t6 x0 122
 7
    addi s0 x0 9
 8
    add t0 x0 x0
 9
    slli t1 t0 2
10
    lw t2 0(t1)
11
    lw t3 4(t1)
12
    add t2 t2 t3
13
    sw t2 4(t1)
14
    addi t0 t0 1
15
    bne t0 s0 -14
16
    addi t2 x0 122
17
```

Figure 2: Prefix Sum in RISC-V

0 1 3 6 10 15 21 28 36 45

Figure 3: Expected Results

0	0	0	0	0
4	1	0	0	0
8	3	0	0	0
12	6	0	0	0
16	10	0	0	0
20	15	0	0	0
24	21	0	0	0
28	28	0	0	0
32	36	0	0	0
36	45	0	0	0

Figure 4: Actual Results

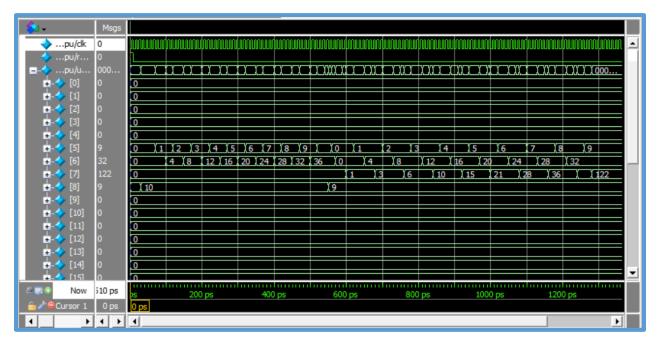


Figure 5: Register File

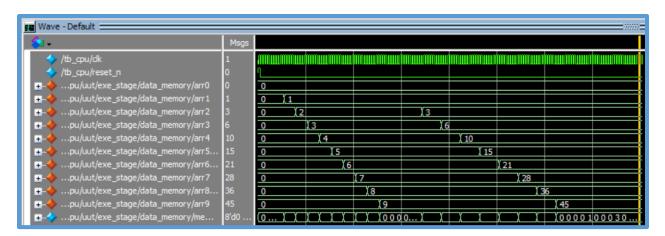


Figure 6: Data Memory

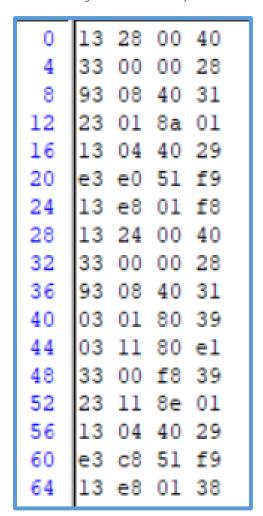


Figure 7: Instruction Memory