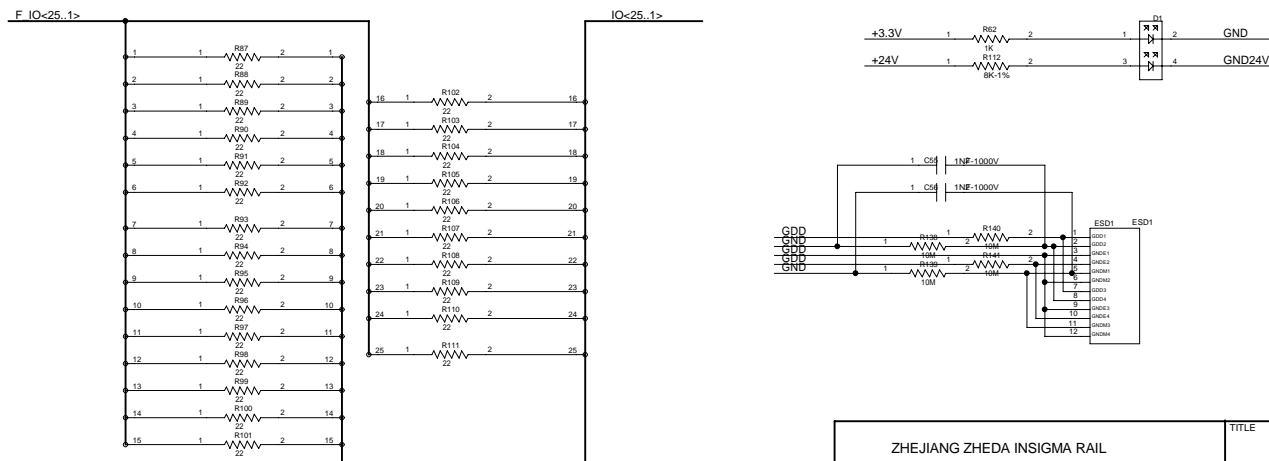
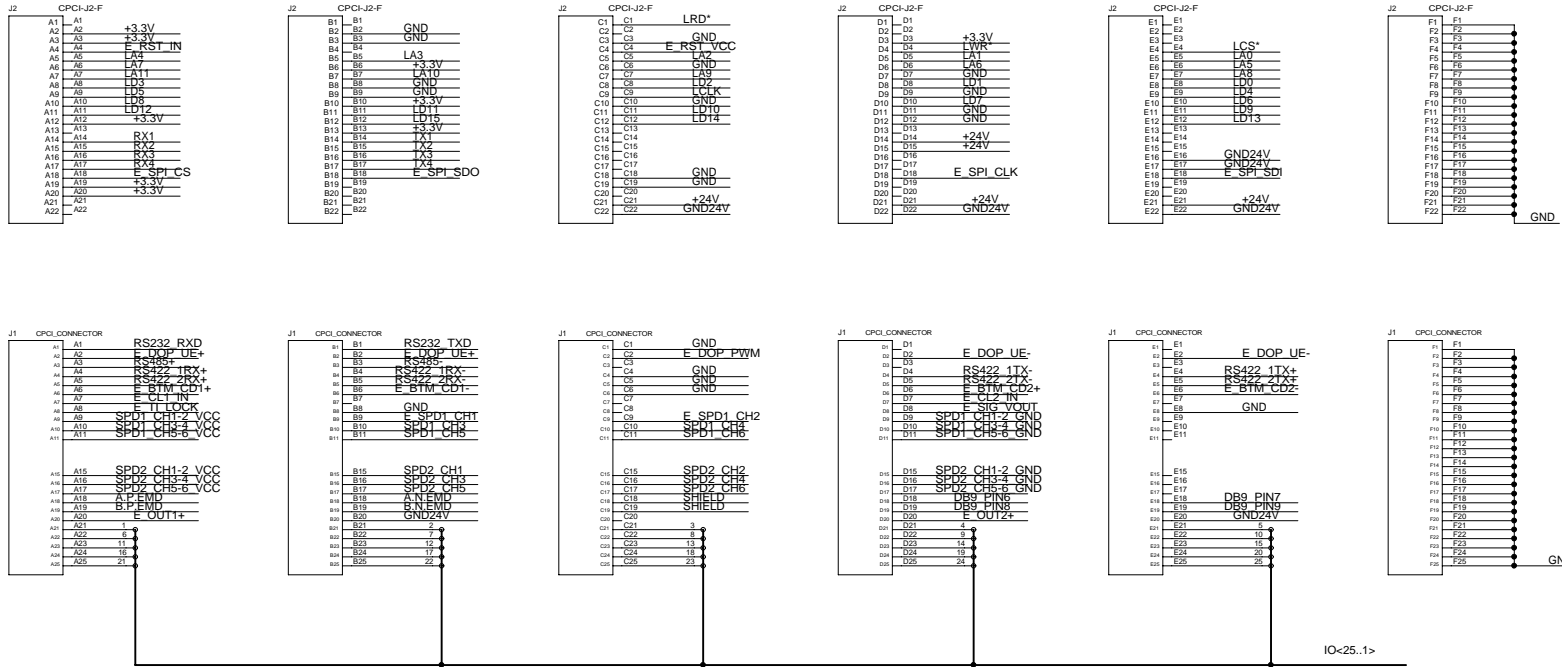


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<table><tr><td>PAGE</td><td>DESCRIPTIONS</td></tr><tr><td>1</td><td>THIS PAGE</td></tr><tr><td>2</td><td>CONNECTORS &amp; POWER LED</td></tr><tr><td>3</td><td>INTERFACE &amp; OUTPUT</td></tr><tr><td>4</td><td>UART</td></tr><tr><td>5</td><td>FPGA</td></tr><tr><td>6</td><td>BTM &amp; DOPPLER &amp; TI &amp; CURRENT LOOP</td></tr><tr><td>7</td><td>SPEED SENSOR</td></tr><tr><td>8</td><td>CURRENT SPEED SENSOR</td></tr><tr><td>9</td><td>MVB</td></tr><tr><td></td><td></td></tr></table>																		PAGE	DESCRIPTIONS	1	THIS PAGE	2	CONNECTORS & POWER LED	3	INTERFACE & OUTPUT	4	UART	5	FPGA	6	BTM & DOPPLER & TI & CURRENT LOOP	7	SPEED SENSOR	8	CURRENT SPEED SENSOR	9	MVB		
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## CONNECTORS & POWER LED

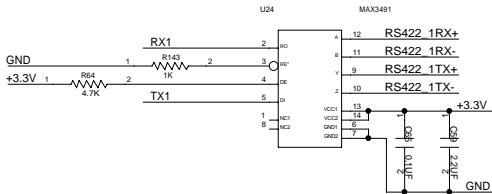
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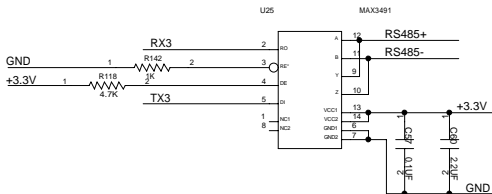
TITLE		DRAWN	
SIZE <b>C</b>	DATE	CHECKED	
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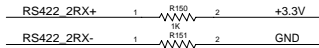
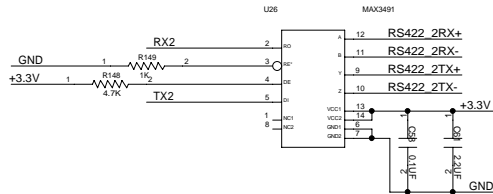
REVISIONS				
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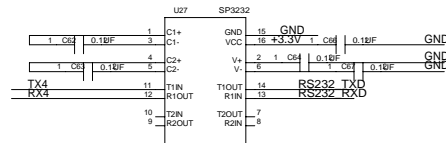
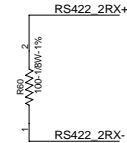
RS422\_1



RS485



RS422\_2



RS232

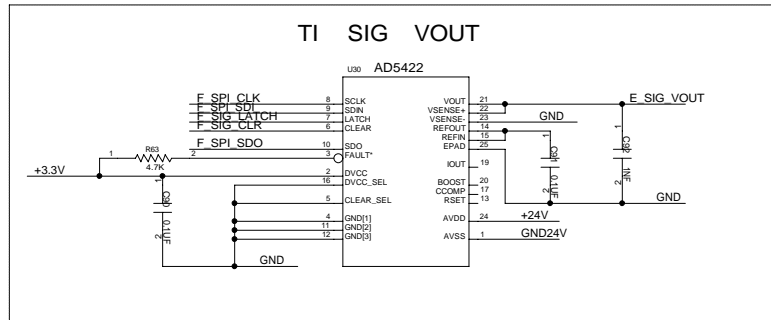
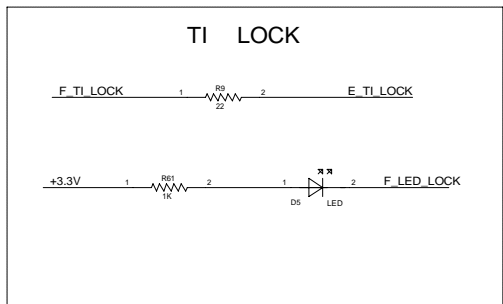
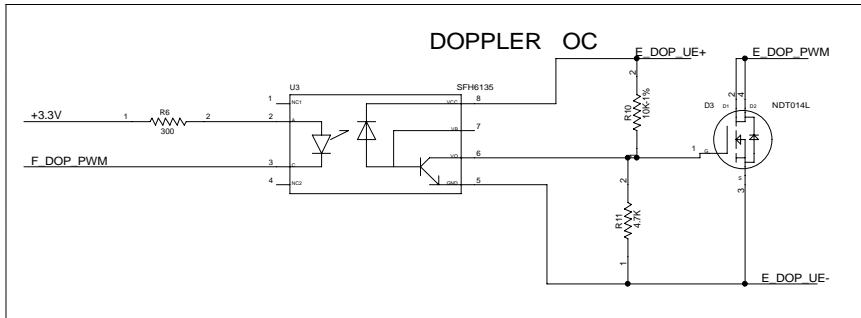
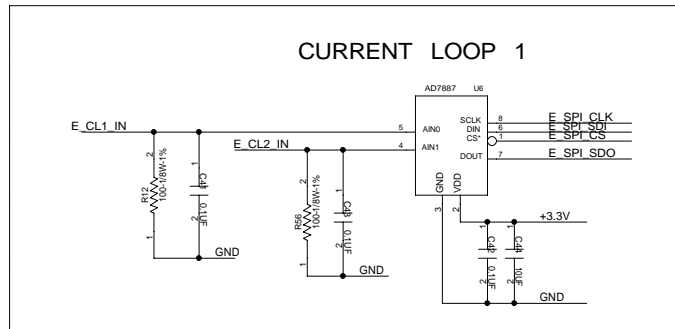
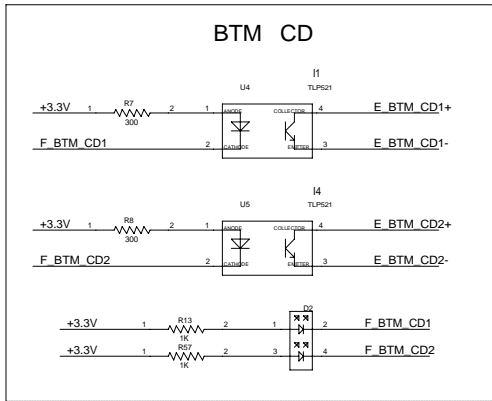
- RS422\_1 - BTM A
- RS422\_2 - BTM B
- RS485 - DOPPLER
- RS232 - TI

UART

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		DRAWING NO.		REV.	SHEET OF



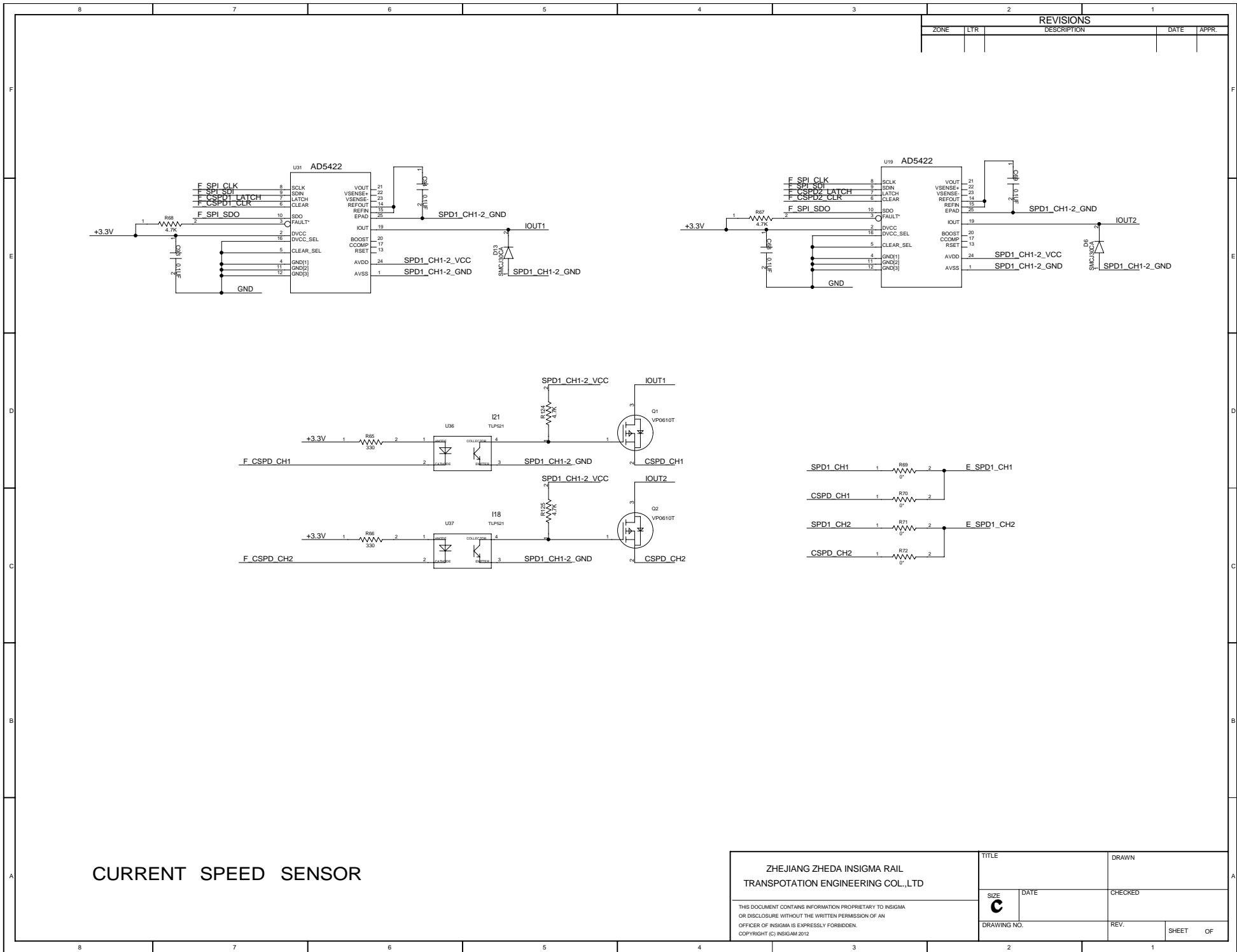
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# BTM & DOPPLER & TI & CURRENT LOOP

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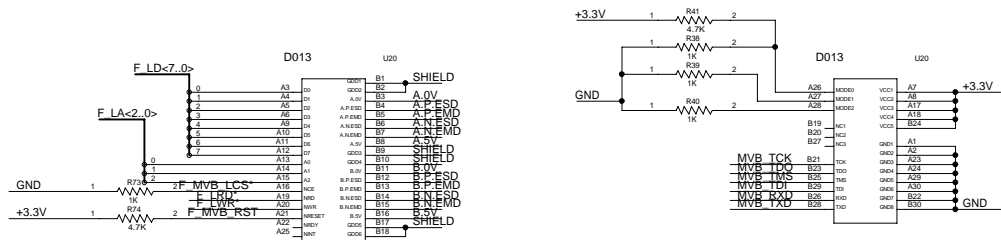


CURRENT SPEED SENSOR

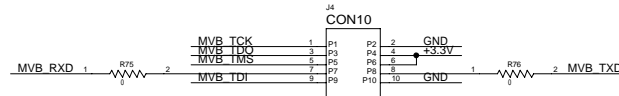
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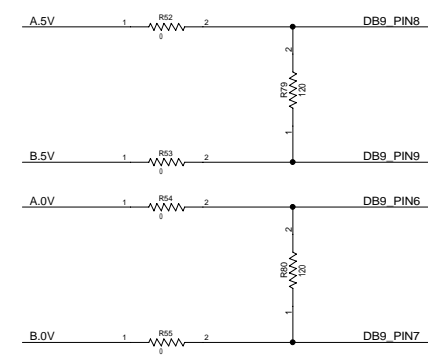
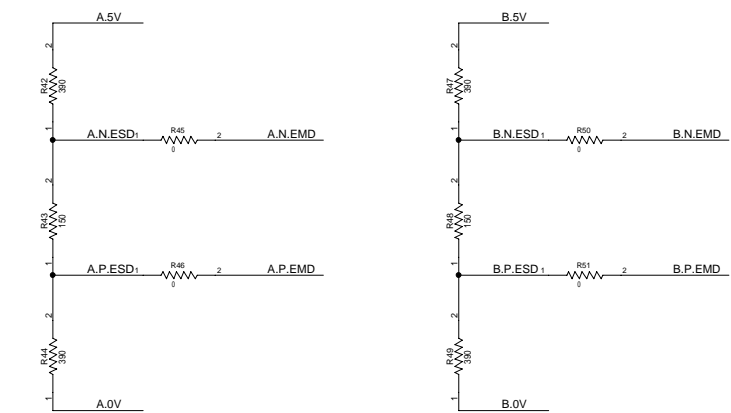
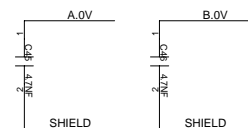
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ZONE	LTR	DESCRIPTION	DATE	APPR.



MODE0:  
0 - ESD, ESD+  
1 - EMD  
MODE1,2:  
0,0 - PARALLEL HOST MODE



Y-CAPACITOR 4.7NF, 250VAC



- A.P.EMD - DB9\_PIN1
- A.N.EMD - DB9\_PIN2
- B.P.EMD - DB9\_PIN4
- B.N.EMD - DB9\_PIN5
- SHIELD\_A
- SHIELD\_B

POPULATE IN ESD

POPULATE IN ESD      POPULATE IN EMD

MVB

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