REVISIONS
ZONE LTR DESCRIPTION DATE APPR.

SIM_DB BOARD(SIM DAUGHTER BOARD)

PAGE	DESCRIPTIONS
1	THIS PAGE
2	CONNECTORS & POWER LED
3	INTERFACE & OUTPUT
4	UART
5	FPGA
6	BTM & DOPPLER & TI & CURRENT LOOP
7	TACHOMETER (SPEED SENSOR)
8	CURRENT SPEED SENSOR
9	MVB

	TITLE	DRAWN
ZHEJIANG ZHEDA INSIGMA RAIL TRANSPOTATION ENGINEERING COL.,LTD	SIM DB BOARD	QIXB
	SIZE	CHECKED
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