FACULTY PROFILE

Shamsuddin S. Khan (shamsuddinkhan@sfitengg.org)

Assistant Professor
Computer Engineering

❖ Date of Joining the Institute : **18/01/2005**

❖ Education :

1. M.E. Computer
   Thadomal Shahani Engineering College (2011)
   Thesis Title: Robust method for facial Expression Recognition

2. B.E. Computer
   SSPM College of Engineering, Kankavli (2004)
   Project: DIGITAL IMAGE ENHANCEMENT TECHNIQUES

❖ Total Experience in Years : **12 Years (29/01/2016)**

A. Teaching : **12Years**
- Assistant professor: 6 years
- Lecturer:5
- Faculty at Computer Station Inc:1 year

❖ Courses Taught:
- Digital Logic Design and Application (UG Level)
- Neural Network and Fuzzy System (UG Level)
- Operating System(UG Level)
- Distributed Computing (UG Level)
- Artificial Intelligence (UG Level)
- Robotics and Artificial Intelligence (UG Level)
- Parallel and Distributed System (UG Level)
- Advanced Operating System (PG Level)

❖ Skill Sets:
  ▪ Application software: Matlab and Simulink, , SCILAB, Prolog.
  ▪ Computer language: C, C++, Java, VC++.

❖ Publication & Conferences : **(29/01/2016)**

  a. Conference   **b. Journal**
  National   : 01   National   : 00
  International : 01   International : 09

❖ Professional Memberships : **ISTE**
  ▪ Service to Professional Bodies:
  ▪ Member-SFIT-Grievance and Redressal committee
  ▪ CSI-Co-coordinator for SFIT-CSI 2014-15

❖ Projects Guided :
  ▪ M.E Project: **7**
  ▪ B.E Project: **19**+

❖ Academic Duties:
  ▪ ISO Internal Auditor: St. Francis Institute of technology(SFIT), Mumbai-400103
  ▪ Member Unfair Means, Discipline & Anti Ragging Committee, St. Francis Institute of technology(SFIT), Mumbai-400103

❖ Interaction with Professional Institutions :
  ▪ University of Mumbai as Examiner and Moderator
  ▪ University of Mumbai as Paper Setter
  ▪ NMIMS as Examiner and Moderator

❖ R&D Activities:

❖ **Continuing Education Programs**
  ▪ Two day ISTE and Mumbai University approved orientation workshop on "**Ph.D. Preparatory 2016**" held during 8-9th January 2016 in SFIT Borivali.
  ▪ Two Week ISTE Workshop on "**Pedagogy for Effective use of ICT in Engineering Education**" to be held during January 2015 by IIT Bombay held under the National Mission on Education through ICT (MHRD).
  ▪ Two Week ISTE Workshop on "**Cyber Security**" to be held during 10th-20th July 2014 by IIT Bombay held under the National Mission on Education through ICT (MHRD).
  ▪ One week AICTE-ISTE approved under TEQIP STTP on "**Advances in Signal And Image Processing (ASIP)**" from 26th -30th November 2012 in Government Engineering College

Jalgaon.

- Two Week ISTE Workshop on "**Introduction to Research Methodology"** to be held during 25thJune 2012 - 4th July 2012 by IIT Bombay held under the National Mission on Education Through ICT(MHRD).
- Attended and presented a paper on "**Neural Network and DCT Based Facial Expression Recognition**" in a National Conference on "Nascent Trends in Information Technologies and Communication Technologies" held on Sept 27th to Sept 29th 2011 in Terna Engineering College Navi Mumbai – Nerul.
- One week ISTE approved short-term training programme on "**Image Processing Application: Basic to Advance (IPA-BA-2010)**" conducted by St. Francis Institute of Technology, Borivli, Mumbai during 11th - 15th January 2010.
- Attended the National Conference on Trends and Challenges in Electronics, Communication, Information & Computer Technology(ELECTRO INFO COM -2007) organized by St. Francis Institute of Technology, Mumbai during 4th to 6th January 2007.
- One week AICTE-ISTE approved under TEQIP short-term training programme on "**Web Technology**" from 4th -8th December 2006 in SSGM COE Shegaon.
- One week STTP on **" Red Hat Enterprise Server System   Administration & Network Services with Security Administration"** from 17th - 23rd Dec 2005 at SFIT Borivli.
- Two week STTP on **"Faculty Development Programme"** from 25th June- 9th July 2005 at SFIT Borivali.
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- Attended and successfully completed two days International workshop on "**Robotics with Boe-Bot and Lynxmotion ALA5"** at Xavier Institute of Engineering, Mahim during 23rd and 24th August 2011.
- Attended and successfully completed five days' workshop on "**Object Oriented Analysis and design using UML with Essential of Rational Software Architecture"** at St. Francis Institute Of Technology, Borivali during 19th May to 23rd May 2011
- Attended and successfully completed four days' workshop on "**Deep Drive Faculty Enablement Program"** at Infosys Technology Limited Pune, during 21st December to 24th December 2010.
- Attended and successfully completed one day workshop on "**Neural Networks and Fuzzy Systems**" at Thadomal Shahani Engineering College, Bandra, Mumbai on 24th February 2007.
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