FACULTY PROFILE

Dakshata Panchal (dakshatapanchal@sfitengg.org)

Assistant Professor
CMPN Engineering

- ❖ Date of Joining the Institute : **01/02/2006**

- ❖ Education :

  1. Pursuing Ph.D. in Computer Engineering
     St. Francis Institute of Technology, Mumbai University

  2. M.E. Computer Engineering
     Vidyalankar Institute of Technology, Mumbai University, 2012
     Dissertation Title: Hepatitis-B Diagnosis Using Artificial Neural Network

  3. B.E. Electronics
     Ramrao Adik Institute of Technology, Mumbai University, 2002
     Project: Programmable Logic Controller

- ❖ Total Experience in Years : **15 Years 4 months (1st July 2017)**

  A. Teaching : 15 **Years 4 months**
    - Assistant professor: 06 years 4 months
    - Lecturer: 09 years

- ❖ Courses Taught:
    - Analysis of Algorithm (PG Level)
    - Advanced Operating System (PG Level)
    - Computer Graphics (UG Level)
    - Discrete Structure (UG Level)
    - Digital Signal and Image processing (UG Level)
    - Microprocessor (UG Level)

- ❖ Skill Sets:
    - Application software: Matlab, Scilab, OpenGL
    - Computer language: C,C++, Java, C#.net

- ❖ Research Advisor in field of
  - ▪ Computer Engineering

- ❖ Active Research in
  - ▪ Computer Graphics (Digital Geometry Processing)

- ❖ Publication & Conferences : **(Till Date)**

a. **Conference**		b. **Journal**	
National	: **04**	National	: **01**
International	: **02**	International	: **06**

- ❖ Books Published : **-NA-**

- ❖ IPR/Patents : **-NA-**

- ❖ Grants Fetched : **-NA-**

- ❖ Awards : -NA-

  - ▪ Professional Memberships : **ISTE, EUROGRAPHICS**

  - ▪ Service to Professional Bodies:
  - ▪ CSI-Branch Counselor for SFIT-CSI 2012-14

  - ▪ Consultancy Activities : **-NA-**

  - ▪ Projects Guided :
  - ▪ PhD : **-NA-**
  - ▪ M.E Project: **06**
  - ▪ B.E Project: **27**

- ❖ Academic Duties:
  - ▪ Exam Committee Convener – SFIT
  - ▪ Time Table Convener - SFIT

- ❖ Interaction with Professional Institutions :
  - ▪ University of Mumbai as Examiner and Moderator
  - ▪ University of Mumbai as Paper Setter
  - ▪ NMIMS University as Moderator and Revaluator
  - ▪ NMIMS University as Paper Setter

❖ R&D Activities:

❖ **Workshop, lecture series & Conferences Conducted:**
- R&D lecture series
- **Faculty Induction Program ,** SFIT, (29 June-10 July 2009)

❖ **Continuing Education Programs**

1. **Embedded DSP : Design and Application,** SFIT, (5-16 June)

2. **Faculty Induction Program ,** SFIT, (19 June-1 July 2006)

3. **Soft Computing Techniques,** SFIT, (15-19 June 2008)

4. **Developing Managerial Skills for Senior Technical Academician, Disha-09,** SFIT, (5-9 June 2009)

5. "**Image Processing Application: Basic to Advance (IPA-BA-2010)**", SFIT, (11-15 Jan 2010)

6. "**Introduction to Research Methodologies**" conducted by Indian Institute of Technology, Mumbai during 25th June - 04th July 2012.

7. Two-day ISTE workshop on "**Aakash for Education**" conducted by Indian Institute of Technology, Mumbai during 10th -11th November 2012.

8. AICTE-ISTE approved under TEQIP phase II STTP on "**Advances in Signal and Image Processing**" from 26th -30th November 2012 in Government college of Engineering Jalgaon.

9. Two-day ISTE workshop on "**Research Methods in Education Technology**" conducted by Indian Institute of Technology, Mumbai on 2nd and 9th February 2013.

10. TEQIP approved workshop on "**Cloud Computing**" (INTERFACE 2013) from 5th - 9th April 2013 in VJTI, Mumbai.

11. Two week ISTE workshop on "**Database Management System"** conducted by Indian Institute of Technology, Mumbai during 21st – 31st May 2013.

12. Two week ISTE workshop on "**Computer Programming**" conducted by Indian Institute of Technology, Mumbai during 16th – 21st June 2014.

13. Two week ISTE workshop on "**Computer Networking**" conducted by Indian Institute of Technology, Mumbai during 5th – 9th May 2014.

14. Two week ISTE workshop on "**Cyber Security**" conducted by Indian Institute of Technology, Mumbai during 10th − 20th July 2014.

15. Two week ISTE workshop on "**Pedagogy for effective use of ICT in Engineering Education**" conducted by Indian Institute of Technology, Mumbai during 5th − 21st January 2015.

16. Course on **" Digital Geometry Processing"** conducted by Indian Institute of Technology, Mumbai during Jan 2017 − May 2017.