MSTL0

	S1	S0	M0	M1	M2	M3	M4	M5	M6	M7	M8
Mg_Sr	0	0	0	0	0	0	1	1	0	1	1
My_Sr	0	1	0	0	0	0	1	1	0	1	1
Mr_Sg	1	0	0	0	0	0	1	1	0	1	1
Mr_Sy	1	1	0	0	0	0	1	1	0	1	1
			= 0	= 0	= 0	= 0	= 1	= 1	= 0	= 1	= 1

MSTL1

	S1	S0	M0	M1	M2	M3	M4	M5	M6	M7	M8
Mg_Sr	0	0	0	1	0	0	1	1	0	1	0
My_Sr	0	1	0	1	0	0	1	1	0	1	0
Mr_Sg	1	0	0	1	0	0	1	1	0	1	0
Mr_Sy	1	1	0	1	0	0	1	1	0	1	0
			= 0	= 1	= 0	= 0	= 1	= 1	= 0	= 1	= 0

MSTL2

	S1	S0	M0	M1	M2	M3	M4	M5	M6	M7	M8
Mg_Sr	0	0	0	1	1	0	0	1	1	1	1
My_Sr	0	1	0	1	1	1	0	1	0	0	0
Mr_Sg	1	0	0	1	1	1	0	0	1	0	0
Mr_Sy	1	1	0	1	1	1	0	0	1	0	0
			= 0	= 1	= 1	= S0 + S1	= 0	= S1'	= S1 + S0'	= S1'S0'	= S1'S0'

MSTL3

	S1	S0	M0	M1	M2	M3	M4	M5	M6	M7	M8
Mg_Sr	0	0	0	1	0	1	1	1	1	1	0
My_Sr	0	1	0	1	0	1	1	1	1	1	0
Mr_Sg	1	0	0	1	0	1	1	1	1	1	0
Mr_Sy	1	1	0	1	0	1	1	1	1	1	0
			= 0	= 1	= 0	= 1	= 1	= 1	= 1	= 1	= 0

MSTL4

	S1	S0	M0	M1	M2	M3	M4	M5	M6	M7	M8
Mg_Sr	0	0	0	1	0	1	0	0	1	1	0
My_Sr	0	1	0	1	0	1	0	0	1	1	0
Mr_Sg	1	0	0	1	0	1	0	0	1	1	0
Mr_Sy	1	1	0	1	0	1	0	0	1	1	0

MSTL5

	S1	S0	M0	M1	M2	M3	M4	M5	M6	M7	M8
			= 0	= 1	= 0	= 1	= 0	= 0	= 1	= 1	= 0
Mg_Sr	0	0	0	1	1	1	0	0	1	0	0
My_Sr	0	1	0	1	1	1	0	0	1	0	0
Mr_Sg	1	0	0	1	1	0	0	1	1	1	1
Mr_Sy	1	1	0	1	1	1	0	1	0	0	0
			= 0	= 1	= 1	= S1' + S0	= 0	= S1	= S1' + S0'	= S1S0'	= S1S0'