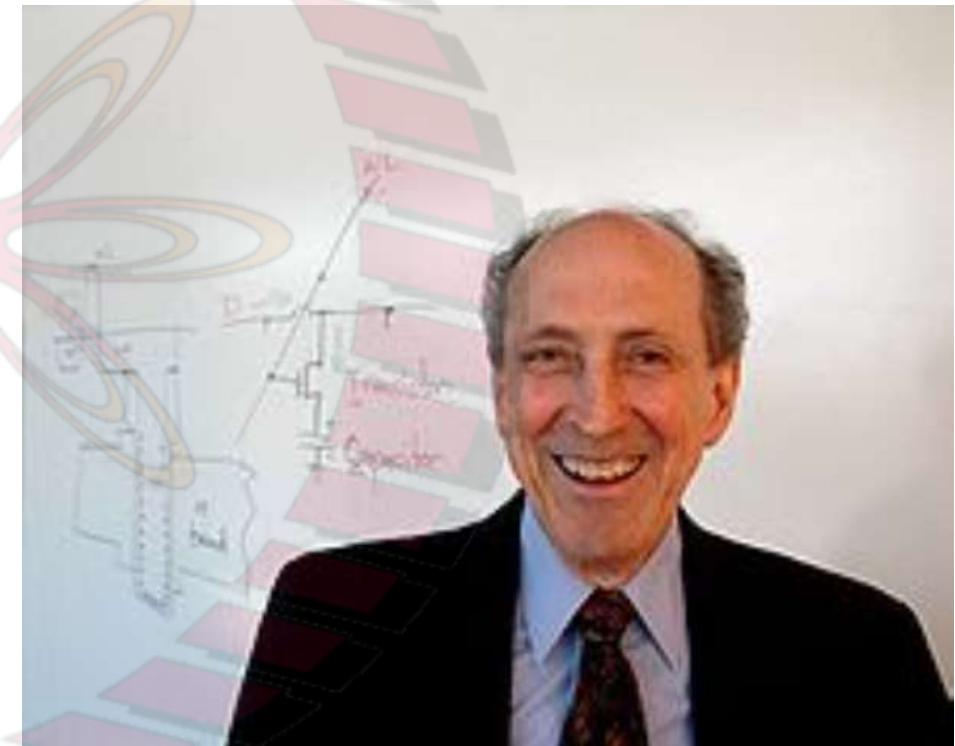


Gordon Moore

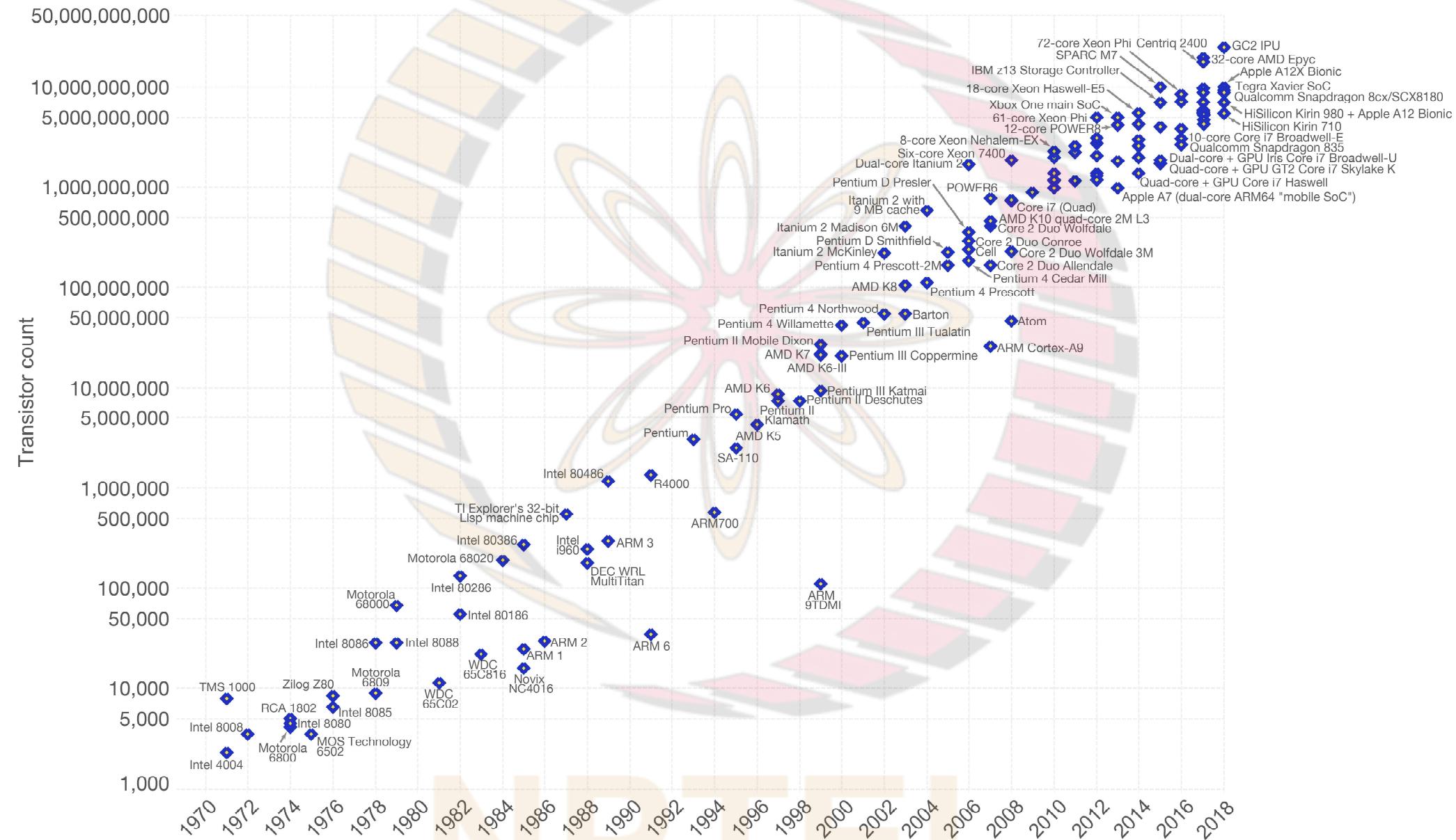
Robert Dennard



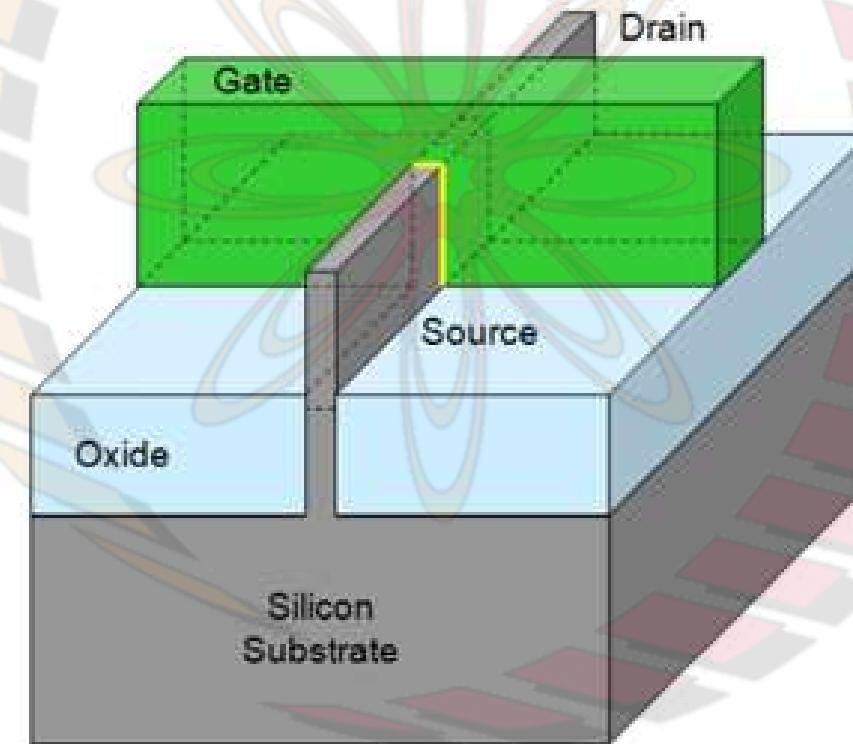
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Moore's Law – The number of transistors on integrated circuit chips (1971-2018)

Moore's law describes the empirical regularity that the number of transistors on integrated circuits doubles approximately every two years. This advancement is important as other aspects of technological progress – such as processing speed or the price of electronic products – are linked to Moore's law.



MOSFET



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MOSFET scaling

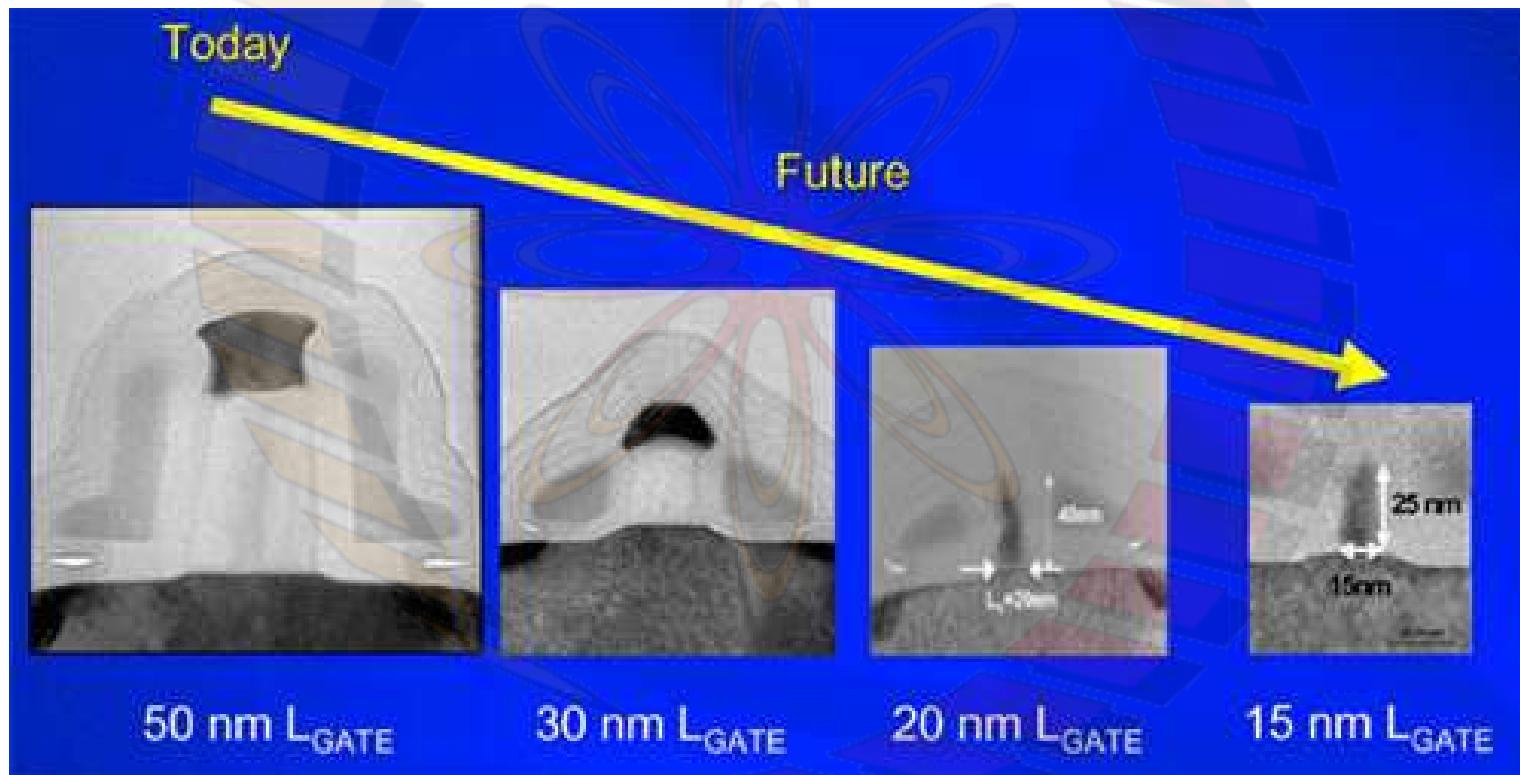


Image: ©Intel Corp.

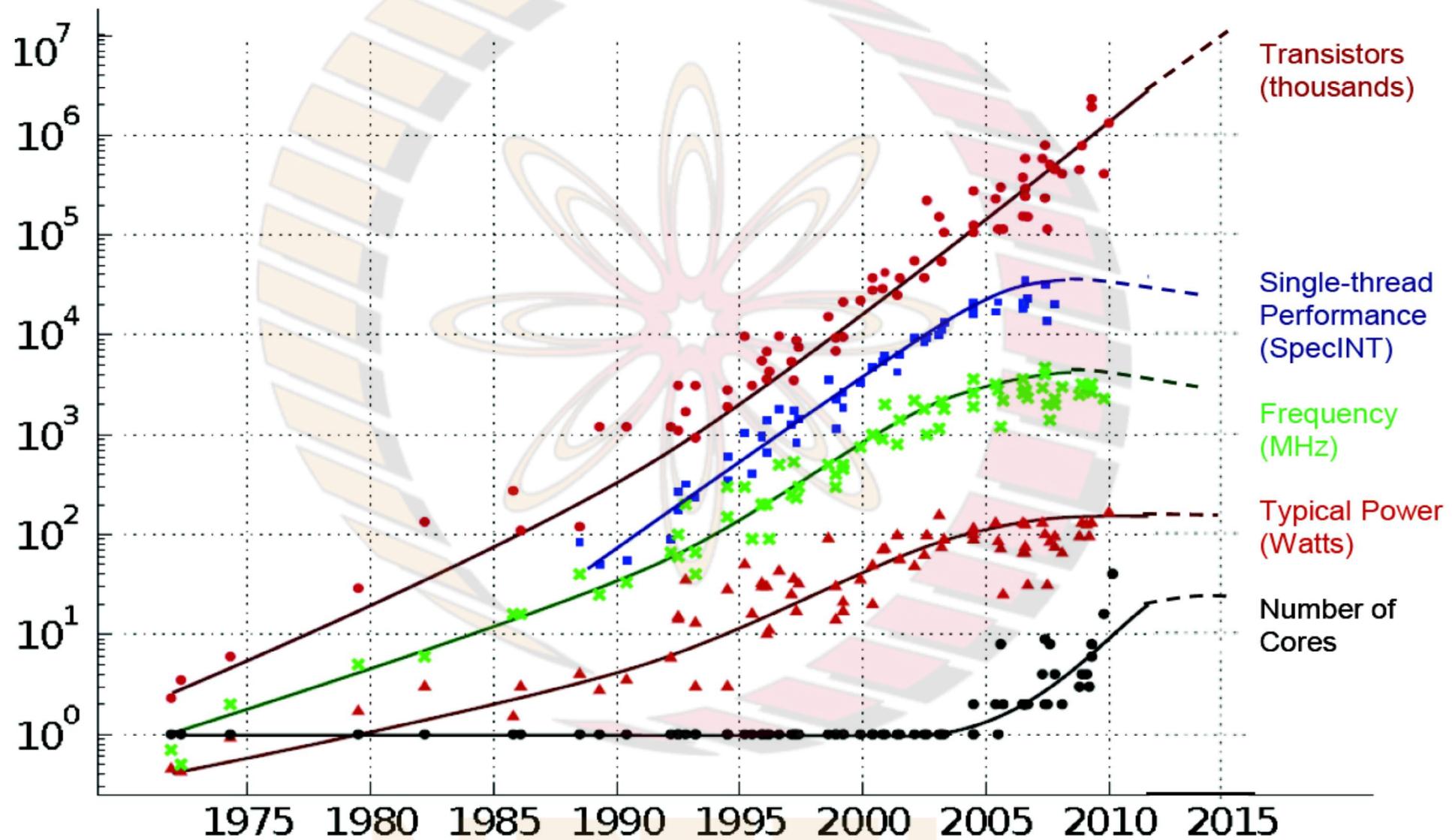
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Dennard Scaling of MOSFET's (1974)

Parameter	Factor
Dimension	l/k
Voltage	l/k
Current	l/k
Capacitance	l/k
Delay time	l/k
Power dissipation/ circuit	l/k^2

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35 YEARS OF MICROPROCESSOR TREND DATA



Original data collected and plotted by M. Horowitz, F. Labonte, O. Shacham, K. Olukotun, L. Hammond and C. Batten
Dotted line extrapolations by C. Moore

Parallel computing

- High performance computing involves parallel programming
- You cannot run away from parallel programming in the future :).
- Problem is people do not think in parallel.
- Automatic parallelization has been a disaster so far.
- There are other limits / roadblocks to parallelism.
Messages need to be communicated.
- Amdahl's Law

Amdahl's law

Amdahl's law states that if P is the proportion of a program that can be made parallel (i.e., benefit from parallelization), and $(1 - P)$ is the proportion that cannot be parallelized (remains serial), then the maximum speedup that can be achieved by using N processors is given as

$$S(N) = \frac{1}{(1 - P) + \frac{P}{N}}$$

NPTEL

Amdahl's law

