

Task Graph Parallelism on GPUs via CUDAGraphs

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Motivation

MIRGE-Com: Overview

- ▶ DG-FEM solver using array-valued data flow graphs.
- For our experiments, we use an unfused version of the operator.

Problem

- ► Target the concurrency across the launched GPU kernels.
 - Would lead to better device usage at lower problem sizes.
 - Hypothesize: Cost savings from:
 - * Lower launch overhead
 - * Exploiting overlap

Approach

- Develop a new ArrayContext.
- ▶ Map *Numpy*-like operations to graph-based IR (*Pytato*).
- Generate CUDAGraph source code by mapping Pytato IR onto PyCUDA.

Code Transformation

```
actx = PytatoCUDAGraphArrayContext()
def f():
    return actx.zeros(100, dtype="float") + 1
f_compiled = actx.compile(f)
f_compiled()
```

Figure: Arraycontext Program

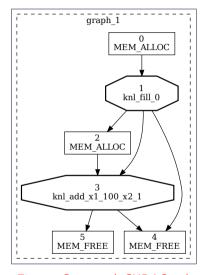


Figure: Generated CUDAGraph

Code Transformation

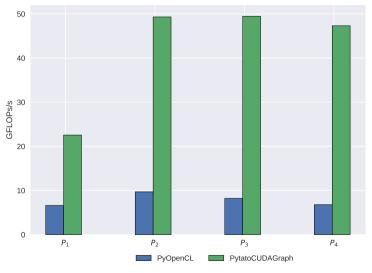
```
mport pycuda.driver as _pt_dry
                                                                                 import numpy as np
                                                                                 from pycuda, compiler import SourceModule as _pt_SourceModule
                                                                                 from pycuda import gpuarray as _pt_gpuarray
                                                                                 from functools import cache
                                                                                 _pt_mod_1 = _pt_SourceModule('#define bIdx(N) ((int) blockIdx.N)\n#define tIdx(N) ((int) threadIdx.N)\n\
                                                                                        nextern "C" _global_ void _launch_bounds_ (32) knl_fill_0(double *_restrict_ out)\n(\n if (99
                                                                                         -1 * tIdx(x) + -32 * bIdx(x) >= 0)\n (\n int const ibatch = 0:\n\n out[tIdx(x) + 32 * bIdx(x)
                                                                                        1 = 0.0: (n ) (n)
                                                                                 _pt_mod_2 = _pt_SourceModule('#define bIdx(N) ((int) blockIdx,N)\n#define tIdx(N) ((int) threadIdx,N)\n\
                                                                                        nextern "C" __global__ void __launch_bounds__(32) knl_add_x1_100_x2_1(double *__restrict__ out,
                                                                                        double const *_restrict___in0)\n{\n if (99 + -1 * tIdx(x) + -32 * bIdx(x) >= 0)\n int
                                                                                        const ibatch = 0;\n\n out[tIdx(x) + 32 * bIdx(x)] = _in0[tIdx(x) + 32 * bIdx(x)] + 11;\n )\n)')
                                                                                 @cache
                                                                                 def exec_graph_builder():
                                                                                     _pt_g = _pt_dry.Graph()
                                                                                     _pt_buffer_acc = {}
                                                                                     _pt_node_acc = {}
   actx = PytatoCUDAGraphArrayContext()
                                                                                     (_pt_memalloc_0, _pt_array_0) = _pt_g,add_memalloc_node(size=800, dependencies=[])
   def f():
                                                                                     _pt_kernel_0 = _pt_g.add_kernel_node(_pt_array_0, func=_pt_nod_ref_function('knl_fill_0'), block=(32,
                                                                                     1, 1), grid=(4, 1, 1), dependencies=[_pt_memalloc_0])
_pt_buffer_acc['_pt_array_0'] = _pt_array_0
         return actx.zeros(100, dtype="float") + 1
   f compiled = actx.compile(f)
                                                                                      pt_node_acc['_pt_kernel_0'] = _pt_kernel_0
                                                                                     (pt_menalloc, _pt_array) = _pt_g, add_menalloc_mode(size=800, dependencies=(_pt_kernel_0])
_pt_kernel = _pt_g.add_kernel_node(_pt_array, _pt_array, _0 + mc=_pt_mod_2, get_function(')
knl add_xi 100 x2 1'), block=(32, _i), grid=(4, _i), dependencies=(_pt_menalloc, _pt_mod_2)
   f compiled()
                                                                                                                                                      _dependencies=[_pt_memalloc, _pt_kernel_0])
                                                                                      _pt_buffer_acc['_pt_array'] = _pt_array
                                                                                      pt buffer acc[' pt array 0'
                                                                                     _pt_buffer_acc['_pt_kernel' _pt_array_0']
pt_node_acc('_pt_kernel')
pt_gradd_nenfree_node(_pt_array_buf_buffernel, _pt_kernel_0))
pt_g_add_nenfree_node(_pt_array_buffernel')
pt_g_add_nenfree_node(_pt_array_buffekernel)
pt_g_add_nenfree_node(_pt_array_buffekernel)
Figure: Arraycontext Program
                                                                                                        imem alloc):
                                                                                      Dt reamit bt gpuarray GPUArray ((100,), dtype='float64', allocator=allocator)
                                                                                          ____g, _pt_g, _pt_node_acc, _pt_buffer_acc) = exec_graph_builder()
                                                                                     exec g.set kernel node params( pt buffer acc[' pt array 0']. kernel node pt node acc[' pt kernel 0
                         Graph dependencies
                                                                                      _pt_exec_g.set_kernel_node_parans(_pt_result.gpudata, _pt_buffer_acc['_pt_array_0'], kernel_node=
                                                                                        _pt_node_acc['_pt_kernel'])
                                                                                     _pt_exec_g.launch()
                                                                                     _pt_tmp = {'_pt_out_': _pt_result}
                                                                                     return _pt_tmp
```

Figure: Generated *PyCUDA* code

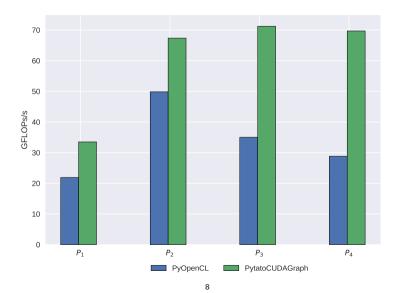
Experimental Setup

- Nvidia Titan V
 - Peak Double prec. FlOps: 6144 GFlOps/s
 - Peak bandwidth: 652.8 GB/s
- ▶ 3D Wave and Euler Operators
- $p \in \{1, 2, 3, 4\}$
- #Tetrahedrons in mesh: 10K (for lower orders)- 27K (for higher orders)
- ► OpenCL Implementation: PoCL-CUDA (3.0)
- ▶ Benchmarks, run instructions at github.com/mitkotak/dg_benchmarks

Wave Operator



Euler Operator



Key Takeaways

- ▶ PytatoCUDAGraphArraycontext abstraction can compile real-world DG-FEM operators.
 - as a drop-in replacement for array program backend.
- Observed a speedup of 3-6x for Wave and 1.5-3x for Euler.

Open Questions

- Is CUDAGraph+FusionActx profitable for MIRGE-Com?
 - * Develop a performance model for CUDAGraphs.
 - * Model peak memory usage for CUDAGraphs.

Future Work

• Upstream the work: Integrate with PyCUDA, Pytato, Arraycontext.