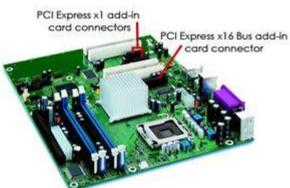
# **How PCI Express Works**

By: Tracy V. Wilson | Updated: Mar 30, 2021

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PCI Express is a high-speed serial connection

that operates more like a network than a bus. Learn how PCI Express can speed up a computer and replace the AGP and view PCI Express pictures.

Photo courtesy Consumer Guide Products

**Peripheral Component Interconnect** (<u>PCI</u>) slots are such an integral part of a computer's architecture that most people take them for granted. For years, PCI has been a versatile, functional way to connect <u>sound</u>, video and network cards to a <u>motherboard</u>.

But PCI has some shortcomings. As <u>processors</u>, <u>video cards</u>, sound cards and networks have gotten faster and more powerful, PCI has stayed the same. It has a fixed width of 32 <u>bits</u> and can handle only 5 devices at a time. The newer, 64-bit PCI-X bus provides more bandwidth, but its greater width compounds some of PCI's other issues.

A new protocol called **PCI Express** (PCIe) eliminates a lot of these shortcomings, provides more bandwidth and is compatible with existing operating systems. In this article, we'll examine what makes PCIe different from PCI. We'll also look at how PCI Express makes a computer faster, can potentially add graphics performance, and can replace the <u>AGP</u> slot.

## **High-Speed Serial Connection**

In the early days of computing, a vast amount of data moved over <u>serial</u> connections. Computers separated data into packets and then moved the <u>packets</u> from one place to another one at a time. Serial connections were reliable but slow, so manufacturers began using <u>parallel</u> connections to send multiple pieces of data simultaneously.

It turns out that parallel connections have their own problems as speeds get higher and higher -- for example, wires can interfere with each other electromagnetically -- so now the pendulum is swinging back toward highly-optimized serial connections. Improvements to hardware and to the process of dividing, labeling and reassembling packets have led to much faster serial connections, such as USB 2.0 and FireWire.

PCI Express is a serial connection that operates more like a network than a <u>bus</u>. Instead of one bus that handles data from multiple sources, PCIe has a switch that controls several point-to-point serial connections. (See <u>How LAN Switches Work</u> for details.) These connections fan out from the switch, leading directly to the devices where the data needs to go. Every device has its own dedicated connection, so devices no longer share bandwidth like they do on a normal bus. We'll look at how this happens in the next section.

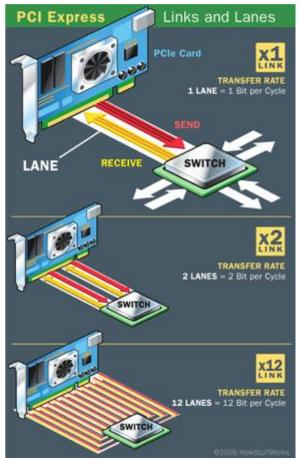
Thank You

Thanks to Joshua Senecal for his assistance with this article.

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# **PCI Express Lanes**



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When the computer starts up, PCIe determines which devices are plugged into the motherboard. It then identifies the links between the devices, creating a map of where traffic will go and negotiating the width of each link. This identification of devices and connections is the same protocol PCI uses, so PCIe does not require any changes to software or operating systems.

Each lane of a PCI Express connection contains two pairs of wires -- one to send and one to receive. Packets of data move across the lane at a rate of one bit per cycle. A **x1 connection**, the smallest PCIe connection, has one lane made up of four wires. It carries one bit per cycle in each direction. A x2 link contains eight wires and transmits two bits at once, a x4 link transmits four bits, and so on. Other configurations are x12, x16 and x32.

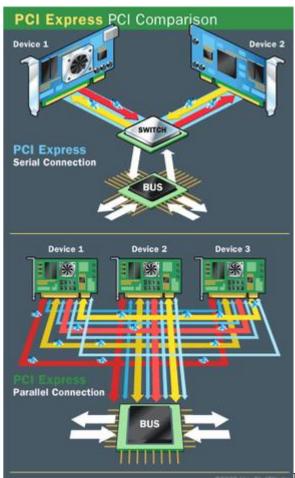
PCI Express is available for desktop and laptop <u>PCs</u>. Its use may lead to lower cost of motherboard production, since its connections contain fewer pins than PCI connections do. It also has the potential to support many devices, including <u>Ethernet</u> cards, <u>USB</u> 2 and <u>video</u> <u>cards</u>.

But how can one serial connection be faster than the 32 wires of PCI or the 64 wires of PCIx? In the next section, we'll look at how PCIe is able to provide a vast amount of bandwidth in a serial format.

Two by Two

The "x" in an "x16" connection stands for "by." PCIe connections are scalable by one, by two, by four, and so on.

# **PCI Express Connection Speeds**



Devices using PCI share a common bus, but each device using PCI Express has its own dedicated connection to the switch.

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The 32-bit PCI bus has a maximum speed of 33 MHz, which allows a maximum of 133 MB of data to pass through the bus per second. The 64-bit PCI-X bus has twice the bus width of PCI. Different PCI-X specifications allow different rates of data transfer, anywhere from 512 MB to 1 GB of data per second.

A single PCI Express lane, however, can handle 200 MB of traffic in each direction per second. A x16 PCIe connector can move an amazing 6.4 GB of data per second in each direction. At these speeds, a x1 connection can easily handle a gigabit Ethernet connection as well as audio and storage applications. A x16 connection can easily handle powerful graphics adapters.

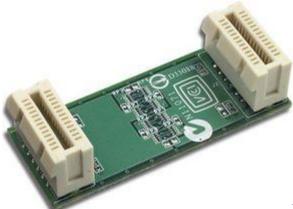
How is this possible? A few simple advances have contributed to this massive jump in serial connection speed:

- Prioritization of data, which allows the system to move the most important data first and helps prevent bottlenecks
- Time-dependent (real-time) data transfers
- Improvements in the physical materials used to make the connections
- Better handshaking and error detection
- Better methods for breaking data into packets and putting the packets together again. Also, since each device has its own dedicated, point-to-point connection to the switch, signals from multiple sources no longer have to work their way through the same bus.

### Slowing the Bus

Interference and signal degradation are common in parallel connections. Poor materials and crossover signal from nearby wires translate into noise, which slows the connection down. The additional bandwidth of the PCI-X bus means it can carry more data that can generate even more noise. The PCI protocol also does not prioritize data, so more important data can get caught in the bottleneck. Using the <u>Accelerated Graphics Port</u> (AGP) slot for video cards removes a substantial amount of traffic, but not enough to compensate for faster processors and I/O devices.

# **PCI Express and Advanced Graphics**



**NVIDIA** SLI link card

Photo courtesy **NVIDIA** 

We've established that PCIe can eliminate the need for an <u>AGP</u> connection. A x16 PCIe slot can accommodate far more data per second than current AGP 8x connections allow. In addition, a x16 PCIe slot can supply 75 watts of power to the video card, as opposed to the 25watt/42 watt AGP 8x connection. But PCIe has even more impressive potential in store for the future of graphics technology.

With the right hardware, a motherboard with two x16 PCIe connections can support two graphics adapters at the same time. Several manufacturers are developing and releasing systems to take advantage of this feature: **NVIDIA Scalable Link Interface** (SLI): With an SLI-certified motherboard, two SLI graphics cards and an SLI connector, a user can put two video cards into the same system. The cards work together by splitting the screen in half. Each card controls half of the screen, and the connector makes sure that everything stays synchronized.

**ATI CrossFire**: Two ATI Radeon® video cards, one with a "compositing engine" chip, plug into a compatible motherboard. ATI's technology focuses on image quality and does not require identical video cards, although high-performance systems must have identical cards. Crossfire divides up the work of rendering in one of three ways:

- splitting the screen in half and assigning one half to each card (called "scissoring")
- dividing up the screen into tiles (like a checkerboard) and having one card render the "white" tiles and the other render the "black" tiles
- having each card render alternate frames

**Alienware Video Array**: Two off-the-shelf video cards combine with a Video Merger Hub and proprietary software. This system will use specialized cooling and power systems to handle all the extra heat and energy from the video cards. Alienware's technology may eventually support as many as four video cards.

Since PCI, PCI-X and PCI Express are all compatible, all three can coexist indefinitely. So far, video cards have made the fastest transition to the PCIe format. Network and sound adapters, as well as other peripherals, have been slower in development. But since PCIe is compatible with current operating systems and can provide faster speeds, it is likely that it will eventually replace PCI as a PC standard. Gradually, PCI-based cards will become obsolete.

For more information about PCI Express and related topics, check out the links on the next page.

Sizing Up

Smaller PCIe cards will fit into larger PCIe slots. The computer simply ignores the extra connections. For example, a x4 card can plug into a x16 slot. A x16 card, however, would be too big for a x4 slot.

Originally Published: Aug 17, 2005

### **PCIe Slots FAQs**

### What is a PCI express slot?

A PCIe or PCI express slot is the connection between a PC's motherboard and peripheral components.

#### Is PCI compatible with PCI express?

They are not compatible because they have different configurations. There are motherboards, however, that host a combination of the two.

#### Does the graphics card go into the PCIe slot?

It should go into the first available PCI Express x16 slot. That isn't to say, however, that lower slots are incapable of running a graphics card.

#### Is PCI the same as PCIe?

They are not the same. PCIe is able to connect graphics cards that PCI can not, and it is also faster by comparison.

#### What is the main difference between PCI and PCIe?

PCIe is a serial interface whereas PCI is a parallel interface.

# **Lots More Information**

### **Related Articles**

- How AGP Works
- How the Personal Computer Works
- How PCI Works
- How Ethernet Works
- How Bits and Bytes Work

### **More Great Links**

- <u>Understanding PCI Express</u>
- PCI Express: An Overview: Page 1
- PCI Express for Graphics
- IBM Redbooks | Introduction to PCI Express
- Intel® Developer Network for PCI Express Architecture