
New Tutorial Proposal Submission Confirmation Email

VLSID Website <noreply@vlsid.org>
To: saroj.rout@silicon.ac.in

Sun, Sep 15, 2024 at 11:54 PM

Dear Dr. Saroj Rout,

We thank you for submitting a tutorial proposal titled “Machine Learning in Analog IC Design Automation”.

Your Submission ID: CFT_2025_59 .

Please stay tuned for the next steps.

Meanwhile, in case of any queries, please raise a request at <https://vlsid.org/contact-us/>.

Please find complete submission data below.

Regards,

VLSID 2025 Committee

*** Submission done on: 2024-09-15 23:54:42 ***

*** All Data Below ***

Tutorial Title

Machine Learning in Analog IC Design Automation

Salutation

Dr.

Name

Saroj Rout

Gender

Male

Email

saroj.rout@silicon.ac.in

Mobile Number

919090752283

Organization

Silicon University

Designation / Job Title:

Additional Professor

Linkedin Profile URL

<https://www.linkedin.com/in/sroutk>

Upload Photograph:**Upload Tutorial Proposal**

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