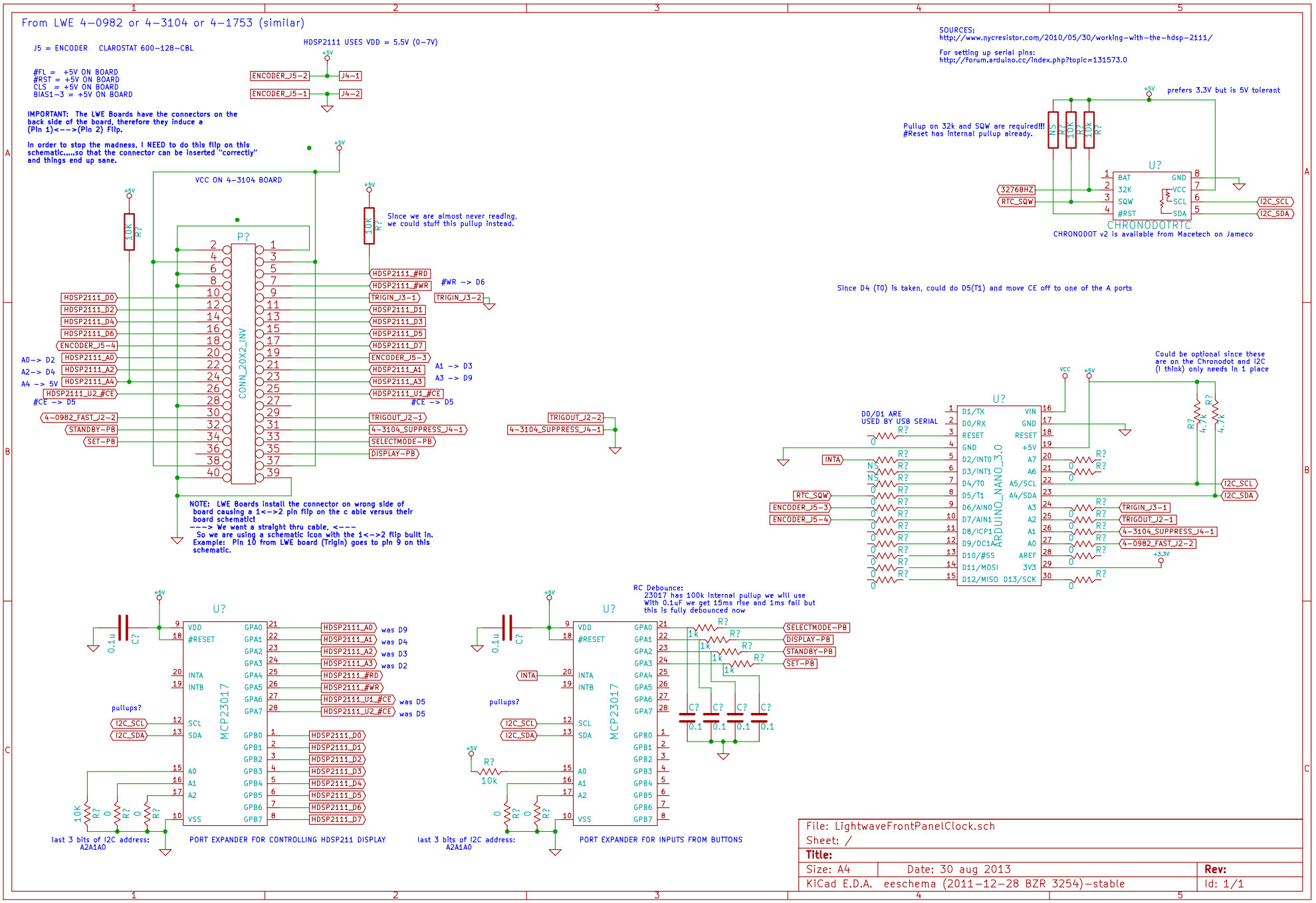
From LWE 4-0982 or 4-3104 or 4-1753 (similar)
J5 = ENCODER CLAROSTAT 600-128-CBL
#FL = +5V ON BOARD
#RST = +5V ON BOARD
CLS = +5V ON BOARD
BIAS1-3 = +5V ON BOARD
IMPORTANT: The LWE Boards have the connectors on the back side of the board, therefore they induce a (Pin 1)<-->(Pin 2) Flip.
In order to stop the madness, I NEED to do this flip on this schematic.....so that the connector can be inserted "correctly" and things end up sane.
HDSP2111 USES VDD = 5.5V (0-7V)
ENCODER_J5-2
ENCODER_J5-1
J4-1
J4-2
VCC ON 4-3104 BOARD
P?
CONN_20X2_INV
Since we are almost never reading, we could stuff this pullup instead.
#WR -> D6
A0-> D2
A2-> D4
A4 -> 5V
#CE -> D5
A1 -> D3
A3 -> D9
NOTE: LWE Boards install the connector on wrong side of board causing a 1<->2 pin flip on the c able versus their board schematic!
---> We want a straight thru cable, <---
So we are using a schematic icon with the 1<->2 flip built in.
Example: Pin 10 from LWE board (Trigin) goes to pin 9 on this schematic.
SOURCES:
http://www.nycresistor.com/2010/05/30/working-with-the-hdsp-2111/
For setting up serial pins:
http://forum.arduino.cc/index.php?topic=131573.0
prefers 3.3V but is 5V tolerant
Pullup on 32k and SQW are required!!!
#Reset has internal pullup already.
32768HZ
RTC_SQW
CHRONODOTRTC
CHRONODOT v2 is available from Macetech on Jameco
Since D4 (T0) is taken, could do D5(T1) and move CE off to one of the A ports
Could be optional since these are on the Chronodot and I2C (I think) only needs in 1 place
D0/D1 ARE USED BY USB SERIAL
ARDUINO_NANO_3.0
MCP23017
PORT EXPANDER FOR CONTROLLING HDSP211 DISPLAY
PORT EXPANDER FOR INPUTS FROM BUTTONS
RC Debounce:
23017 has 100k internal pullup we will use
With 0.1uF we get 15ms rise and 1ms fall but this is fully debounced now
pullups?
last 3 bits of I2C address: A2A1A0
File: LightwaveFrontPanelClock.sch
Sheet: /
Title:
Size: A4
Date: 30 aug 2013
Rev:
KiCad E.D.A. eeschema (2011-12-28 BZR 3254)-stable
Id: 1/1