

Computer Architecture - Project 3

Members:

Name	B-number
Mrugank Jadhav	B00972210
Amitesh Dubey	B00892352
Rujuta Vaidya	B00963799

Task Distribution

Sr. no.	Task	Assigned to
1	create jump instruction and Functional units	Rujuta
2	Renaming table logic	Amitesh
3	IQ logic	Mrugank
4	LSQ logic	Amitesh
5	ROB and Commit Functional unit	Rujuta
6	Early tagging	Amitesh
7	New Forwarding bus logic	Mrugank
8	Branch prediction	Mrugank