# COMP 122/L: Computer Arch. and Assembly Language Spring 2021

Instructor: Maryam Jalali (mjalalita@csun.edu)

Office: TBD

Office Hours: Tuesday: 2:30 PM - 3:30 PM via Zoom

Thursday: 2:30 PM - 3:30 PM via Zoom

Section	Class Title	Dates & Times	Room	Class Dates
COMP122	COMP ARCH	TuTh	Online	Jan 26, 2021-
16314	ASSEM (Lecture)	12:30 PM - 12:55 PM	Class	May 14, 2021
			via Zoom	•
COMP122-L	COMP ARCH	TuTh	Online	Jan 26, 2021-
16315	ASSEM (Laboratory)	01:00 PM - 02:15 PM	Class	May 14, 2021
			via Zoom	

#### **Final Exam:**

Section	Class Title	Exam Date	Exam Time	Exam Room
COMP122 16314	COMP ARCH ASSEM (Lecture)	MAY 20, 2021	12:45 PM - 02:45 PM	Online via Zoom

### **Zoom Lecture Policy:**

In the event the course is held online, students may be required to appear on **webcam** during lectures and exams. For students who feel this is a violation of their privacy, they can take steps to create a neutral background, either physically or electronically. For any student who still objects, the instructor may be able to grant exceptions if the student provides the instructor with a reasonable justification for why the policy is not acceptable. Requests for exceptions will be considered on a per student basis.

#### Communication:

Please use email. Please keep emails short and focused, and use a clear subject line beginning with "COMP 122 Question". Many technical questions are better handled in person during lecture and lab rather than email since the class as a whole might benefit from the discussion.

You may email me (mjalalita@csun.edu) at any time; I will generally respond within 24 hours (during the academic days). Always include your name, course, and CSUN email address in your messages to me; an email address like meqwik@love.com leaves me clueless about who you are!

## **Course Description (from the catalog)**

Introduction to computer architecture, assembly language programming system software and computer applications. Number systems and data representation. Internal organization of a computer. Primitive instructions and operations. Assembly language.

## **Learning Objectives**

A successful student will learn basic assembly programming skills, understand the fundamentals of digital logic design, and understand the interface between the two. The ARMv4 instruction set will be used for assembly, though the concepts are broadly applicable. In particular, successful students will be able to:

- Describe how modern computers represent numbers, and interconvert between different numeric representations
- Perform common operations over computer-represented numbers, and design circuits which perform these operations
- Write programs in ARM assembly, including conditionals, loops, arrays, and functions
- Design and simplify combinatorial circuits with Boolean algebra and Karnaugh maps
- Design sequential circuits, including those implementing finite state machines
- Design a simplistic processor implementing a restricted assembly language
- Understand, from a high-level, the design of a high-level processor
- ...among many others

**Prerequisite:** Grade of "C" or better in <u>COMP 110/L</u>; Grade of "C" or better in <u>MATH 103</u>, <u>MATH 104</u>, <u>MATH 105</u>, <u>MATH 150A</u> or <u>MATH 255A</u>, or a passing score on the <u>Mathematics Placement Test (MPT)</u> that satisfies prerequisites for MATH 150A or 255A; Lower division writing requirement. **Corequisite:** COMP 122L.

#### Course Material:

Course material is available on Canvas (<a href="https://canvas.csun.edu">https://canvas.csun.edu</a>). Labs, Assignments, and Exams and gwill be posted on canvas (<a href="https://canvas.csun.edu">https://canvas.csun.edu</a>). Any questions about a homework/exam grade should be addressed within **two** days of posting. After two days, all grades are final.

#### **Textbook**

No textbook is required. If you'd like a textbook for further study, two decent supplemental textbooks are:

- 1. Computer Organization and Design: The Hardware/Software Interface (David A. Patterson and John L. Hennessy); any edition from the past several years
- 2. Computer Systems Organization and Architecture (John D. Carpinelli)

#### **Assignment:**

**Note**: All assignments submitted in "digital file" format (.zip, .rar, .pdf, .docx, etc.) must be submitted using Canvas. An "Assignment" area will be created for each assignment.

Assignments submitted via email will not be accepted unless explicitly approved by the instructor prior to email submission.

Homework/Projects will be assigned with due dates. Students are expected to:

- a) Complete the assignments on time to the best of their ability.
  - Students are expected to submit their own original work.
- b) **ASK QUESTIONS** if problems are encountered or if more information is required (**BEFORE** the due date!).

In the event that there is a problem with Canvas, you may email your assignments and projects to me (mjalalita@csun.edu), though this should be considered a last resort.

# Grading

You will receive a **single combined grade** for the lecture and lab. Your grade is based on the following components. <u>Canvas will be only used for posting the grades of the exams, quizzes and labs</u>. At any point of semester, if you want to estimate your performance in the class, you must use the following table.

Quizzes	5%
Lab Assignments	25%
Lab Midterm Exam	15%
Lecture Midterm Exam	15%
Lab Final Exam	20%
Lecture Final Exam	20%

The exact number of lab assignments has not been set, as this will depend somewhat on how the class progresses. Lab assignments are submitted through Canvas (<a href="https://canvas.csun.edu/">https://canvas.csun.edu/</a>). In the event that there is a problem with Canvas, you may email your assignment to me (<a href="miglalita@csun.edu">miglalita@csun.edu</a>), though this should be considered a last resort. Quizzes will be taken via Canvas. Notice will be given one week ahead of each quiz.

**Plus/minus grading is used**, according to the scale below. The left column shows the minimal score necessary to receive the grade in the right column. The highest letter grade possible given the score is chosen; e.g., if you receive an 88.2, you'd receive a 'B+' for the course, which corresponds to being >= 86.5.

If your score is >=	you will receive
96.5	A+
92.5	Α
89.5	A-
86.5	B+
82.5	В
79.5	B-
76.5	C+
72.5	С
69.5	C-
66.5	D+
62.5	D
59.5	D-
0	F

- NOTE: Failure to take the Final Exam will result in a grade of "WU" which is equivalent to a grade of "F"
- An important part of this course is the operating system terminology, concepts, and definitions; therefore, I do not answer questions during examinations.
- In fairness to all, I don't give make-up for any missed projects, homework, or exams.
- An incomplete (I) grade is given for genuine medical and other certified emergencies only; it is never given to catch up with missed assignments. Furthermore, to receive an Incomplete grade, you must have successfully completed at least two-thirds of the semester with a passing grade.

## **Collaboration for Lab Assignments**

All students are required to submit their own individual work. For lab assignments (and **only** lab assignments), students may discuss among each other, as long as they don't share actual solutions (this forbids digitally sharing code). That is, you **cannot** simply copy someone else's solution. The only stipulation is that **if you do discuss with someone else, say so in your submission.** This is not for punitive reasons; this is only so I get a sense of who is working with who. My intention with this policy is to enable collaborative learning, as opposed to simply sharing a solution.

## Late Policy:

Late assignments will be accepted without penalty if prior arrangements have been made or there is some sort of legitimate emergency (at my discretion). If an assignment is otherwise submitted late, it will be penalized according to the following scale:

If your assignment is late by <= this many days	it will be deducted by	
1	10%	
2	30%	
3	60%	
4+	100%	

To be clear, assignments that are submitted four or more days beyond the deadline will not receive credit.

## **Plagiarism and Academic Honesty**

While collaboration is allowed on lab assignments, you are responsible for all of your own work. You may **not** take code from online sources and submit it as your own. No discussion whatsoever is allowed during exams, except with the instructor. Any violations can result in a failing grade for the assignment, or potentially failing the course for egregious cases. A report will also be made to the Dean of Academic Affairs.

Students who repeatedly violate this policy across multiple courses may be suspended or even expelled.

#### Disabled Students

"If you have a disability and need accommodations, please register with the Disability Resources and Educational Services (DRES) office or the National Center on Deafness (NCOD). The DRES office is located in Bayramian Hall, room 110 and can be reached at (818) 677-2684. NCOD is located on Bertrand Street in Jeanne Chisholm Hall and can be reached at (818) 677-2611. If you would like to

discuss your need for accommodations with me, please contact me to set up an appointment."

# **Changes to Syllabus**

Changes may be needed to this syllabus and to the course plan. All such changes will be announced in class and will be announced via email. Students are responsible for this information.

# Class Schedule and List of Topics (Subject to Change)

Exactly which topics are covered and when is subject to change.

Week	Dates	Topics
1	1/26 - 1/28	Number representation
2	2/04 - 2/06	Floating point, operations on binary values
3	2/09 - 2/11	ARM assembly: introduction and arithmetic
4	2/16 - 2/18	ARM assembly: conditionals and memory operations
5	2/23 - 2/25	ARM assembly: loops and arrays
6	3/02 - 3/04	ARM assembly: functions
7	3/09 - 3/11	More ARM assembly; review
8	3/16 - 3/18	Spring Break, No classes
9	3/23 - 3/25	,
		3/25 Lecture Midterm Exam
10	3/30 - 4/01	Boolean logic and introductory combinatorial circuits
11	4/06- 4/08	Simplifying circuits with Boolean algebra and K-maps
12	4/13- 4/15	Sequential circuits
13	4/20- 4/22	Finite state machines
14	4/27- 4/29	Processor control units
15	5/04- 5/06	More processor control units and advanced processors
16		More processor control units and advanced processors, 5/13 Lab Final Exam
17	5/20	5/20 Final Lecture Exam 12:45 PM - 02:45 PM