

## 1. Description

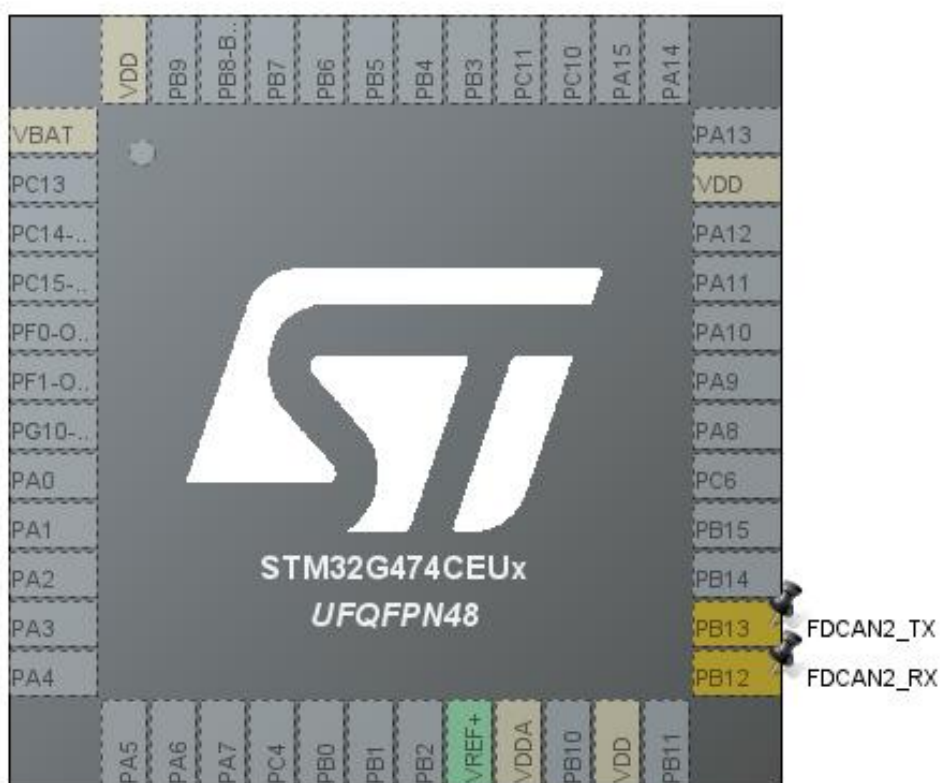
### 1.1. Project

Project Name	moteus-fdcantest
Board Name	custom
Generated with:	STM32CubeMX 5.3.0
Date	02/22/2021

### 1.2. MCU

MCU Series	STM32G4
MCU Line	STM32G4x4
MCU name	STM32G474CEUx
MCU Package	UFQFPN48
MCU Pin number	48

## 2. Pinout Configuration

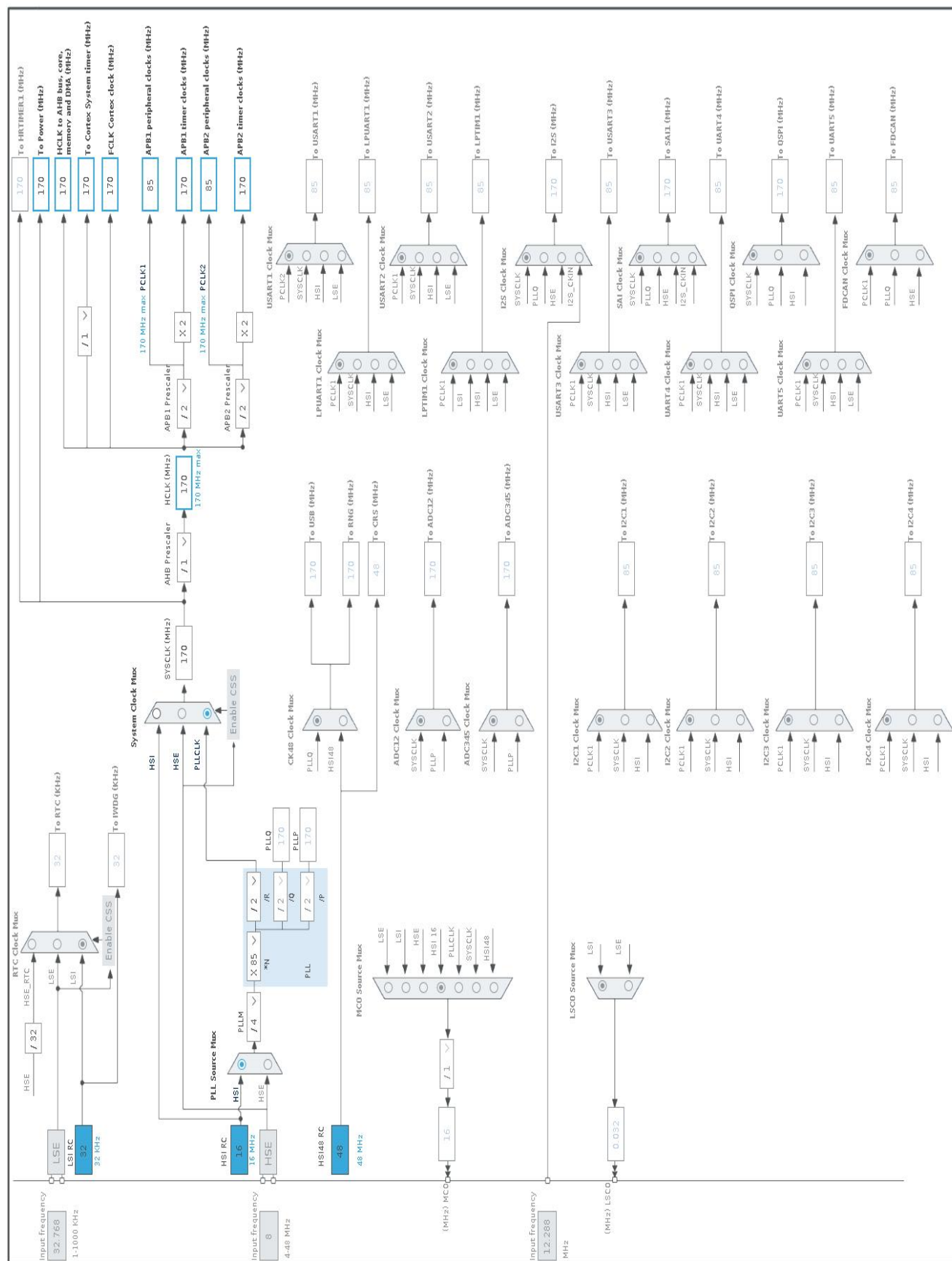


### 3. Pins Configuration

Pin Number UFQFPN48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
21	VDDA	Power		
23	VDD	Power		
25	PB12 *	I/O	FDCAN2_RX	
26	PB13 *	I/O	FDCAN2_TX	
35	VDD	Power		
48	VDD	Power		

\* The pin is affected with a peripheral function but no peripheral mode is activated

## 4. Clock Tree Configuration



## 5. Software Project

### 5.1. Project Settings

Name	Value
Project Name	moteus-fdcantest
Project Folder	/home/josh/tmp/cubemx/moteus-fdcantest
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_G4 V1.1.0

### 5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No

## 6. Power Consumption Calculator report

### 6.1. Microcontroller Selection

Series	STM32G4
Line	STM32G4x4
MCU	STM32G474CEUx
Datasheet	DS12288_Rev0

### 6.2. Parameter Selection

Temperature	25
Vdd	3.0

## **7. IPs and Middleware Configuration**

### **7.1. SYS**

**Timebase Source: SysTick**

**mode: save power of non-active UCPD - deactive Dead Battery pull-up**

**\* User modified value**

## 8. System Configuration

### 8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
Single Mapped Signals	PB12	FDCAN2_RX	Alternate Function Push Pull	No pull-up and no pull-down	Low	
	PB13	FDCAN2_TX	Alternate Function Push Pull	No pull-up and no pull-down	Low	

### 8.2. DMA configuration

nothing configured in DMA service



### 8.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Prefetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
PVD/PVM1/PVM2/PVM3/PVM4 interrupts through EXTI lines 16/38/39/40/41	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
FPU global interrupt	unused		

\* User modified value

## ***9. Software Pack Report***