Meng-Chieh Chen (Jay)

Advisor	Research Topic
Tian-Sheuan Chang	**SNN LLM Accelerator**

I am a graduate student at NYCU's Institute of Electronics, specializing in digital circuit design and AI accelerator research. My project has been recognized at academic conferences. I'm eager to apply my expertise and internship experience to the field of digital IC design.

EXPERIENCES

- **2024 VLSI CAD Conference**
- **2024 APCCAS Conference**

  SNN-Based Transformer Accelerator - VESTA

- **2025 Novatek Summer Internship**

  Digital Design - ASIL Module Optimization

- **EE Camp – General Coordinator**
- **Student Association – Executive Member**

SKILLS & EXPERTISE

- Digital Circuit Design (Verilog / SystemVerilog)
- Circuit Simulation (SPICE)
- Software Engineering (C++ / Python)

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EDUCATION

National Yang Ming Chiao Tung University

2021 – 2024

Department Electronics and Electrical Engineering - GPA: 4.09 (18.82%)

National Yang Ming Chiao Tung University

2025 – Expected: May 2027

Institute of Electronics - GPA: 4.3

RELATED COURSES

- IC Lab – A+
- Computer Organization – A+
- Digital Integrated Circuits – A+
- ML Intelligent Chip Design – A+
- Digital Circuits and Systems – A+
- VLSI Digital Signal Processing – A+