Errata 4th edition

page 404 Eq. 8.11:

dB:

$$\mathrm{SNR} = \frac{V_{in,rms}^2}{kT/C_{hold}} = \frac{(0.4/2\sqrt{2})^2}{4.10^{-21}C_{hold}} = 10^{64/10} \qquad (8.11)$$

So a hold capacitor of $C_{hold} = 0.5$ pF results

C_{hold} has to be in the numerator.

Page 409 above eq. 8.17:

In hold mode, the time-contin
as in Eq. 8.14. As the circuit is
ing and the load capacitor domir
$\omega_{UGBW}C_{hold}/g_m = 1$:

$v_{out2,n}^2 =$

leaving out any transconductance n

ω_{UGBW} C_{hold} /g_m=1, C_{hold} should be C_{load}

p. 439 Eq 9.16

$$\mathrm{SNDR} = 10\,^{10}\log\left(\frac{\hat{A}^2/2}{10^{HD2/10} + kT/C + A_{LSB}^2/12 + \omega_A^2\tau_{jitter}A_{rms}^2}\right) \qquad (9.16)$$

The denominator should read:

$$10^{THD/10}A_{rms}^2 + {KT}/{C} + {A_{LSB}^2}/{12} + \omega_A^2\tau_{jitter}^2A_{rms}^2$$

p. 544, example 11.3:

Example 11.3 (continued)

$$\left(-\frac{V_{REF}}{R_{right}^2}\right)^2 \sigma_{Rright}^2 + \left(-\frac{V_{REF}}{R_a^2}\right)^2 \sigma_{Ra}^2 =$$

$$\frac{I_{tot}}{4R}\left(\sigma_{Rright}^2 + \sigma_{Ra}^2\right) = \frac{I_{tot}}{4}\sqrt{0.0115^2 + 0.02^2} = 0.0058 I_{tot}$$

first line of the equation: remove the '=' sign, second line: add square root sign.
last line of equation should be

$$\sigma_{Iright-Idown} = \left({}^{I_{tot}}/_{4R}\right)\sqrt{\sigma_{Rright}^2 + \sigma_{Ra}^2} = \left({}^{I_{tot}}/_{4}\right)\sqrt{0.0115^2 + 0.02^2} = 0.0058 I_{tot}$$

p. 628:

active and generates two differential currents, based on the input voltages, here: V_{in} and V_{REF}. C_1 and C_2 are discharged during the time period T_{int1}.the most used com The voltages V_1 and V_2 are falling until they reach a level of approximately $V_{DD} - V_{T,M3,4}$. Now the NMOS transistors M_3, M_4 open up and start discharging

remove "the most used com"