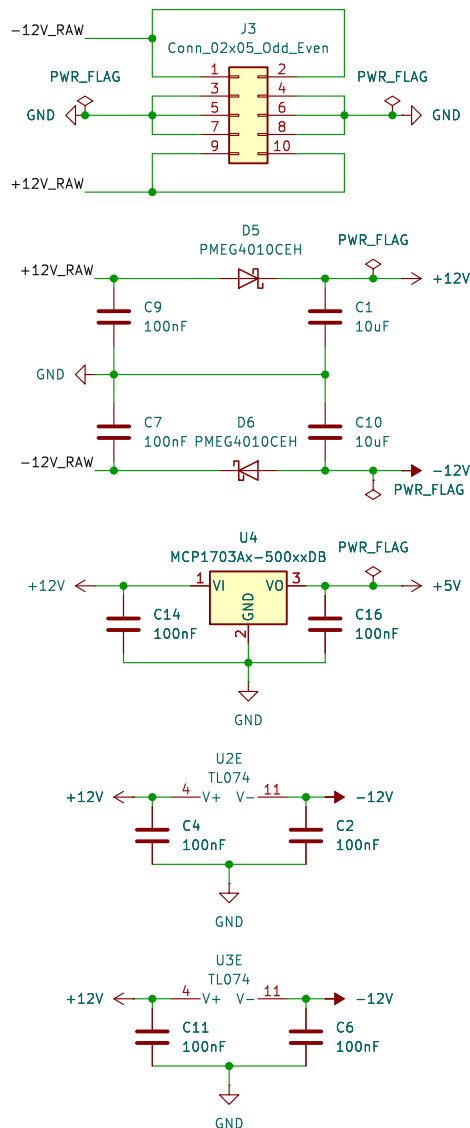
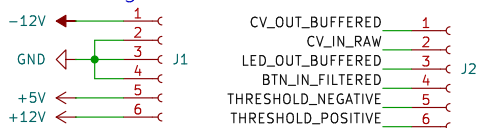


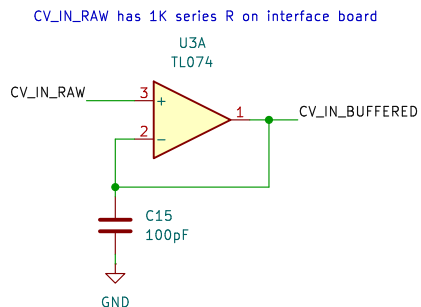
## Power



## Signal Distribution

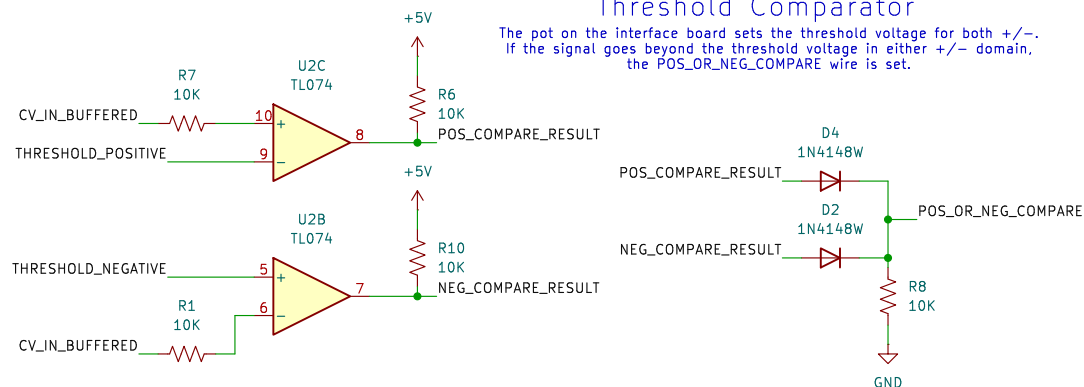


## CV Input

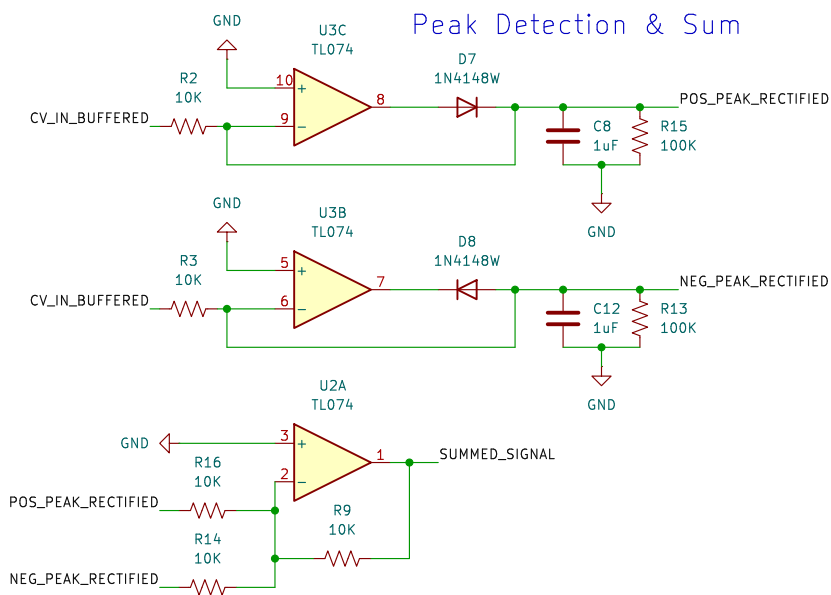


## Threshold Comparator

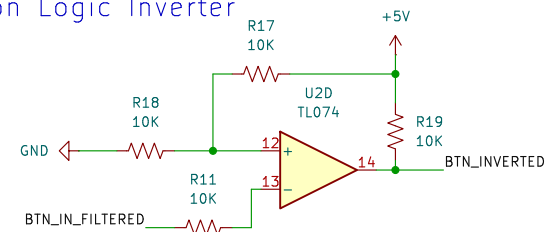
The pot on the interface board sets the threshold voltage for both +/- . If the signal goes beyond the threshold voltage in either +/- domain, the POS\_OR\_NEG\_COMPARE wire is set.



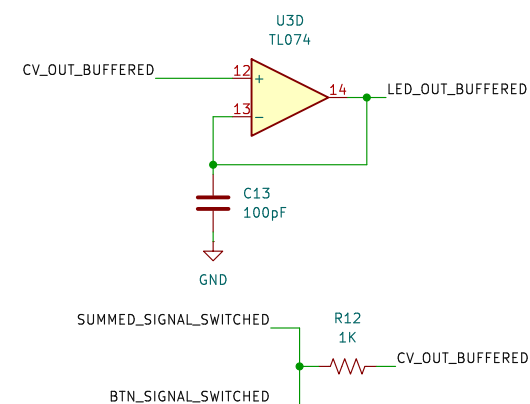
## Peak Detection & Sum



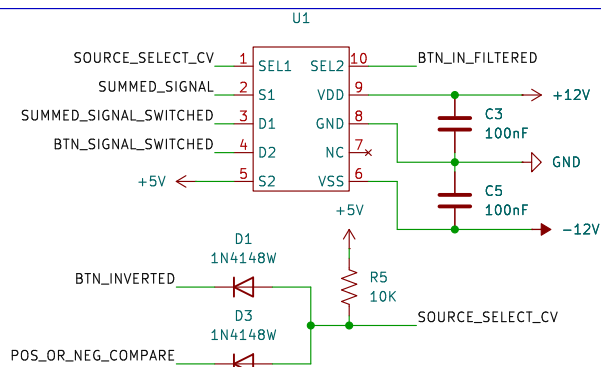
## Button Logic Inverter



## Output



## Switching Logic



Sound Byte Labs

Sheet: /

File: cv-gate.kicad\_sch

**Title: CV/Button Square Wave Converter Logic Board**

Size: A4

Date:

KiCad E.D.A. 9.0.0

Rev:

Id: 1/1