

LNA Design

ECE 432 Microwave Circuit Design II

Jackson Pugh
Michael Woodruff
Portland State University
Portland, OR 97207

Abstract—This report documents the design, simulation, and testing of a 10dB Low Noise Amplifier (LNA) suitable for operation between 2.4GHz and 2.6GHz using the SAV-541+ E-PHEMT transistor. The design was completed and simulated in Agilent's Advanced Design System software. Good results were achieved in simulation over the required frequency range. However, when constructed and tested, the amplifier was determined to unstably oscillate instead of amplifying. Possible reasons for this are discussed.

I. INTRODUCTION

Low Noise Amplifiers are useful when attempting to amplify a signal that is very close to the noise floor, such as a broadcast signal. The particular application for the amplifier discussed in this report was an FSK receiver using 2.4GHz and 2.6GHz signals. The LNA was to be connected to an impedance matching network and antenna on the input side. The output was to drive a frequency detector that determine which of the two frequencies was being received. Both of these circuits are beyond the scope of this report but serve to frame the context in which the LNA was designed for. LNA characteristics that of primary concern when designing for such an application are the gain, noise figure, and return loss. These are generally specified for an LNA prior to design as they have the most significant impact on performance. Other design constraints considered for this LNA were supply voltage and maximum power consumption.

The LNA varies from the other type of amplifier covered in ECE432, the Power Amplifier (PA), in that it is designed to take a very small signal that is relatively close to the Noise Floor and boost it to a level that can be processed. The PA on the otherhand is designed to take a signal that is at a reasonable level for processing and boost it to a level suitable for transmission. This is an important distinction to remember when choosing the elements of an LNA. The Noise Figure of the active elements are of particular concern as this noise will be added to the incoming signal during amplification. The other elements in the circuit will also add noise to the design. Even non-reactive components such as resistor add noise to the LNA. Assuming that a suitable active element is selected, care should then be used to select passive components that are suitable for microwave LNA applications.

II. LNA DESIGN

The LNA design is comprised of Minicircuit's low noise SAV-541+ transistor. Consideration of a suitable bias network is required for gain and stability. Looking at the SAV-541+

datasheet[2], a good place to bias the transistor is $I_{DS} = 60$ mA at $V_{DS} = 3$ V. The recommended application circuit provided in the datasheet is used in the design. The BJT current mirror helps draw I_{DS} to 60 mA. The supply power was chosen to be 3.7 V (instead of 3.3 V) due to the 0.7 V drop from the BJT current mirror.

Table I lists the design constraints for the LNA discussed in this report. These were designed for with the sole exception of the supply current. This was increased as noted above to account for the voltage drop due to the active biasing network used.

Figure 1 shows the bias network for the transistor circuit. Figure 2 shows the DC measurements of the parameters of interest. Figure 3 shows the S-Parameter comparison between the bias network circuit and the manufacturer S2P data. The bias network shows little discrepancy in simulation from the datasheet's assertion of 60mA at 3V_{DS}. 2N3906 PMOS transistors were used for the model and the actual biasing circuit. These are common PMOS transistors that are cheap and readily available. Their frequency characteristics are not great but the DC biasing inductors should attenuate the high frequency signals on the gate and drain enough to allow for proper operation. The authors' only concern is that the values used in simulations were 100nH while the largest ones available when assembling the circuit were 22nH.

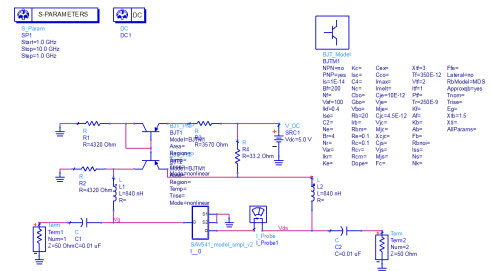


Fig. 1. DC bias network for the SAV-541+ transistor.

Figure 4 shows various information provided by the ADS

TABLE I. LNA DESIGN CONSTRAINTS

Frequency	2.4 to 2.6	(GHz)
Gain (min)	10	(dB)
Noise Figure (max)	2.5	(dB)
Input Return Loss (min)	10	(dB)
Output return Loss (min)	10	(dB)
Supply Voltage	3.3	(V)
Supply Current (max)	100	(mA)

freq	I_Probe1.i	Vds	Vg
0.0000 Hz	-60.37 mA	2.991 V	502.7 mV

Fig. 2. Current and voltage parameters from the DC bias network for the SAV-541+ transistor.

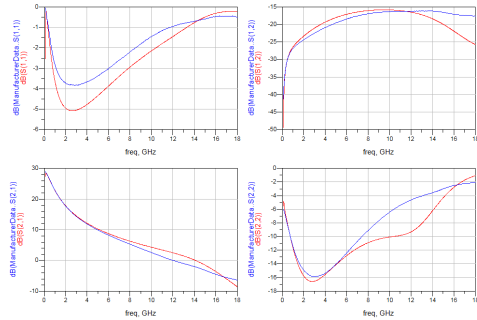


Fig. 3. S-Parameter simulation result comparison between the bias network and the manufacturer S2P data file.

amplifier design guide tool. Observing the Stability Factor, K (μ_{source}/μ_{load}) graph, the circuit is potentially unstable below around 4 GHz. Thus, stabilizing resistors will be necessary in order to make the transistor unconditionally stable between 2.4 - 2.6 GHz (the intended frequency range).

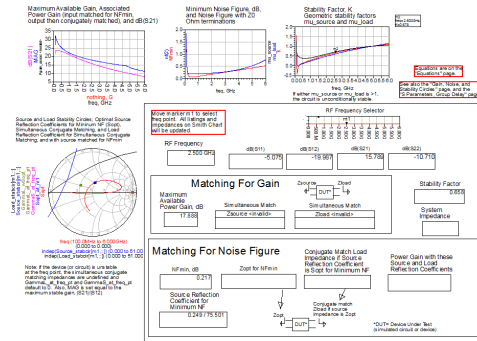


Fig. 4. Amplifier design guide generated in ADS for the bias network for the SAV-541+ transistor.

Figure 5 shows added feedback to the transistor used to stabilize it unconditionally (under 3 GHz). After tweaking the circuit for stability, Figure 6 shows the Design Guide for the stabilized circuit. It shows the circuit is unconditionally stable with a gain of 12.225 dB.

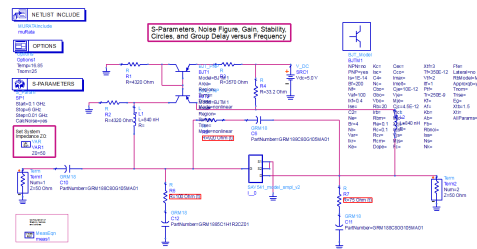


Fig. 5. Stabilized transistor bias circuit for the SAV-541+ using feedback and stabilizing resistors.

The circuit now needs transmission line in order to be able

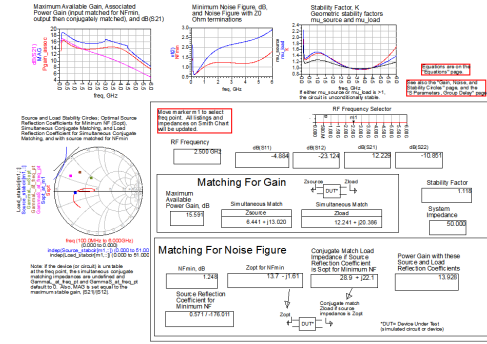


Fig. 6. Amplifier design guide generated in ADS for the bias network for the SAV-541+ transistor.

to manufacture the board. As expected, adding transmission line to the circuit caused the transistor to become potentially unstable. Thus, it was required to tune the feedback components again. Figure 7 shows the finalized stabilizing circuit for the SAV-541+ transistor. Figure 8 shows the design guide plots and shows the gain is above 10 dB.

This circuit was mechanically routed on what was assumed to be FR4, this was however not the case. After assembly the LNA was analyzed using a PNA and found not produce any gain. The circuit was then powered with a bench supply and it was noticed that the current draw of the biasing network varied significantly with time. Further analysis was conducted using the PNA and it was determined that the amplifier was oscillating at various frequencies. The matching network and feedback network was checked to ensure that the proper values had been used. No errors were found. Despite our best efforts this design was never observed to provide stable gain.

Due to the extended period of time that was spent working on the LNA, several other labs were significantly delayed. The amount of work piling up for the FSK project lead to abandoning the LNA's development. A MMIC Amplifier was used instead to good effect.

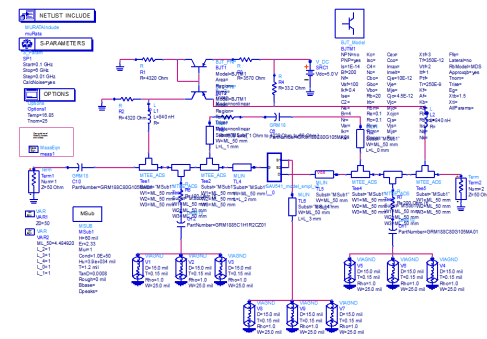


Fig. 7. Final stabilizing circuit for the SAV-541+ transistor.

Figure 9 shows the layout which will be used to manufacture the physical board.

III. CONCLUSION

In summary, an LNA designed to the specifications given in Table I was designed, simulated, and built. The design and simulation of the LNA were consistent with expectations. The

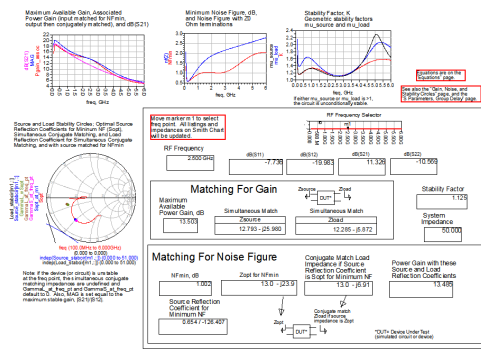


Fig. 8. ADS design guide simulation results for the stabilizing circuit for the SAV-541+ transistor.

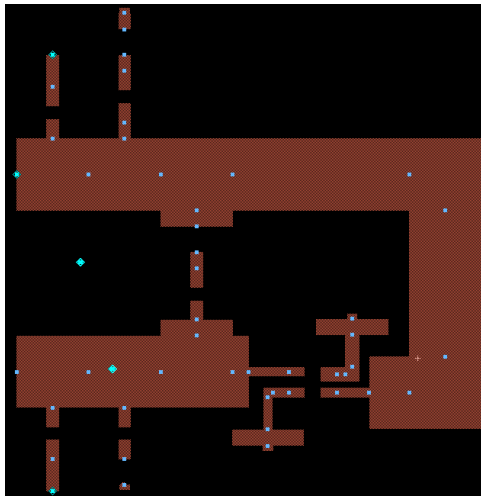


Fig. 9. SAV-541+ transistor layout.

assembled LNA was inconsistent with both the simulations and the designer's expectations. The root cause of this inconsistency was not isolated despite attempts to do so. The authors' working assumption is that the substrate used had a significant variation from typical FR4 in terms of E_r and that it was this variation that caused the instability. Other possibilities include mismarked components or damaged components.

Lessons learned during this project included the importance of properly selecting and verifying all circuit elements used at microwave frequencies and the importance of physical testing as a means to augment computational modeling. While the authors' currently theorize that the oscillations were caused by variations in the circuit elements used in assembly, it is also possible that the issue was due to ADS's simulation providing erroneous results. Thus by testing a physical prototype, the designer can be assured that the models used accurately predict the behavior of the circuit designed.

Despite the eventual failure of the LNA designed for this project, the authors are confident that if presented with another LNA design task that a stable LNA could be implemented to reasonable specifications. This project provided valuable insight into the simulation process and how ADS works. Additional insight was gained into the process of prototyping microwave circuits with a mechanical circuit router.

REFERENCES

- [1] K. Payne, "Practical RF Amplifier Design Using the Available Gain Procedure and the Advanced Design System EM/Circuit Co-Simulation Capability," Agilent Technologies (5990-3356EN), 2008.
- [2] Minicircuit SAV-541+ Datasheet