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THIS DRAWING HAD BEEN GENERATED AND IS MAINTAINED BY A CAD SYSTEM. CHANGES SHALL ONLY BE INCORPORATED AS DIRECTED BY THE GOVERNMENT DESIGN ACTIVITY

NOTES:
1. INTERPRET DRAWING PER ASME Y14.100
2. UNLESS OTHERWISE SPECIFIED, ALL RESISTORS VALUES ARE IN OHMS, CAPACITORS ARE MICROFARADS, INDUCTORS ARE MICROHENRIES.
3. ON & OFF SHEET CONNECTOR REPRESENT ORIGIN AND DESTINATION, NOT NECESSARILY THE DATA FLOW
4. CAD MAINTAINED. CHANGES SHALL BE INCORPORATED BY THE DESIGN ACTIVITY.

REVISIONS

ZONE	REV	DESCRIPTION	DATE	APPROVED
	—			

RED INTERFACE SCHEMATICS

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JTT-X

APPLICATION

CONTRACT NO.
TBD

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DIMENSIONS ARE IN MM
TOLERANCES ARE:
FRACTIONS DECIMALS ANGLES
NA NA NA

DO NOT SCALE DRAWING

MATERIAL

FINISH

DESIGN

CHECKED

DESIGN

ENGINEERING

PROJECT

QUALITY ASSURANCE

MANUFACTURING

DATE

ELECTRONIC
SIGNATURES
ON FILE

SIZE
D

CAGE CODE
0CDN3


DWG NO
112302-01003

FILE NAME
112302-01003

10/03/2025:09:22

SHEET
1 OF 11

REV
0.7

DRS Advanced ISR, LLC
2601 MISSION POINT BLVD.SUITE 100
BEAVERCREEK, OH 45431 (937)429-7408

TITLE
SCHEMATIC DIAGRAM
RED INTERFACE

8

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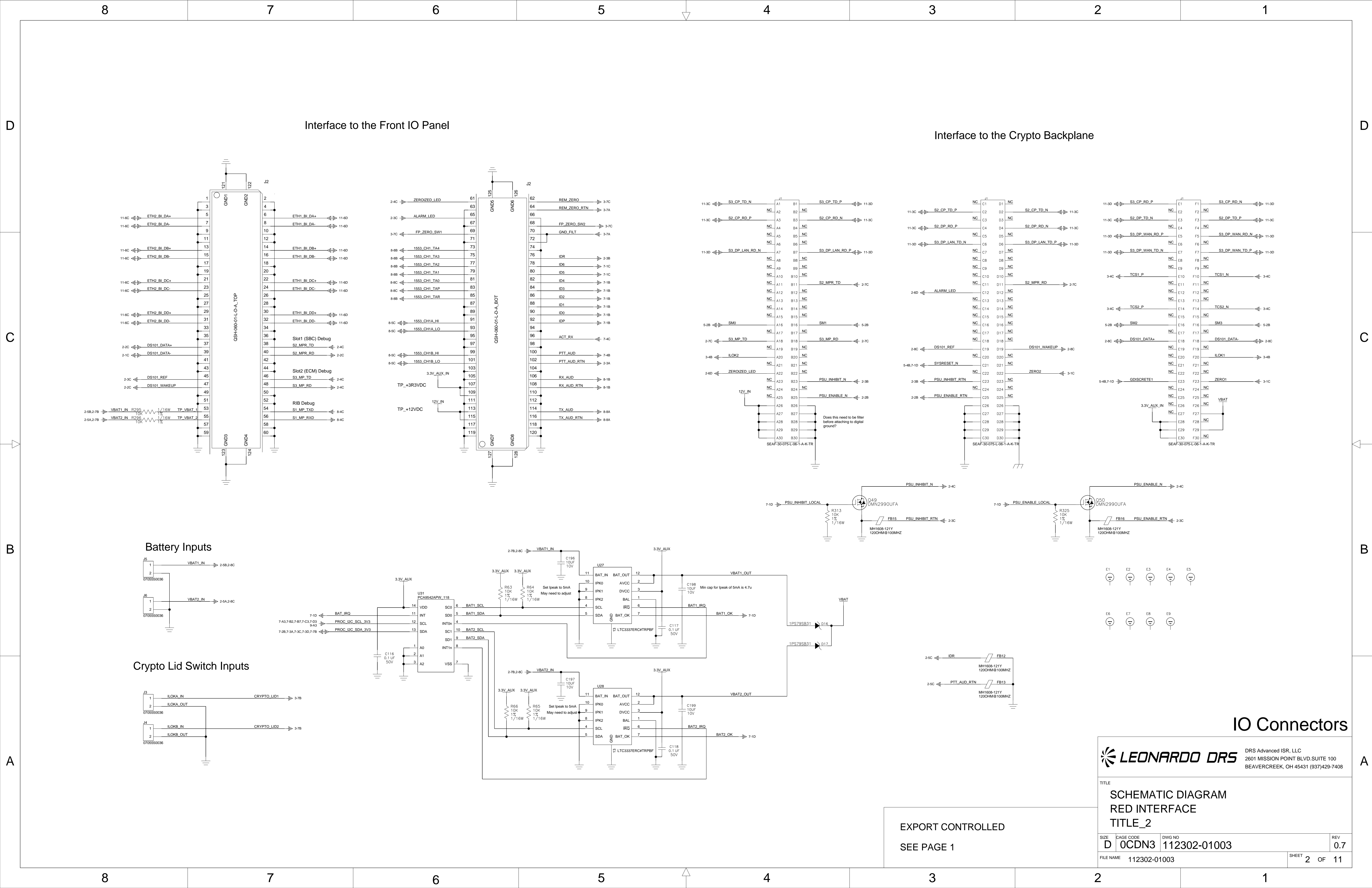
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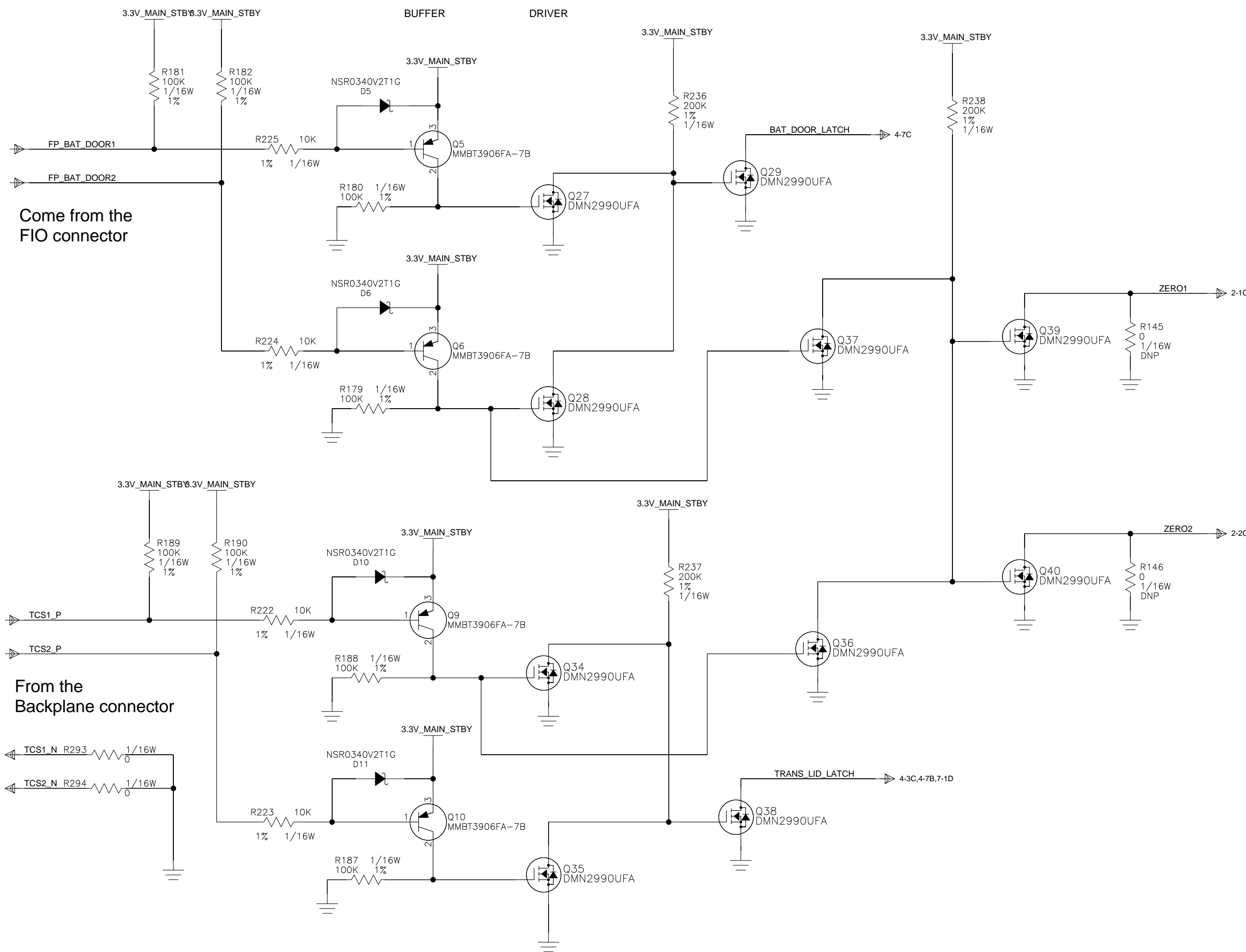
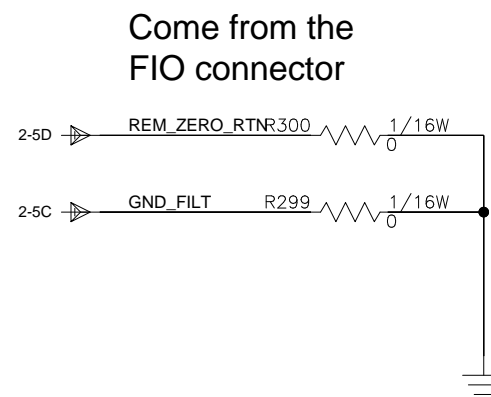
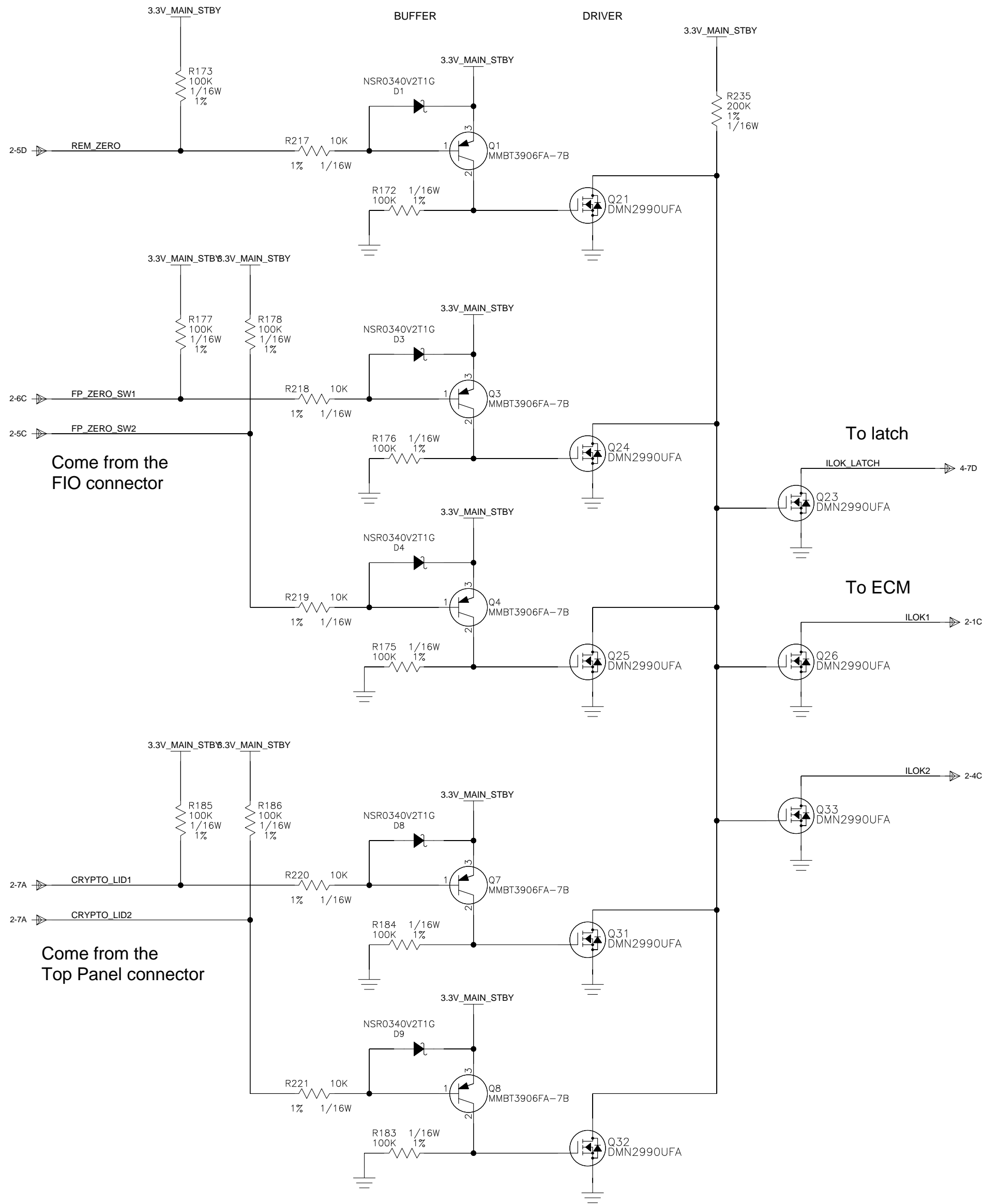
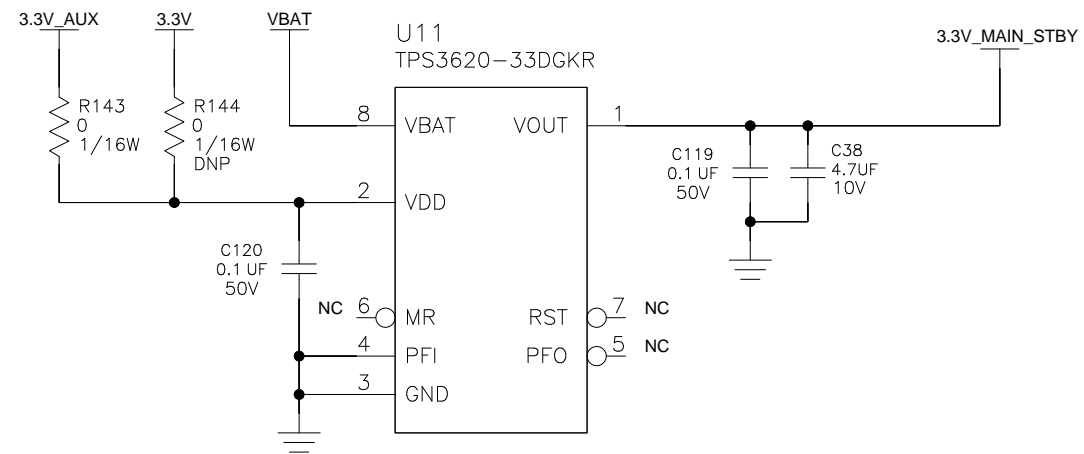
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PADs VX2.1.3



PLATFORM POWER SWITCHING



Security Circuits



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TITLE

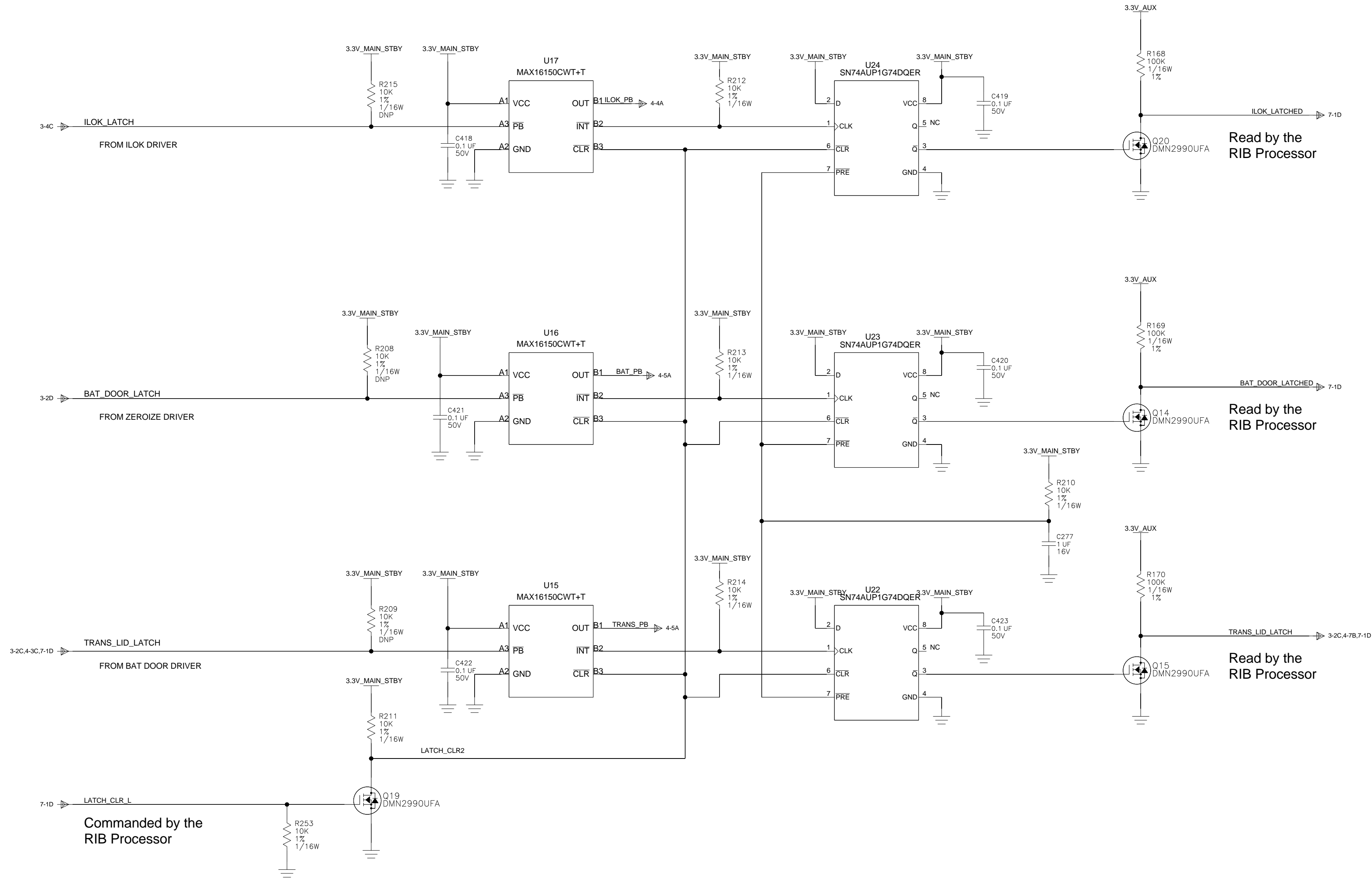
SCHEMATIC DIAGRAM
RED INTERFACE
TITLE_2

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FILE NAME			SHEET
112302-01003			3 OF 11

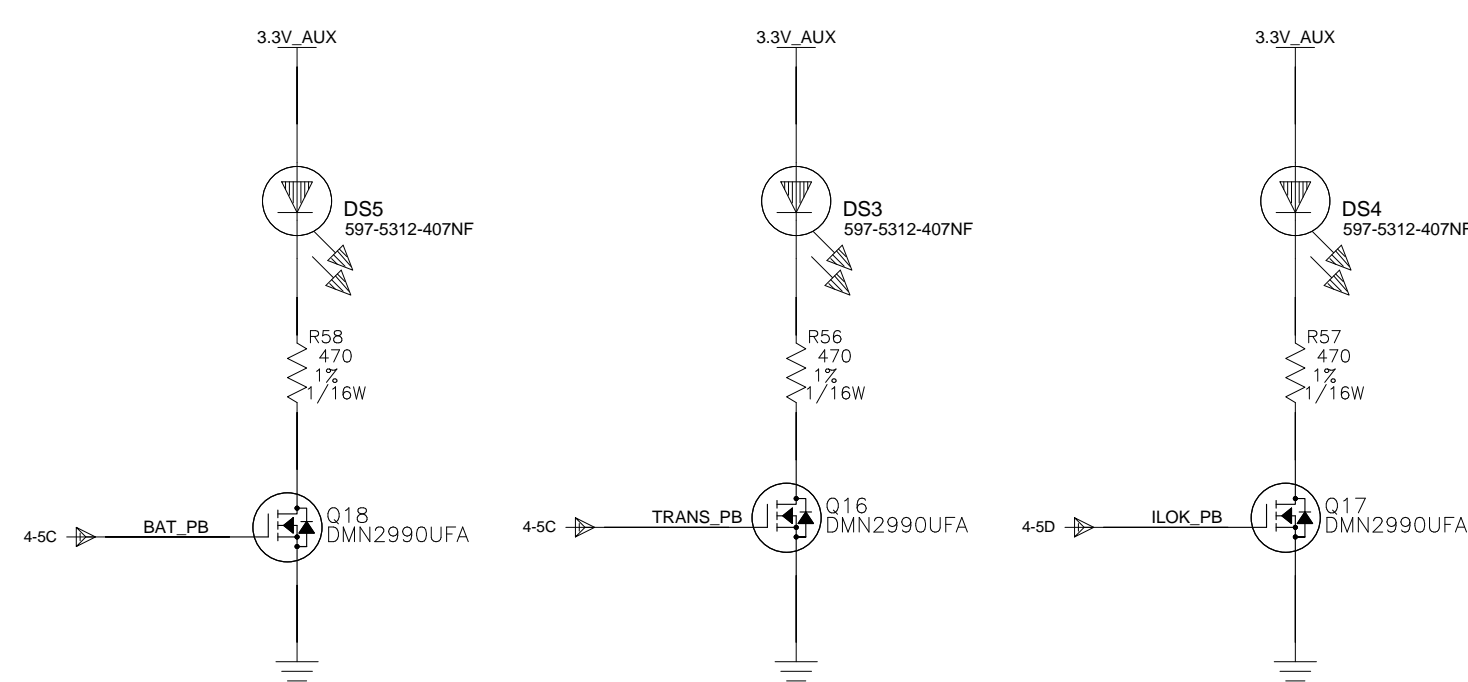
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Status Latches



LED Status Indicators



Security Latches

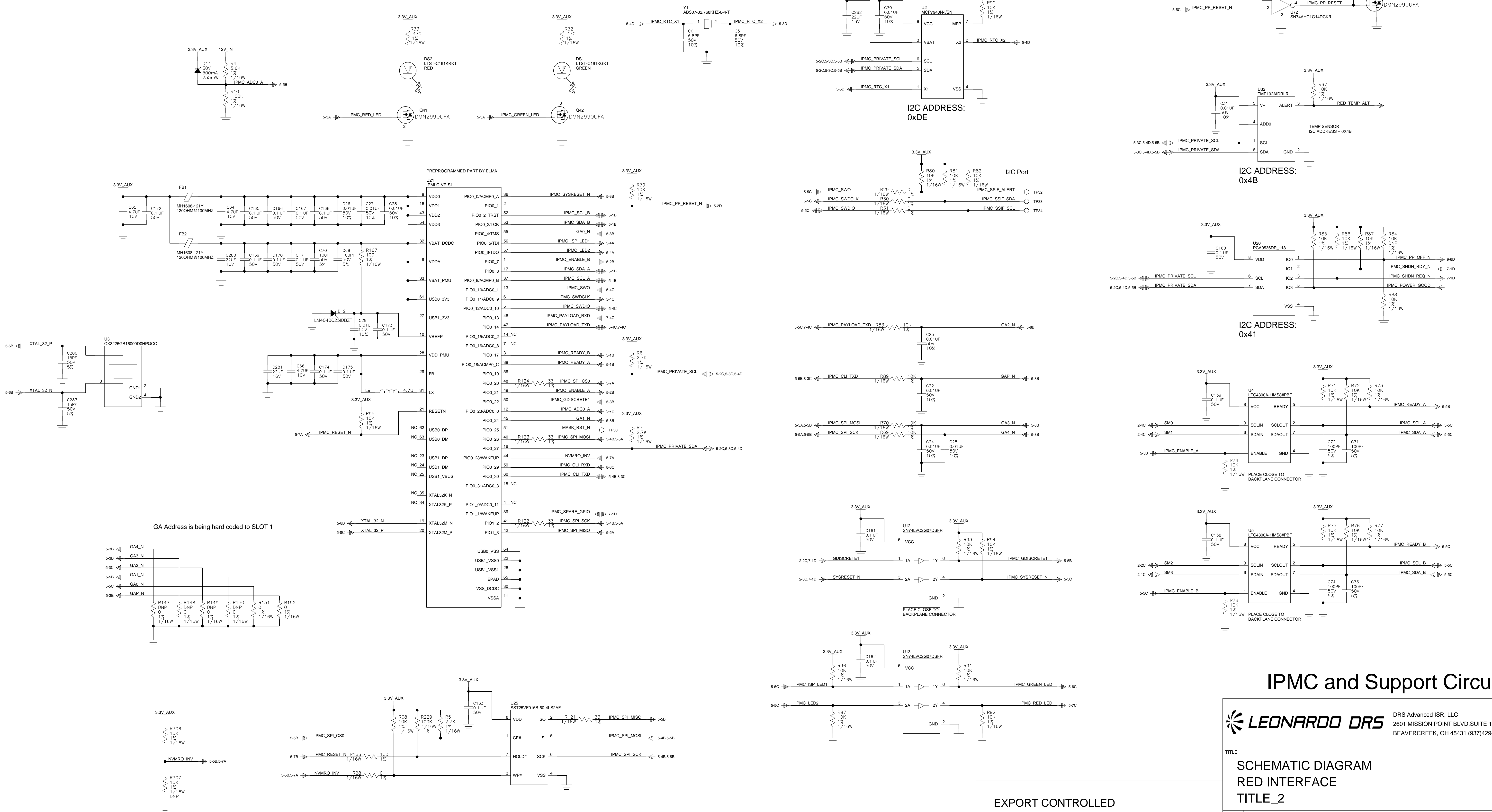
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TITLE SCHEMATIC DIAGRAM RED INTERFACE TITLE_2			
SIZE D	CAGE CODE 0CDN3	DWG NO 112302-01003	REV 0.7
FILE NAME 112302-01003		SHEET 4 OF 11	

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How does IPMC get programmed if it is a blank device?

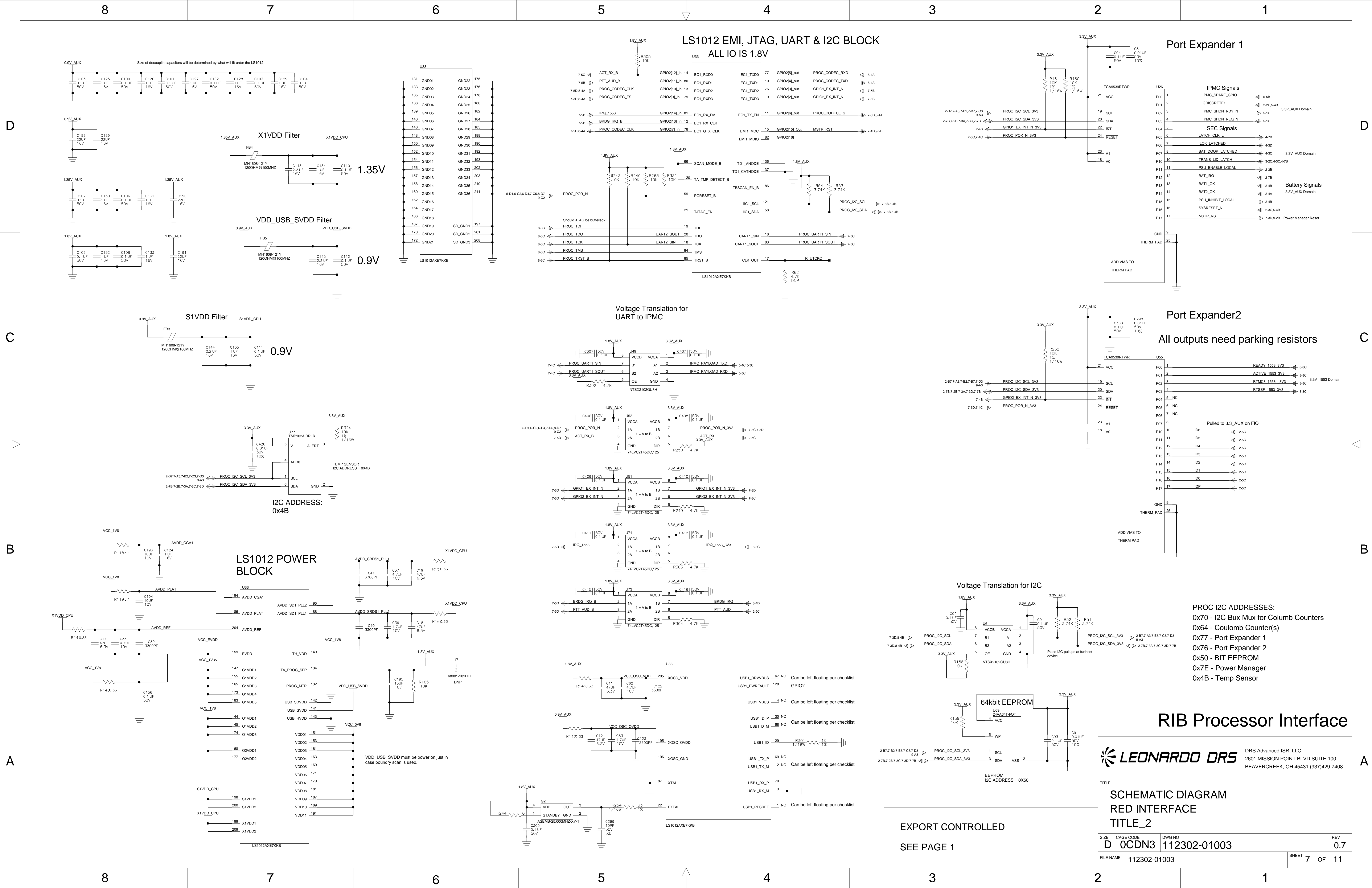
IPMI-C-VP-S1

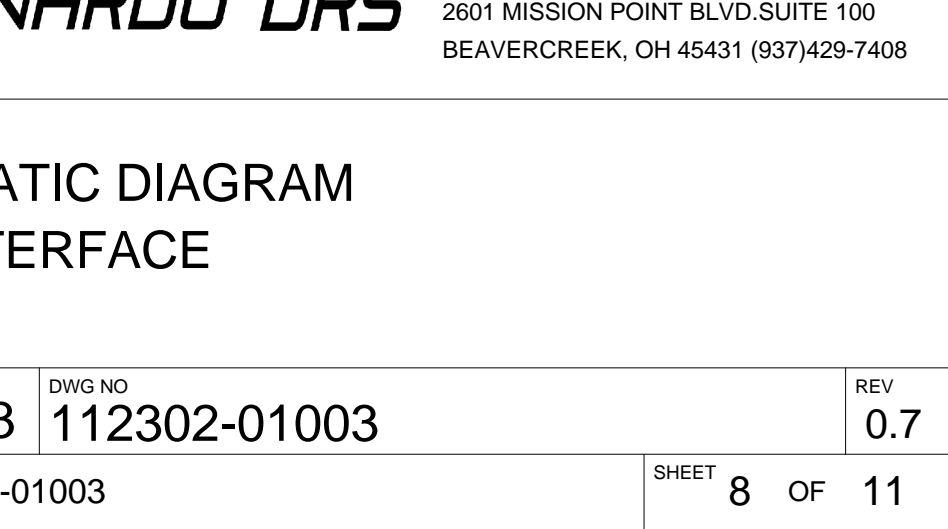
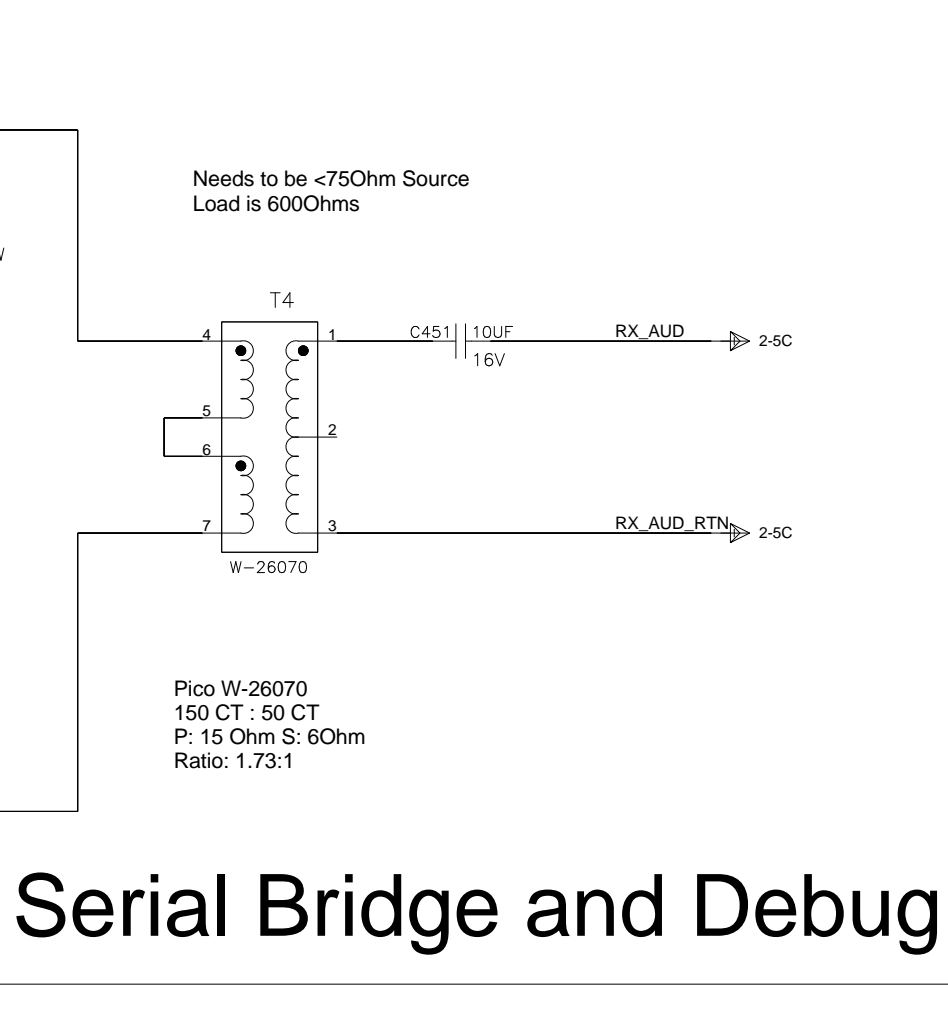
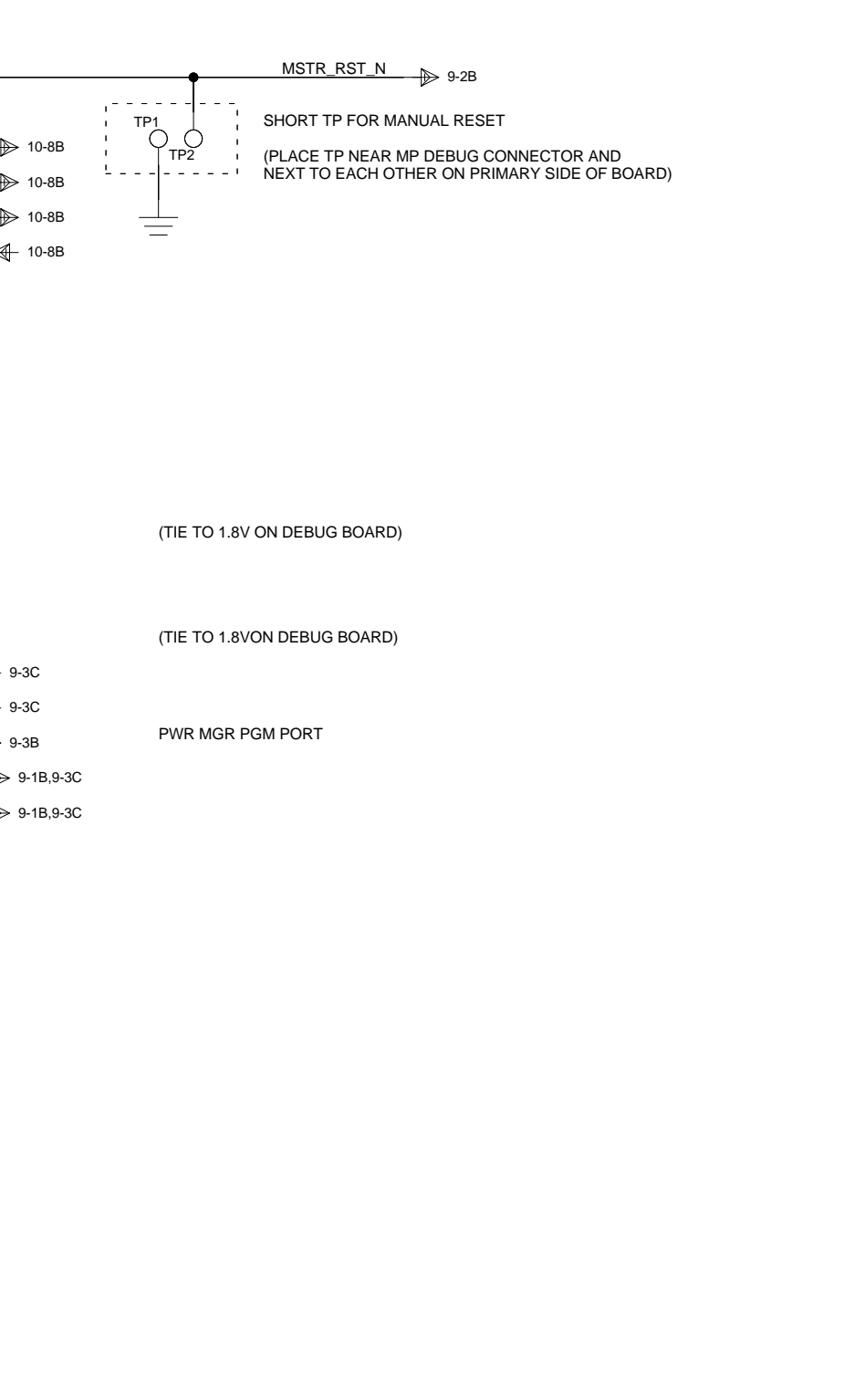
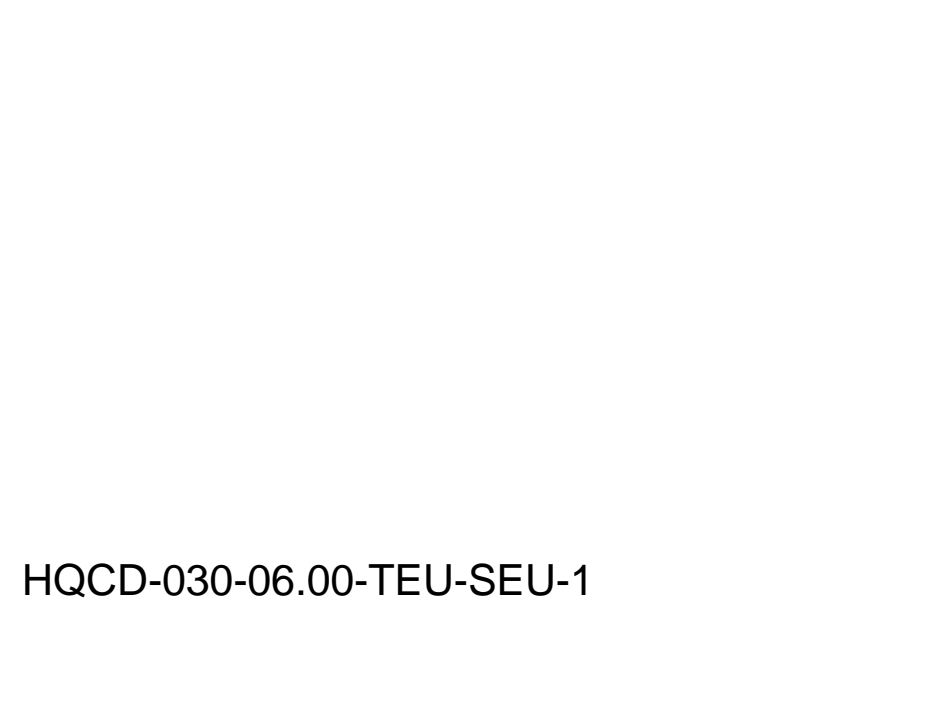


IPMC and Support Circuits

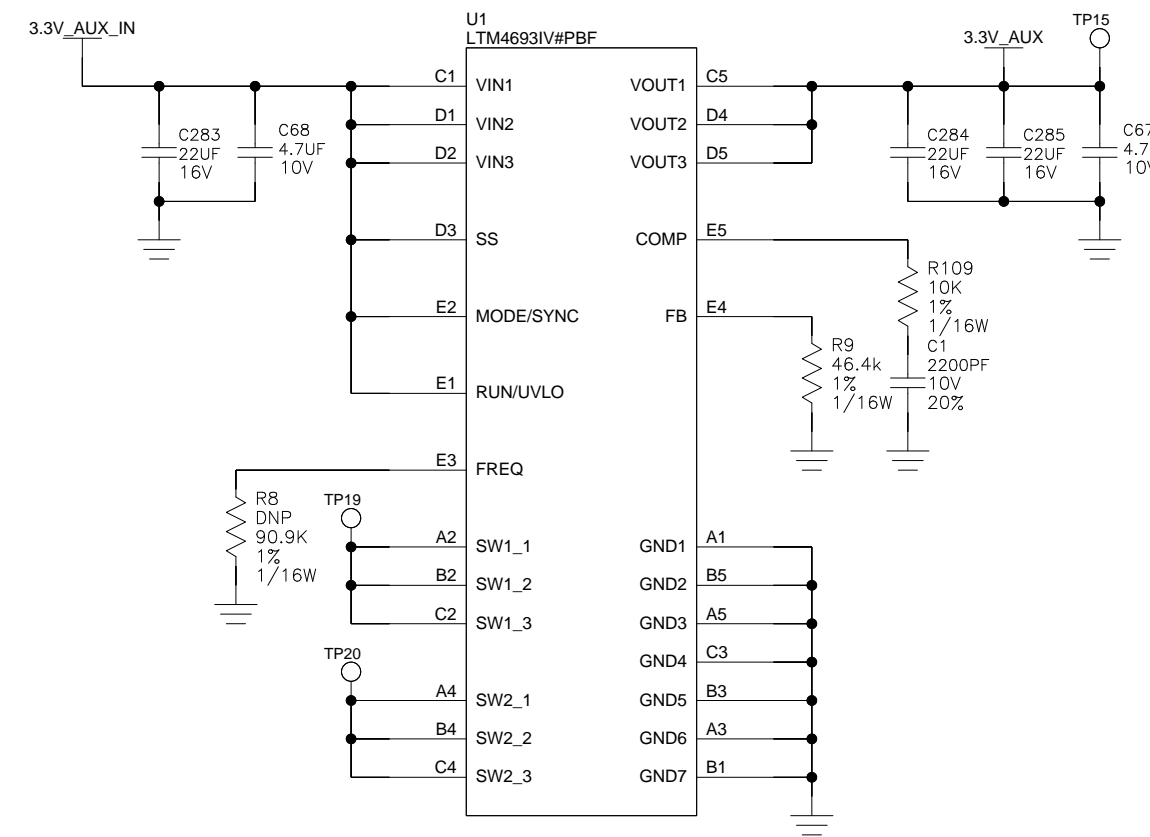
DRS Advanced ISR, LLC 2601 MISSION POINT BLVD.SUITE 100 BEAVERCREEK, OH 45431 (937)429-7408			
TITLE SCHEMATIC DIAGRAM RED INTERFACE TITLE_2			
SIZE D	CAGE CODE OCDN3	DWG NO 112302-01003	REV 0.7
FILE NAME 112302-01003			SHEET 5 OF 11

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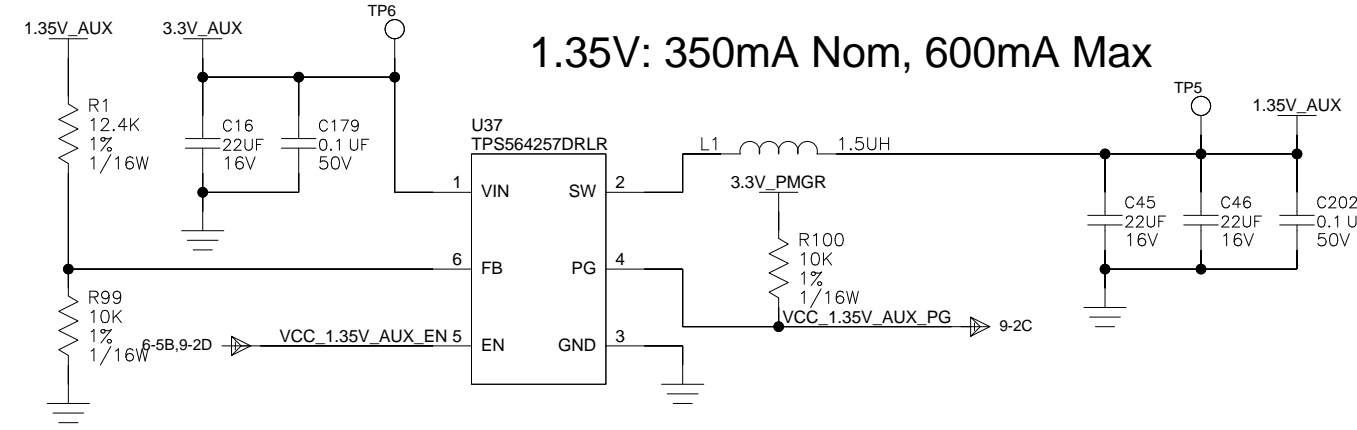
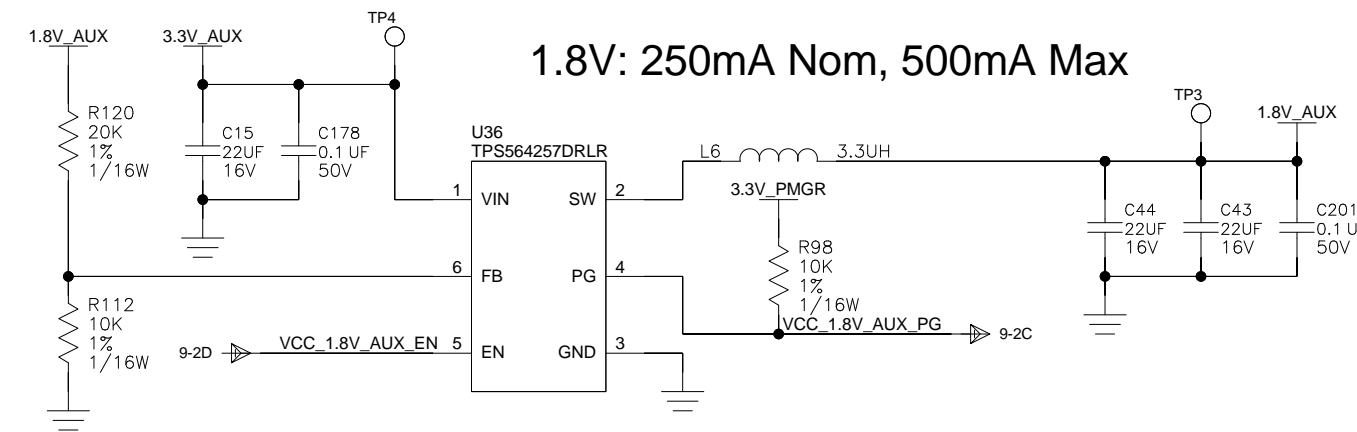
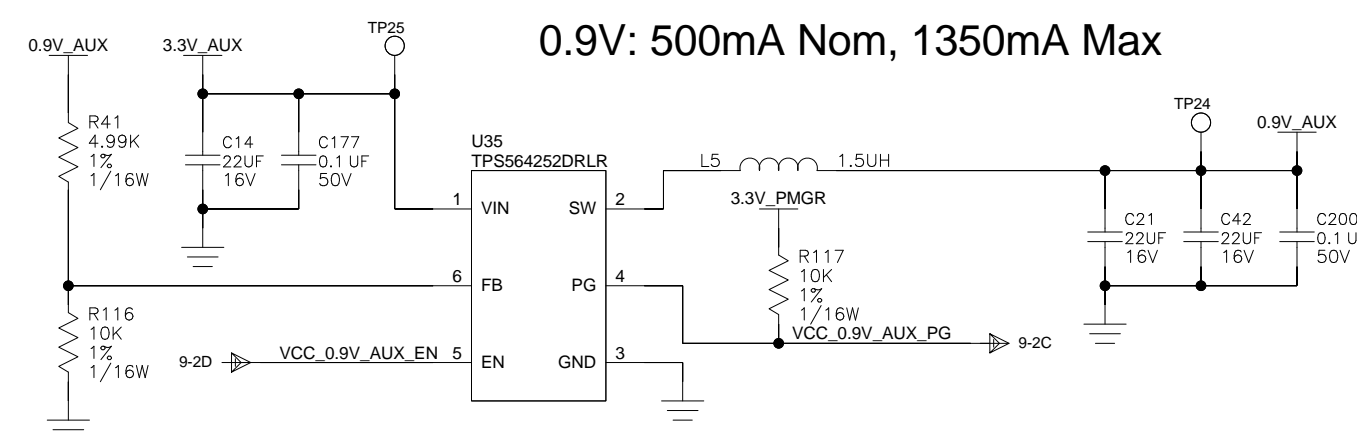




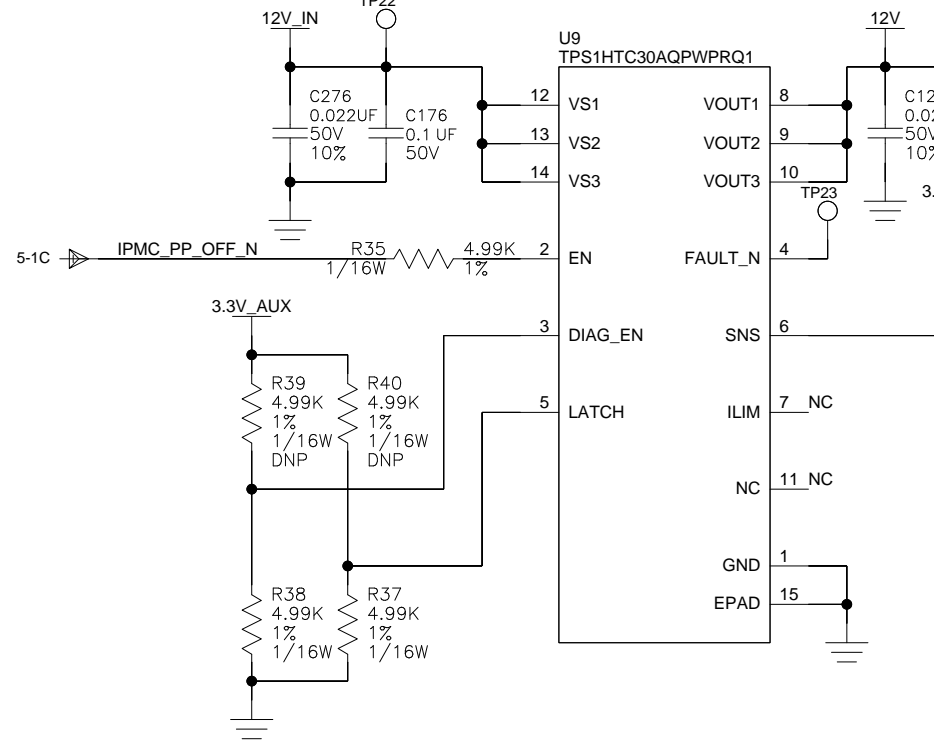
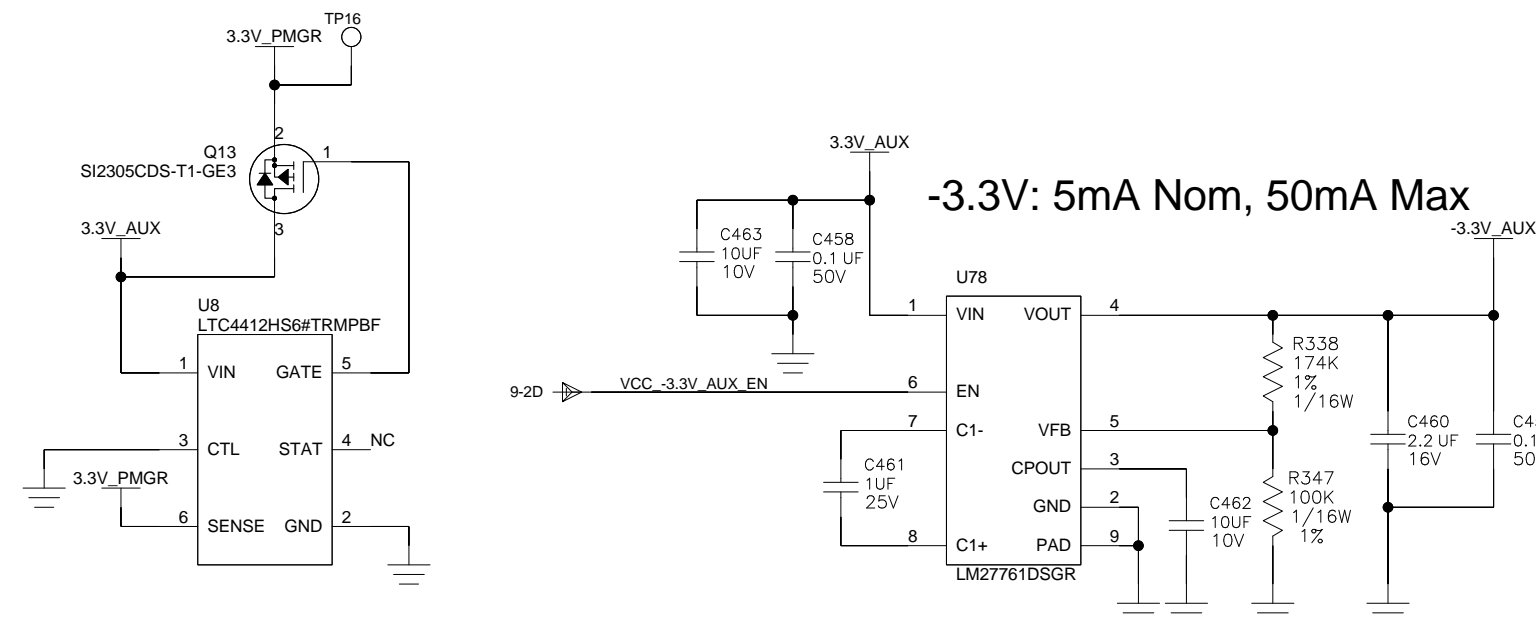
IPMC CONTROLLED 12V ON/OFF SWITCH



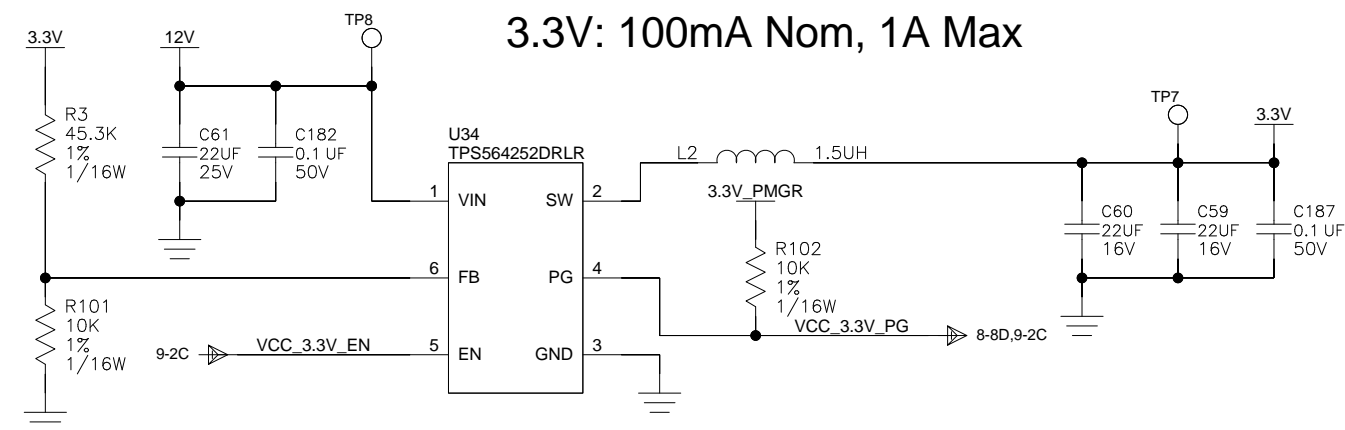
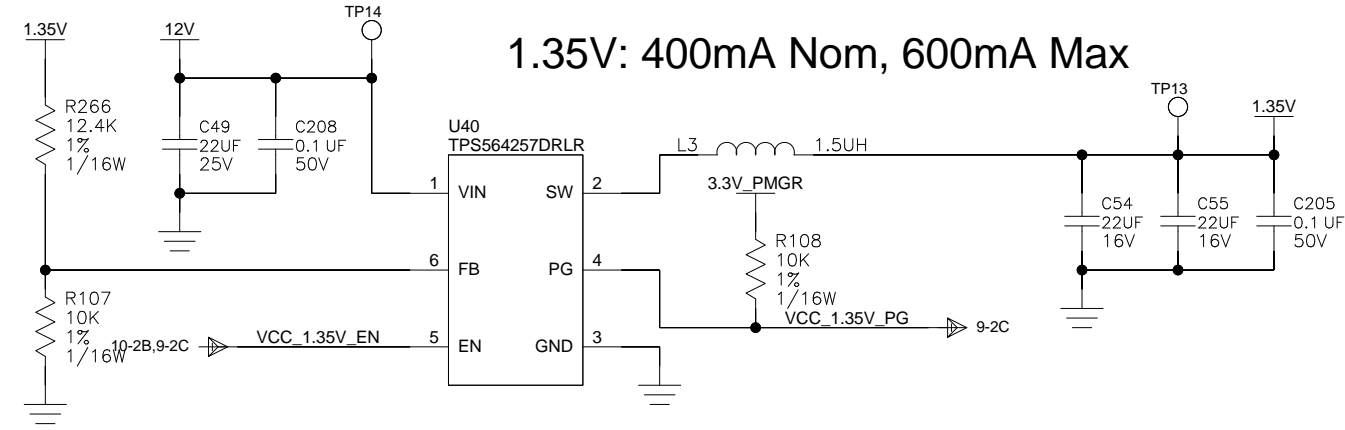
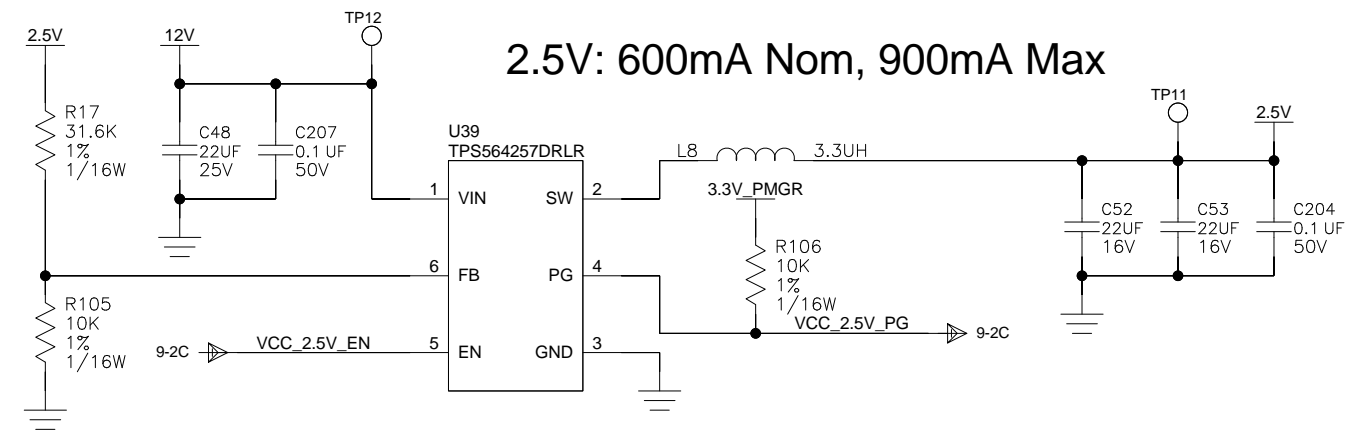
3.3V_AUX Input Regulators



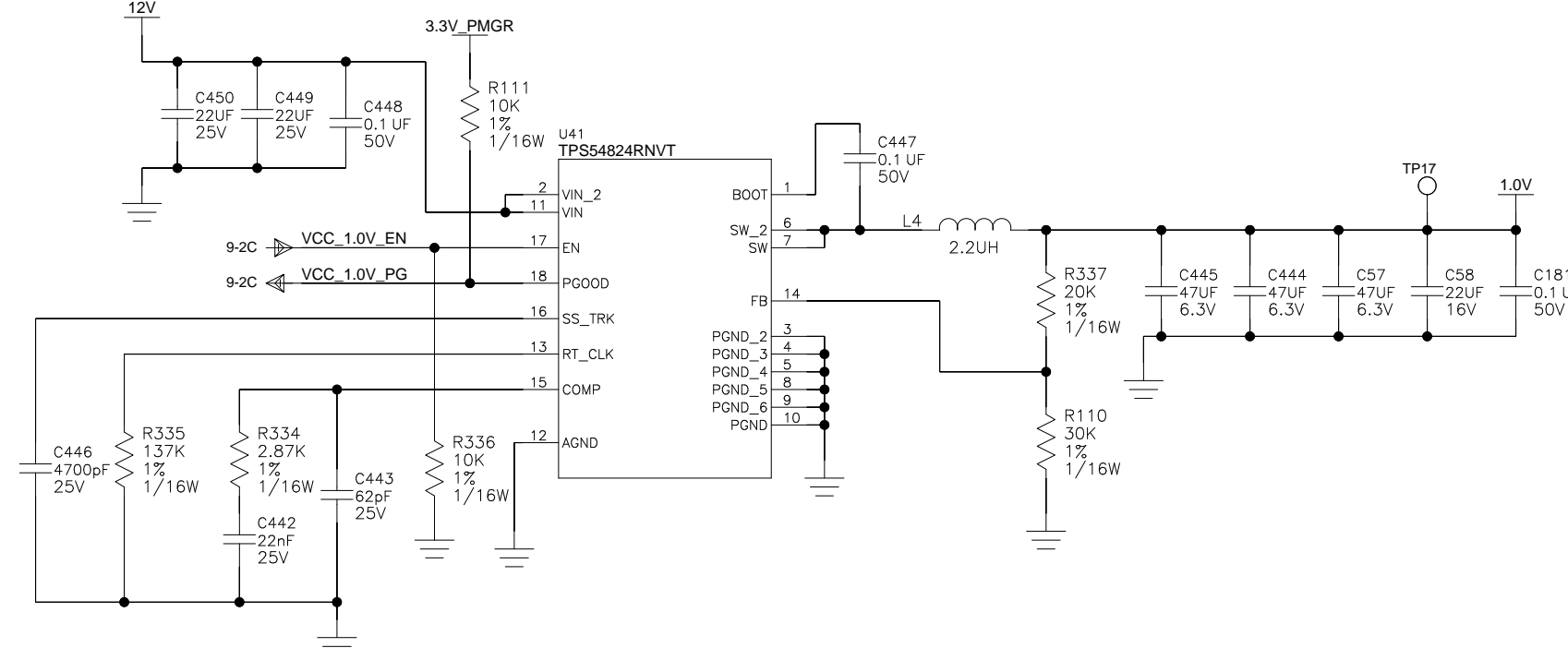
Allows the PMIC to be programmed
with primary power.
Needed?



12V Input Regulators

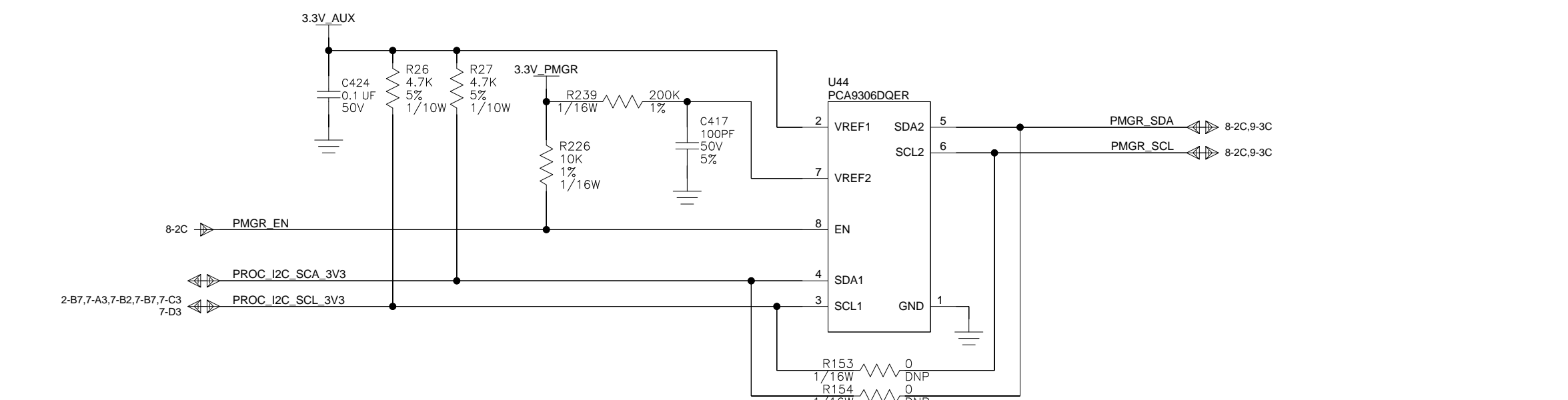
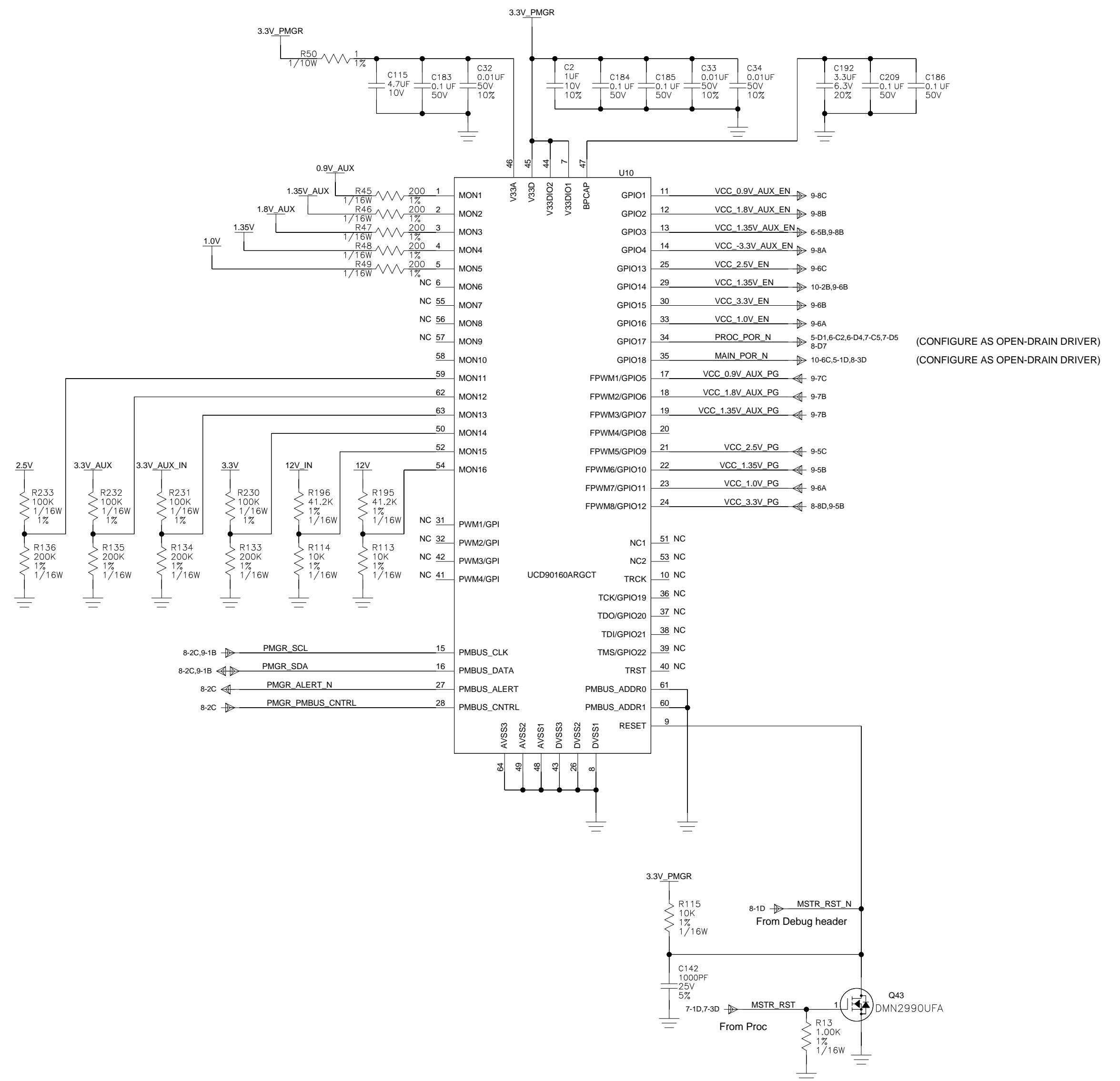


1.0V: 1.8A Nom, 3.0A Max



We have two power on sequences::

- 1) 3.3V_AUX is applied and it's rails come alive
- 2) IMPC releases 12V rail and remaining rails start



Board Power



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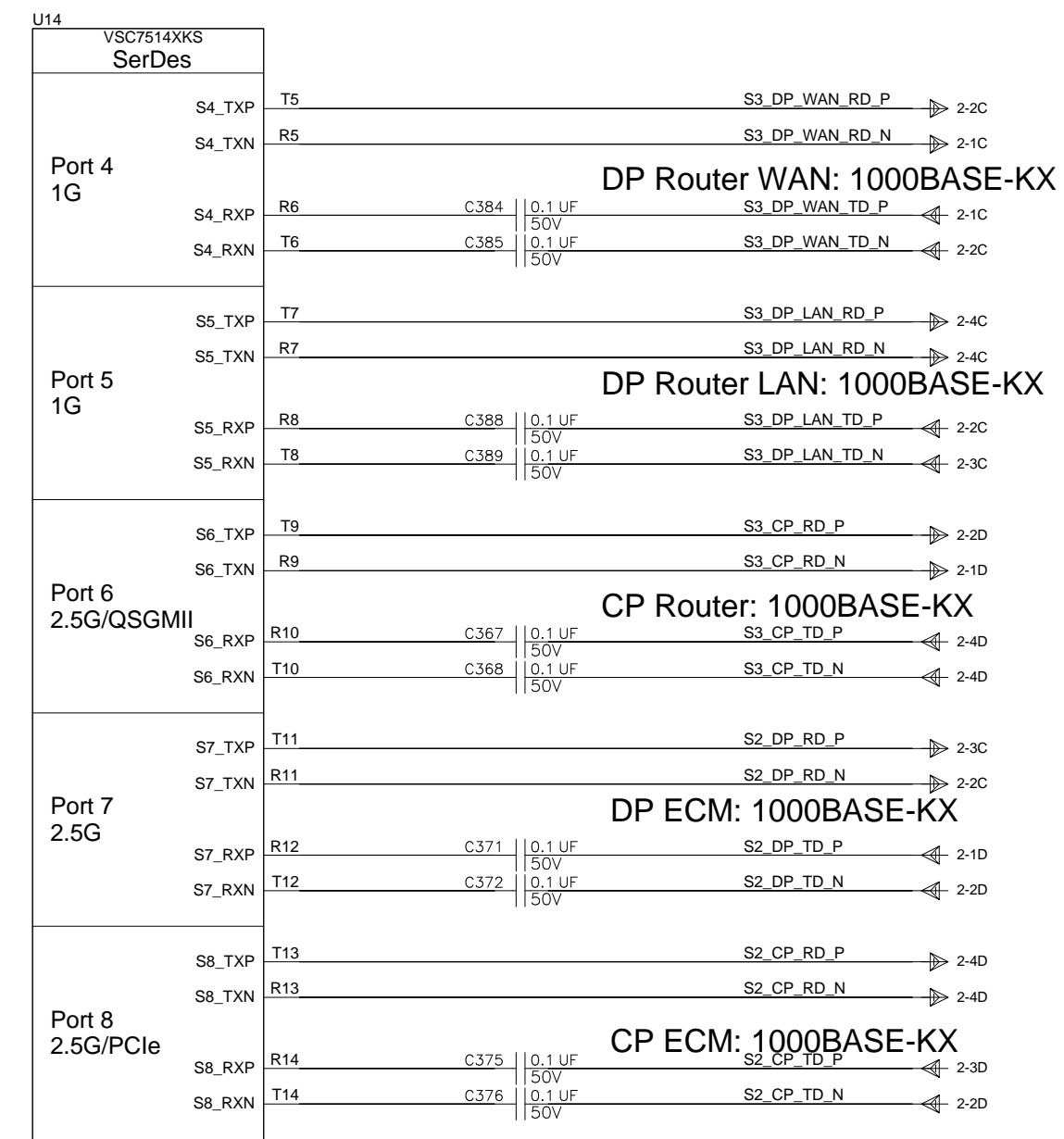
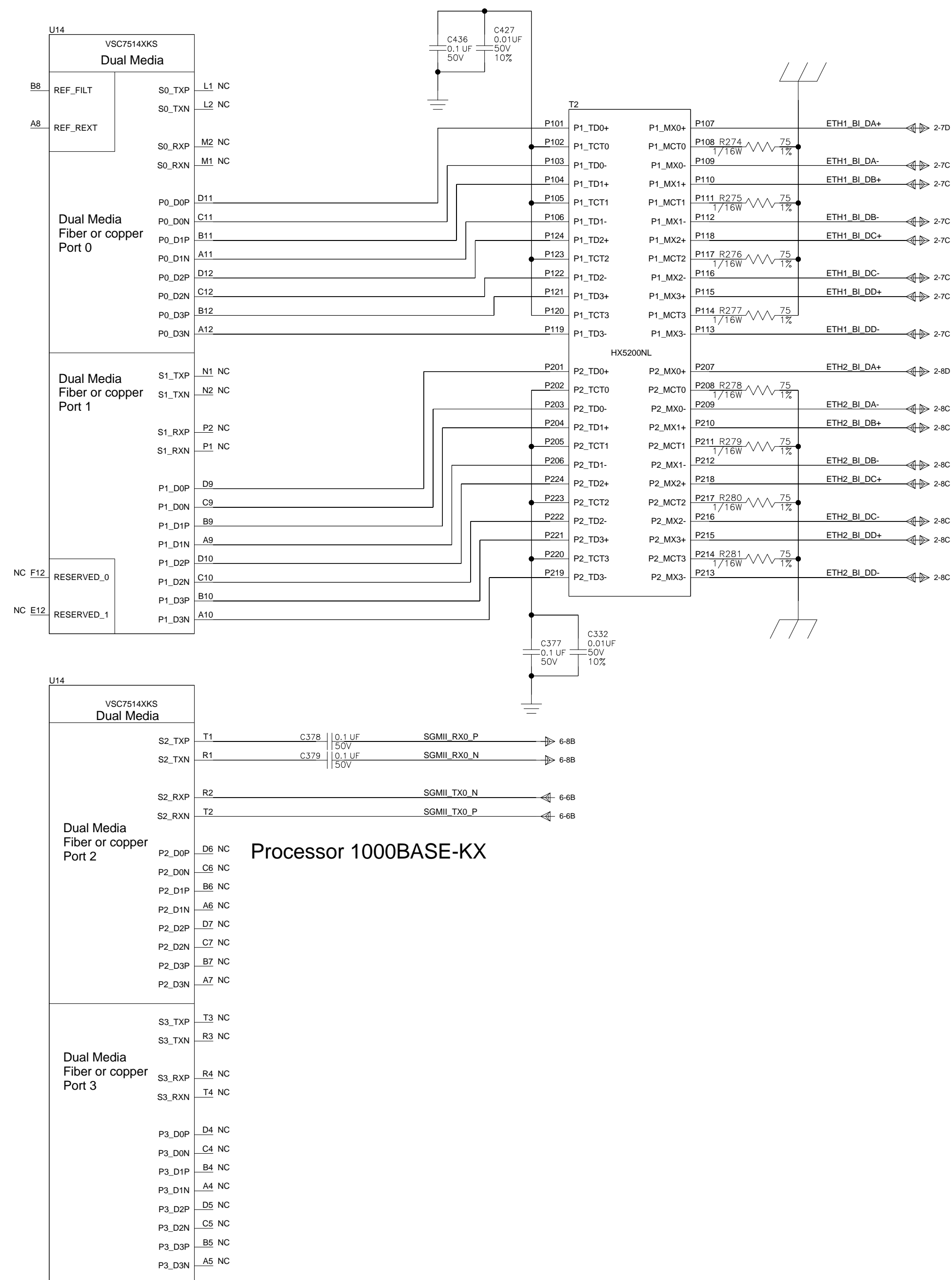
SCHEMATIC DIAGRAM
RED INTERFACE
TITLE_2

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SIZE D	CAGE CODE 0CDN3	DWG NO 112302-01003	REV 0.7
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Ethernet Switch Ports



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TITLE

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RED INTERFACE
TITLE_2

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SIZE D	CAGE CODE 0CDN3	DWG NO 112302-01003	REV 0.7
FILE NAME 112302-01003			SHEET 11 OF 11